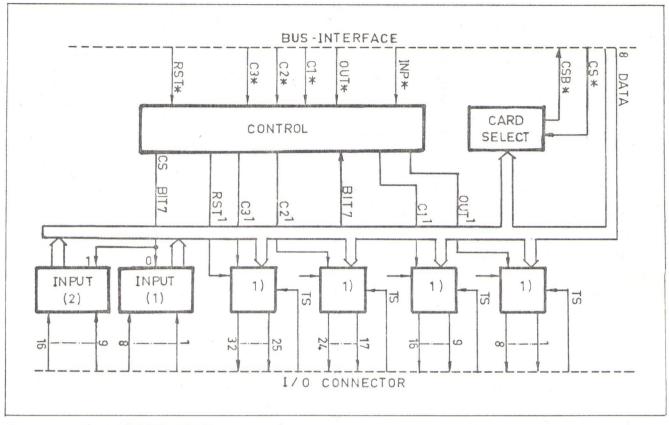Comment: 1) LATCHED DRIVE

TS FÖR "TRISTATE"-CONTROL

DESCRIPTION

4006 is an I/O interface module for TTL-compatible signalling comprising 16 inputs and 32 tristate buffered outputs.

The interface is controlled on byte-level, divided into 8-bit groups of inputs and outputs. Input group is chosen at channel selection. Separate commands are used for the four output groups. These latched outputs can be switched ON or OFF by a user provided control signals, one for each group. If the tristate control is not used then the appropriate tristate control signals shall be drawn to ground. The bussignal RST sets all output lines low.

A code strip is mounted on the I/O-connector to protect the module from being plugged with the wrong end into the backplane. The code strip conforms to DIN 41612. It permits the user to code the I/O-cables to be keyed for the right cards.

SPECIFICATIONS

POWER SUPPLY	+ 5V±5%, 570 mA+load
I/O- outputs	TTL-compatible Driving capacity = 10 TTL loads.
I/O- inputs	TTL-compatible Load = 1 TTL
BUS-connection	I/O-side of the 4680-bus. The bus-signalling includes the signal CSB ✳ (for bus-expansion)
CONNECTOR	64 pin standard Europe-connector (DIN 41612) on both the bus- and I/O- side of the card.
SIZE	Standard Europe-card 100 x 160 mm.
BUS PIN NUMBERING	See System Manual.

I/O PIN NUMBERING

OUTPUTS

Signal	1	2	3	4	5	6	7	8	TS(1)	9	10	11	12	13	14	15	16	TS(2)
Pin	1A	1B	2A	2B	3A	3B	4A	4B	17A	5A	5B	6A	6B	7A	7B	8A	8B	17B

Signal	17	18	19	20	21	22	23	24	TS(3)	25	26	27	28	29	30	31	32	TS(4)
Pin	9A	9B	10A	10B	11A	11B	12A	12B	18A	13A	13B	14A	14B	15A	15B	16A	16B	18B

0—volt pin 19A, 19B

INPUTS

0—volt pin   20A, 20B, 25A, 25B   32A, 32B, 27A, 27B, 26A, 26B

Signal	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
Pin	21A	21B	22A	22B	23A	23B	24A	24B	28A	28B	29A	29B	30A	30B	31A	31B

I/O COMMANDS

INP DATA	Reads 8 bits of data. Data bit 7 at channel selection selects the group: D7 = 0 = group 1, signals 1 - 8. D7 = 1 = group 2, signals 9- 16.

Note. Data bit 0 of INP DATA corresponds to signals 1 and 9 respectively.

OUT DATA	Data to group 1, signals 1 - 8.
OUT C1	Data to group 2, signals 9 - 16.
OUT C2	Data to group 3, signals 17 - 24.
OUT C3	Data to group 4, signals 25 - 32.

Note. Data bit 0 of the OUT-commands corresponds to signals 1, 9, 17 and 25 respectively.

CARD SELECTION

The card is identified at channel selection, done with the command CS, by a jumper plug at location 2C.
Refer to System Manual about coding.