

2003

SEPT 81 1 3

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DESCRIPTION

BESKRIVNING

- 16Kbyte dynamic memoryboard, based on memory circuit 4116.
- Delivered with full capacity 16Kbyte mounted.
- The on-board refresh logic uses the system clock and the Z80 refresh addresses on the address bus, but requires no refresh control signals on the bus.
- 2003 may be used in all Data-board 4680 systems with Z80, without DMA and with max 3MHz system clock, incl. also ABC80 and ABC800 if a memory expansion box is used.
- Address selection is done by on-board switches.

- 16Kbyte dynamiskt minneskort baserat på minneskrets 4116.
- Levereras fullt bestyckat med 16Kbyte.
- Refresh-logiken på kortet använder systemklockan och Z80:s refreshadresser på adressbussen, men kräver inga refresh kontroll signaler på bussen.
- 2003 kan användas i alla Databoard 4680 system med Z80 utan DMA och som använder max 3MHz systemklocka. Även för ABC80 och ABC800 om en minnes-expansionslåda används.
- Adressval sker med omkopplare på kortet.

TECHNICAL DATA

TEKNISKA DATA

Power Supply
Spänningsmatning

+5V +/-5%, 500mA
+12V +/-5%, 50mA
-12V +/-5%, 50mA

Bus connection
Anslutning till bussen

Memory-side.

Connector
Kontaktidon

B 64 pin Standard Europe connector
(DIN 41612).

Size
Storlek

Standard Europe card, 100 x 160 mm.

Memory circuits
Minneskretsar.

4116-3 access time 200ns.

INSTALLATION

1. Select base-address.

The base address of the module is selected with the jumpers S1 and S2 as tabled below. To ABC80 the base address 32k should be selected.

S2	S1	BASE ADDRESS
ON	ON	0K
ON	OFF	16K
OFF	ON	32K
OFF	OFF	48K

2. Connect CPU-clock.

The CPU-clock must be connected to the 2003. In the DataDisc 82-88 the CPU-clock is connected through the backplane to the memory side. The clocksignal must be soldered or wired in all the other systems. Connect the clock signal at pins 5B between SBC-CPU and 2003. In double board systems, the 2003 uses the DMA position. Make sure that the clock is included in the connection cable to ABC80. Jumper S3 selects the clock phase. Connect S3:2-3 for ABC80 and SBC but S3:2-1 (inverted phase) for double board CPU.

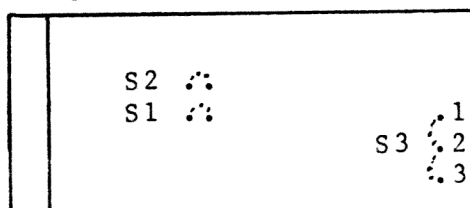
3. Insertion.

SWITCH THE POWER OFF
Turn the component side to the right. Put the 2003 in the memory-side.

INSTALLATION

1. Välj basadress.

Basadressen väljs med bygling S1 och S2, se nedanstående tabell. Till ABC80 ska basadressen 32k väljas.



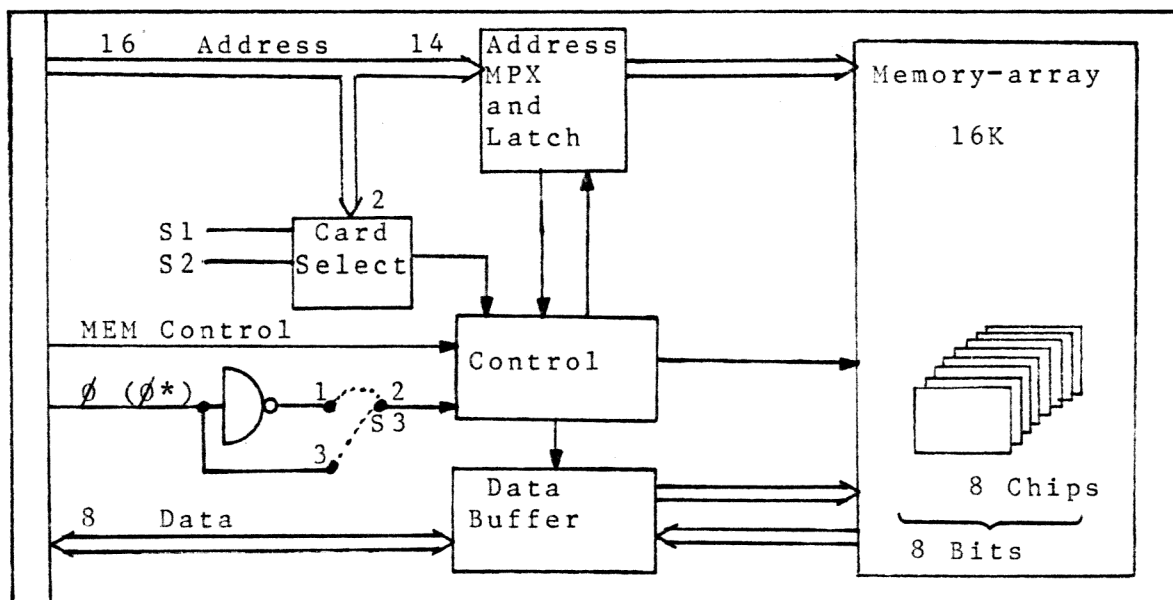
2. Anslut CPU-klockan.

CPU-klockan måste anslutas till 2003. I DataDisc 82-88 är CPU-klockan framdragen på bakplanet till minnesidan. I övriga systems bakplan måste klocksignalen dras fram. Klocksignalen ansluts mellan 5B på enkorts datorplatsen och 2003. I tvåkorts system använder 2003 DMA platsen. Kontrollera att klocksignal finns i anslutningskabeln till ABC80. Bygling S3 väljer klockfas. Bygla S3:2-3 för ABC80 och enkorts datorn men S3:2-1 (inverterad klocka) för tvåkorts datorn.

3. Insättning.

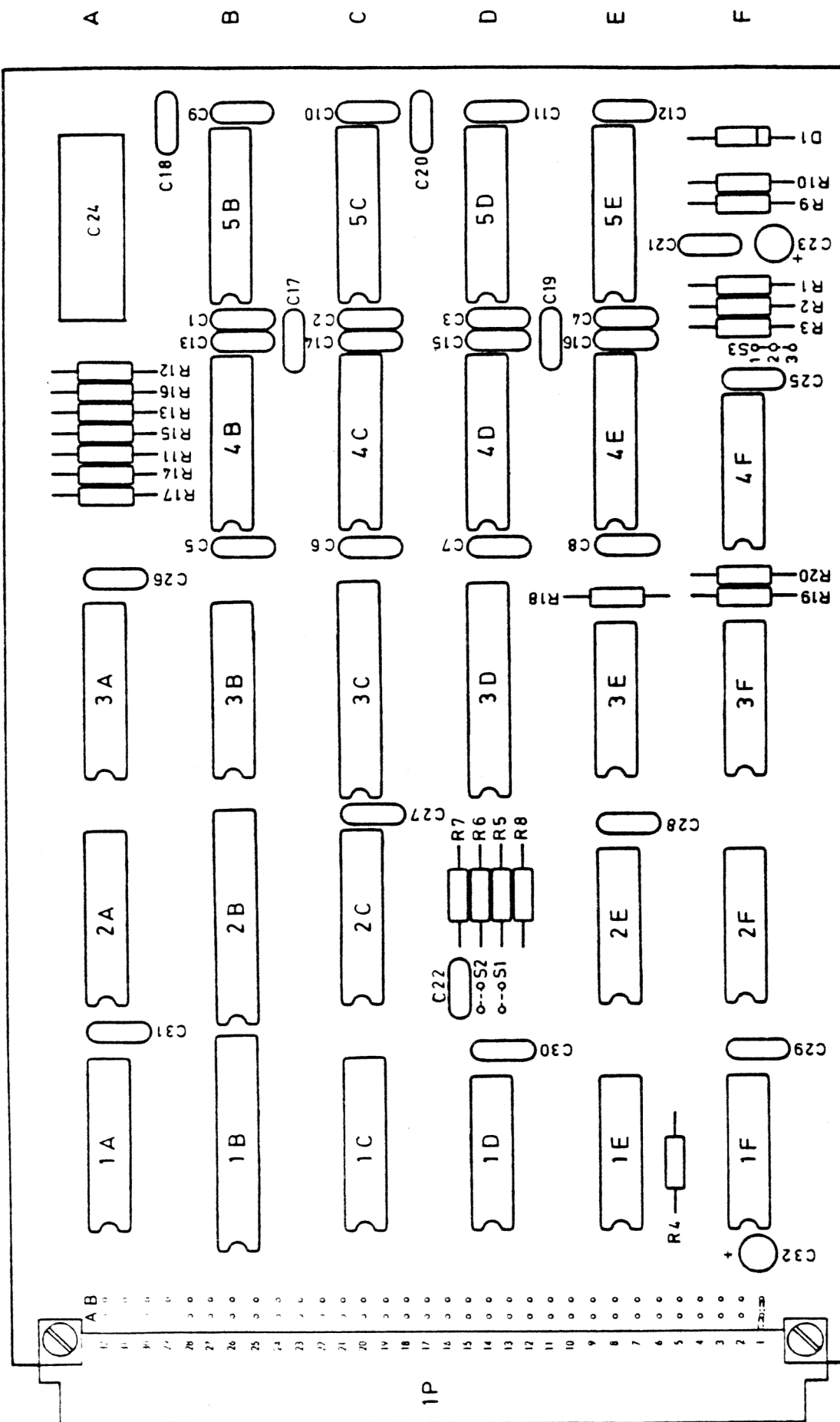
SLÅ FRÅN SPÄNNINGEN
Vänd komponentsidan åt höger. Placera kortet på minnessidan.

This datasheet information is subject to change without notice.



COMPONENT DIAGRAM

KOMPONENTDIAGRAM



Not: S3 Jumper 1-2 for Data Board 1043 CPU
S3 Jumper 2-3 for ABC 80 and Data Board 1062, 1057 CPU

S2	S1	Base Address
0	0	000 HEX NOT
0	1	100:000 " " 4000 " "
1	0	200:000 " " 8000 " "
1	1	300:000 " " C000 " "

0 = Jumper
1 = No jumper