

FSI**FIELD
SERVICE
INFORMATION**

ANKOM

1005-02-21

Product no./Artikelnumm

4201-00

Product DS90 Floppy/Databoard interface board
Artikel

☐ Immediately Omedelbart	☐ Optional Valfritt
☒ Upon failure Vid fel	☐ Information Information

Date	850208
Datum	
Signature	MK
Sign.	

Bulletin no.	45
Bulletin nr	
Page no.	1/1
Sida nr	

Affects/Påverkar

Problem: Floppy accesses is not performed successfully.

Reason: At power up the Z80 DMA generates a bus request signal to the Z80 CPU controlling the floppy controller chip. This will give that the CPU will never start.

Action: Replace Z80A DMA circuit in position 5G with a Z80A DMA from Zilog.

Note: Proper circuit is now used in production.

**DATA
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FIELD
SERVICE
INFORMATION

ANKOM

1985-02-21

Besv.....

Product no./Artikelnummer

2100-10

Product DS90 0.5/2 Mb memory board
Artikel

☐ Immediately Omedelbart	☐ Optional Valfritt	Date 850208 Datum	Bulletin no. 47 Bulletin nr
☒ Upon failure Vid fel	☐ Information Information	Signature MK Sign.	Page no. 1/1 Sida nr

Affects/Påverkar

Problem: System crashes caused by damaged memory contents.

Reason: Improper address drivers may generate noise on memory matrix address inputs.
High noise level on the board.

Action: Replace IC 74LS393 in position 13E with 74393.
Replace IC 74F153 in positions 10C, 11C, 12C, 13C, 10E 10F, 11F, 12F AND 13F with 74F353.
Connect pin 13 on IC 74F74 position 11A to pin 14 (+5V) on the same IC.

Note: These changes is now done in production.
New product number will be 2100-20

DATA
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**FIELD
SERVICE
INFORMATION**

ANKOM

1985-02-21

Product no./Artikelnummer

1102-10

1102-20

Product DS90 M68010 CPU board
Artikel

Besv.....

☐ Immediately
Omedelbart
☒ Upon failure
Vid fel

☐ Optional
Valfritt
☐ Information
Information

Date 850208
Datum

Bulletin no. 46
Bulletin nr

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Affects/Påverkar

CPU boards with floating point option installed

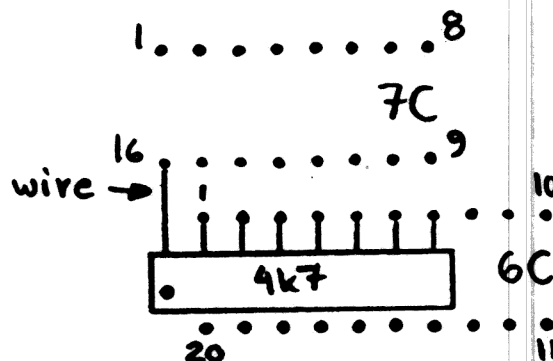
Problem: Poor noise immunity when floating point option is installed.

Action: Apply seven (7) 4.7K pullup resistors on the solder side of the board as follows:

Place a 7*4.7K SIL resistor network between the two rows of pins in position 6C (PAL16L8A PAT8022) and connect pins 2-8 of the SIL net to pins 1-7 on the IC.

Pin 1 on the SIL net should be connected to pin 16 (+5V) on the IC in position 7C.

Please refer to drawing below as seen from the solder side.



Note: This change is now done in production.