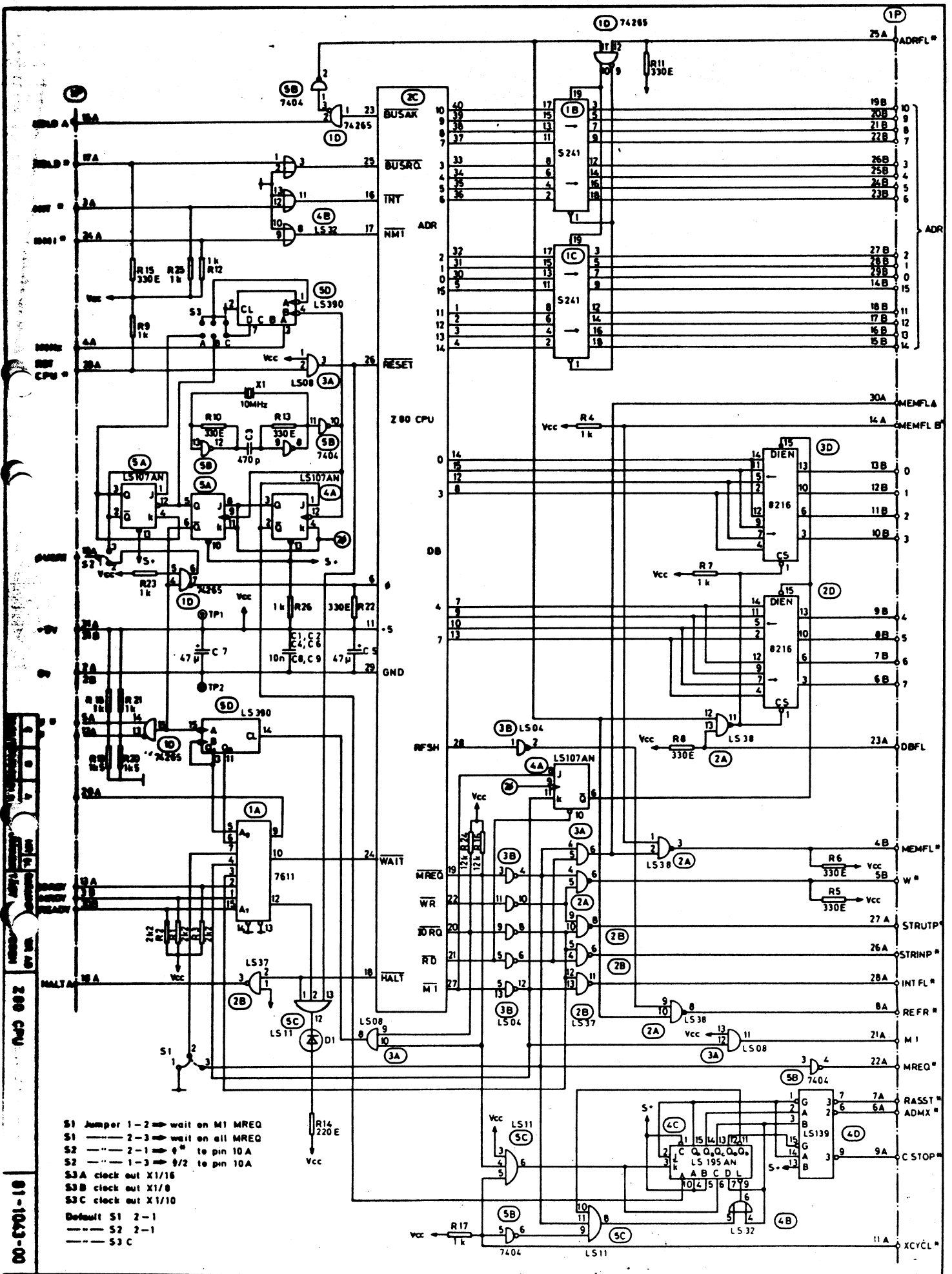
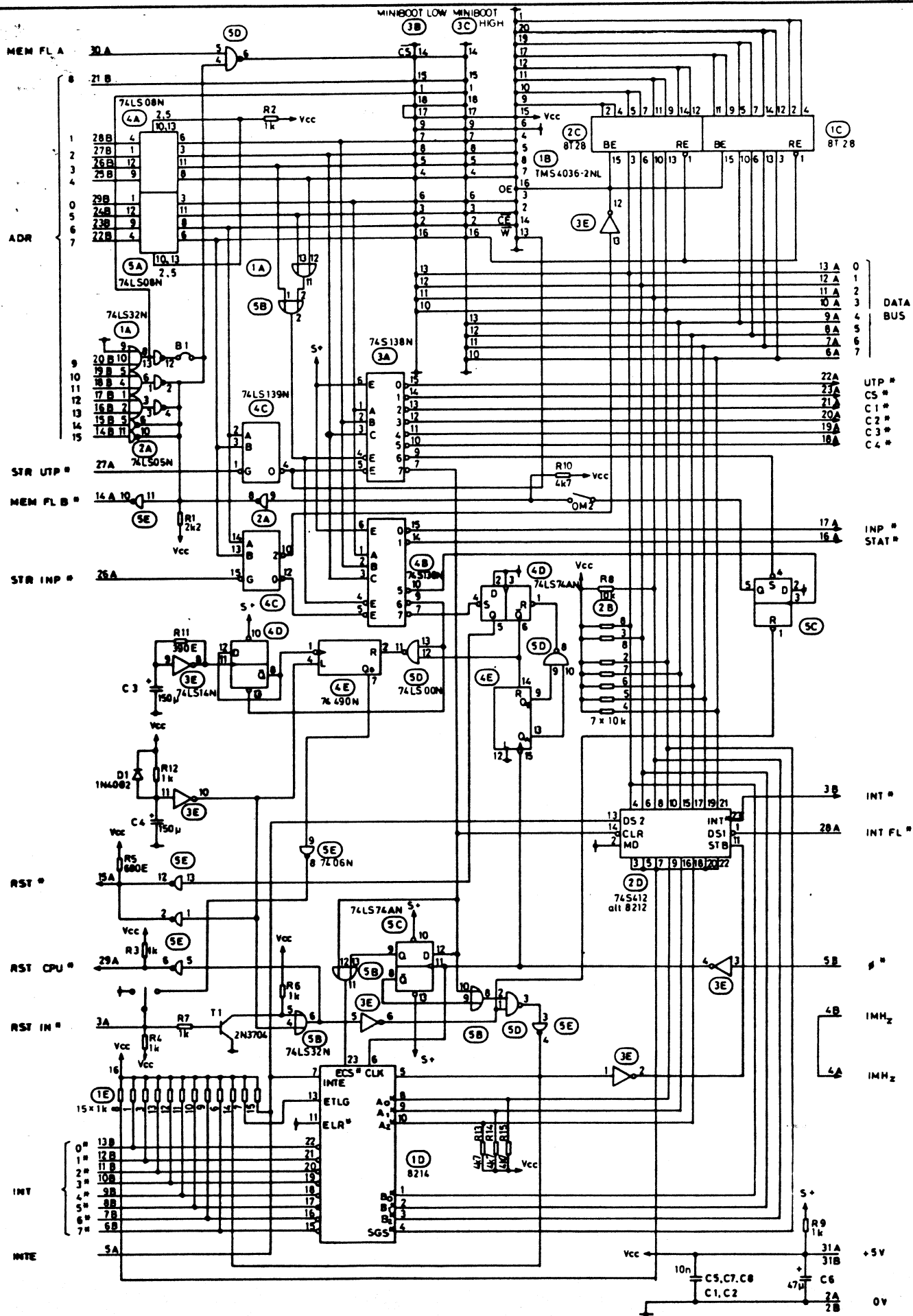
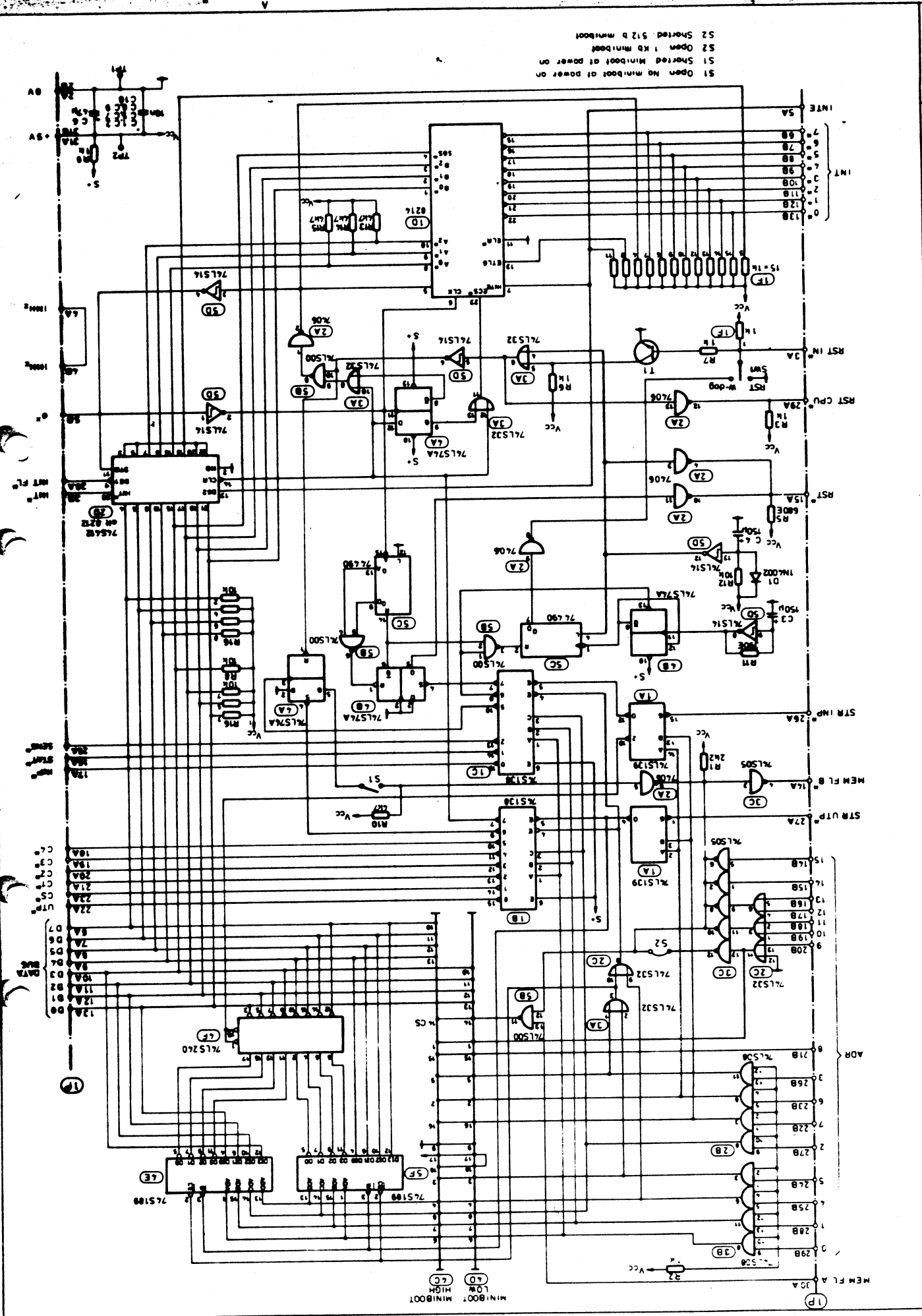






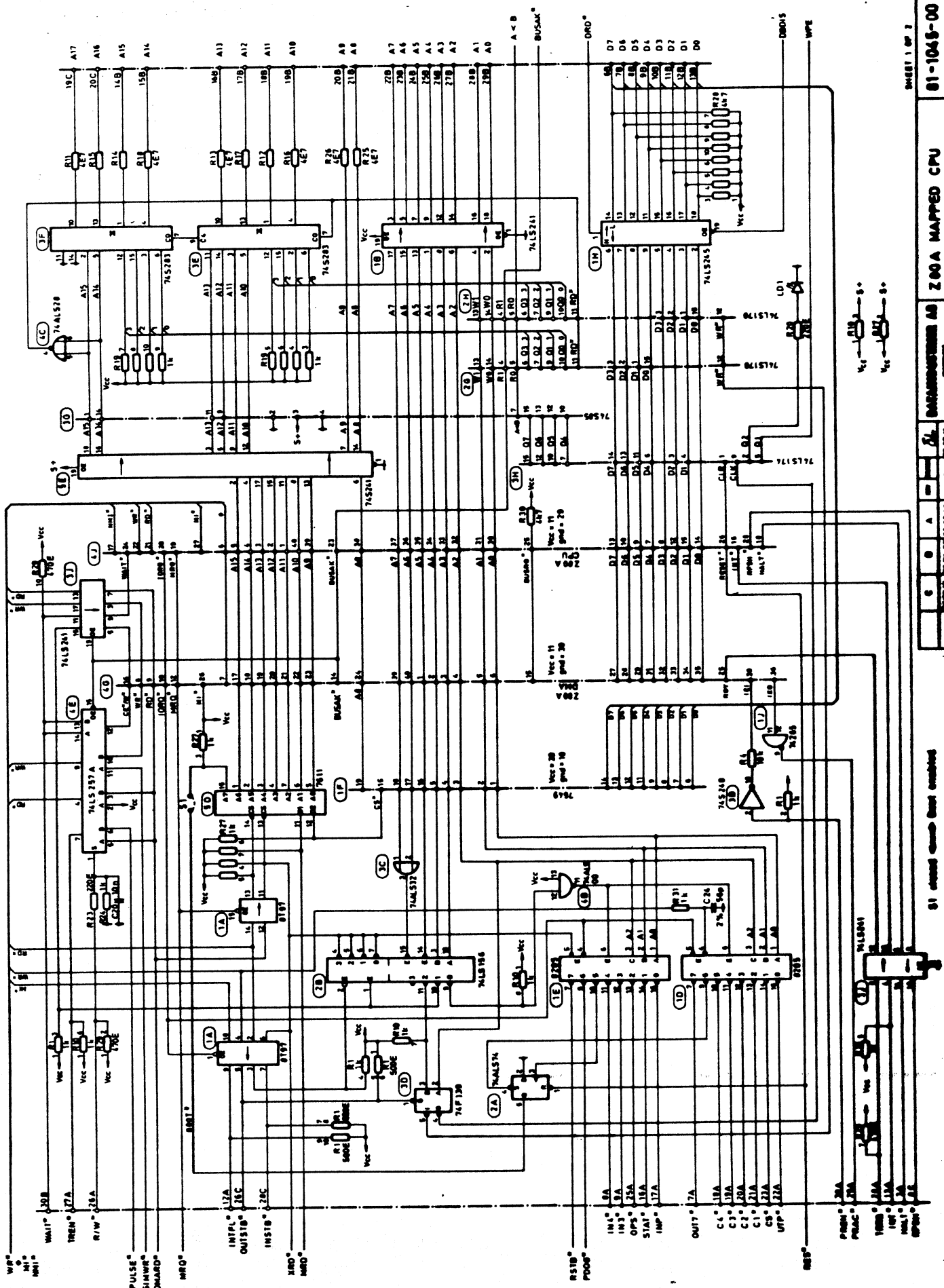






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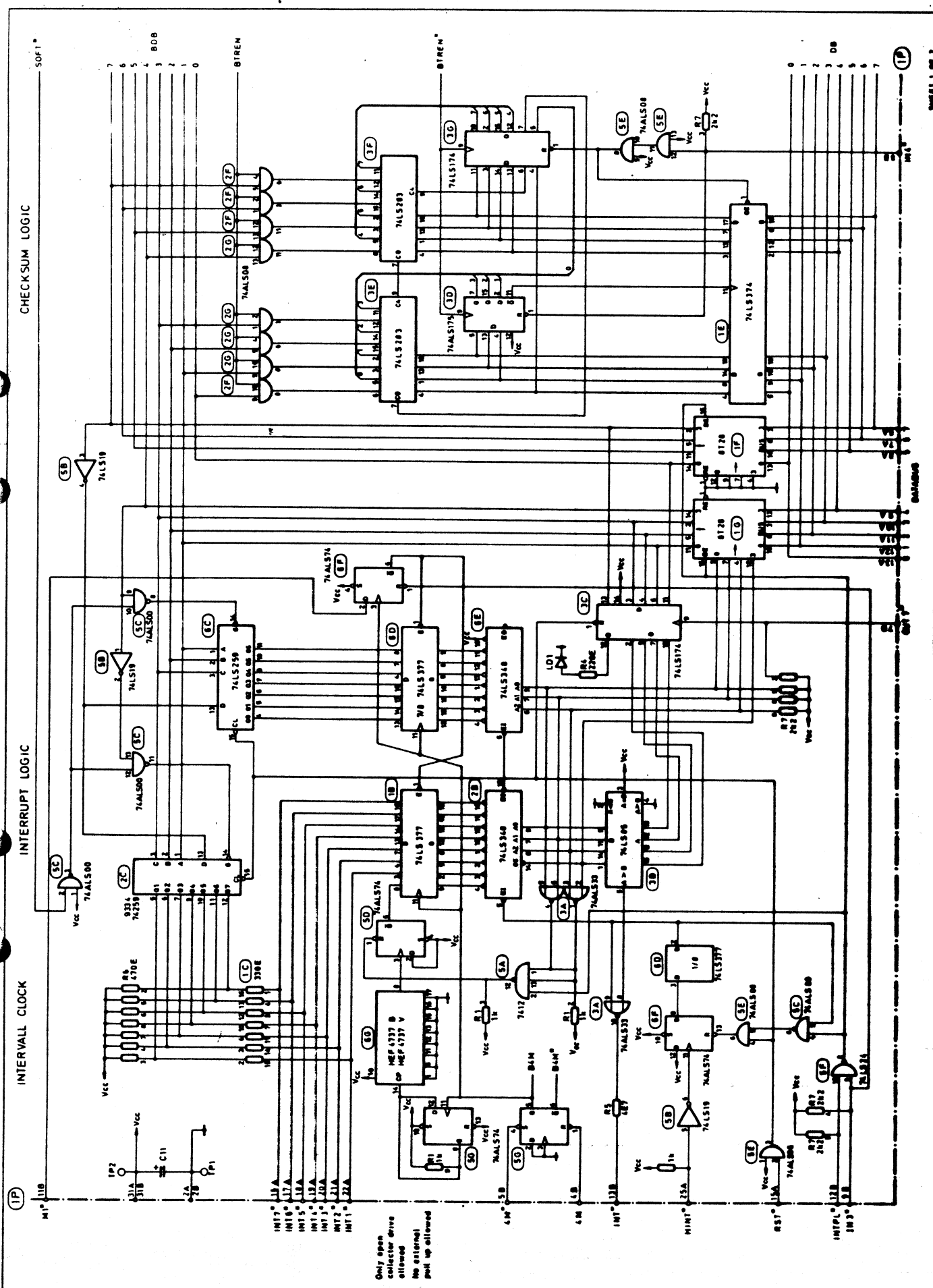




3.2 closed ~~and~~ Delay one state on Patch

200A MAPPED CPU

01-1045-00

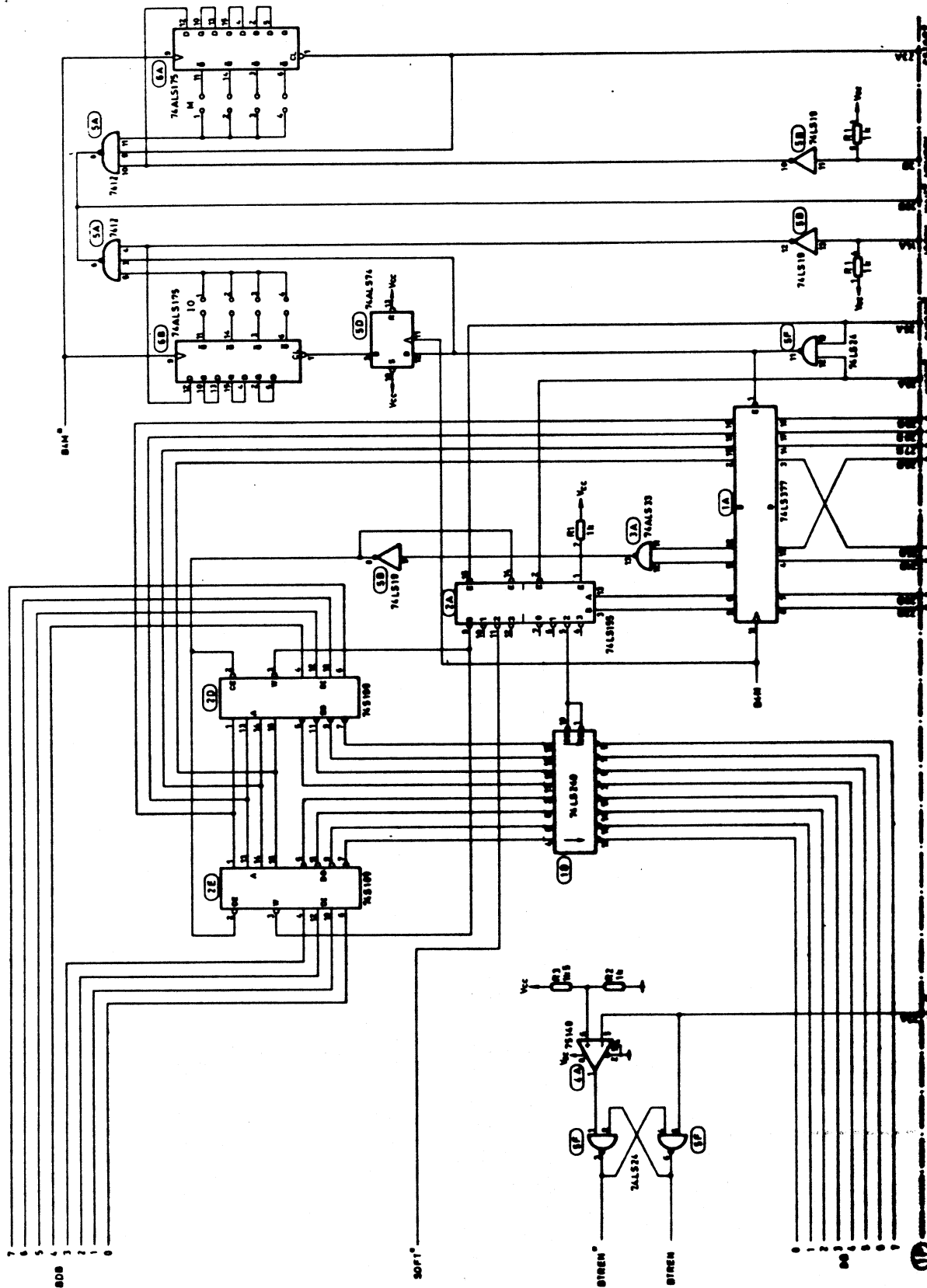


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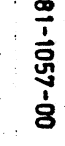
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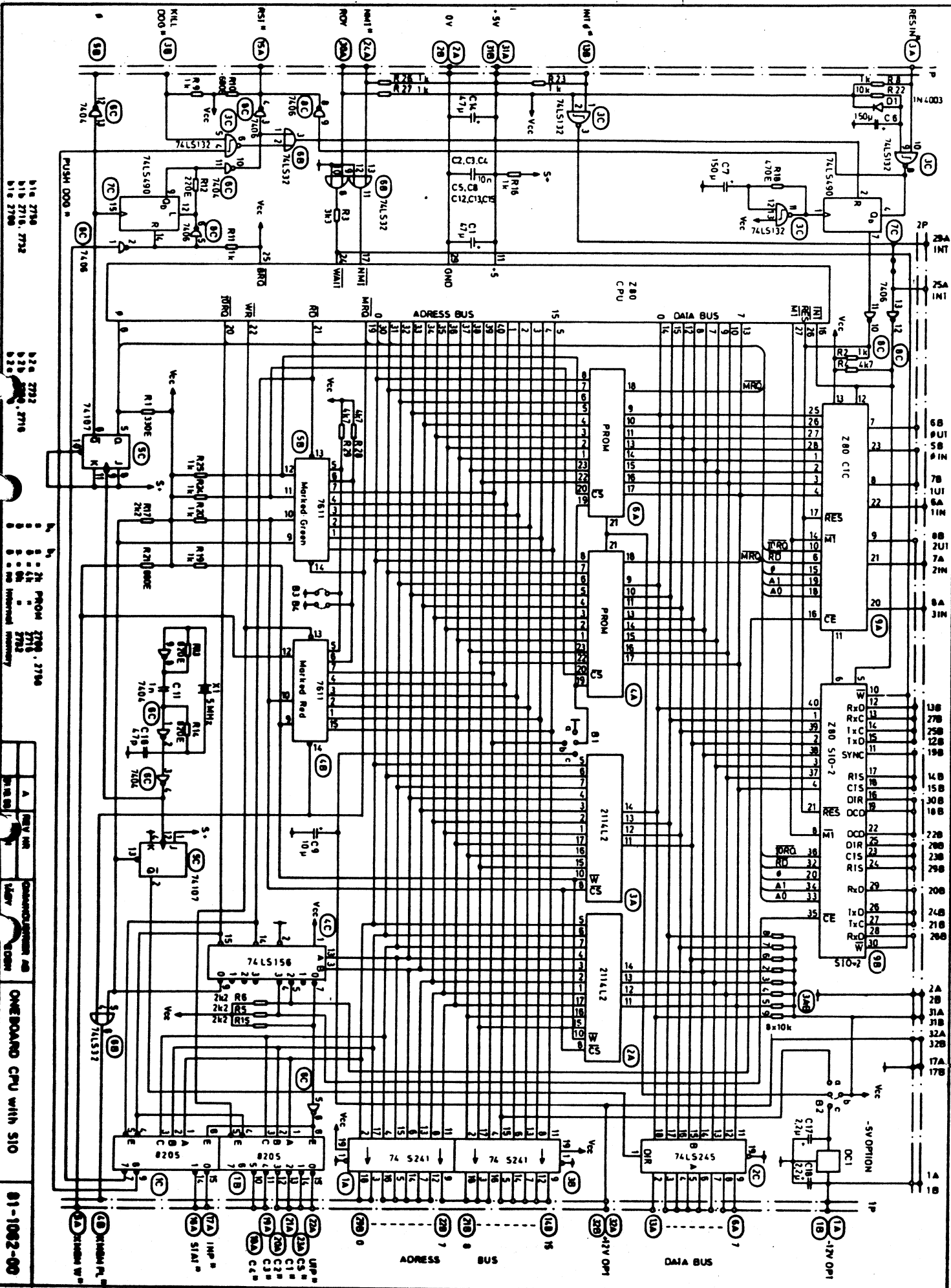
I/O - RAM

WAIT TIMER

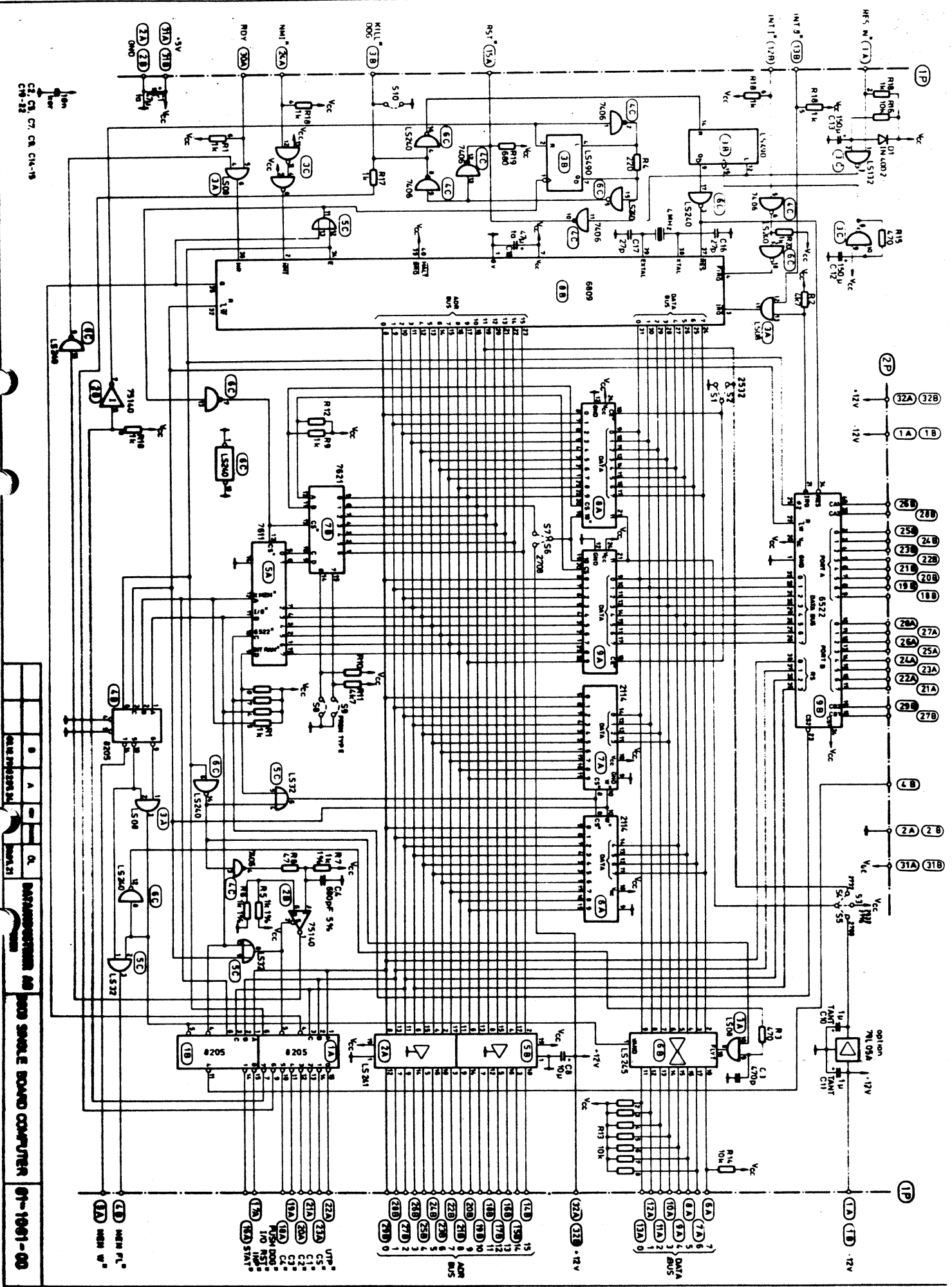


Pin number of wait status when memory low
 1: wait status
 2: wait status
 3: wait status
 4: wait status
 5: wait status
 6: wait status
 7: wait status
 8: wait status
 9: wait status
 10: wait status
 11: wait status
 12: wait status
 13: wait status
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 37: wait status
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 87: wait status
 88: wait status
 89: wait status
 90: wait status
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 95: wait status
 96: wait status
 97: wait status
 98: wait status
 99: wait status
 100: wait status

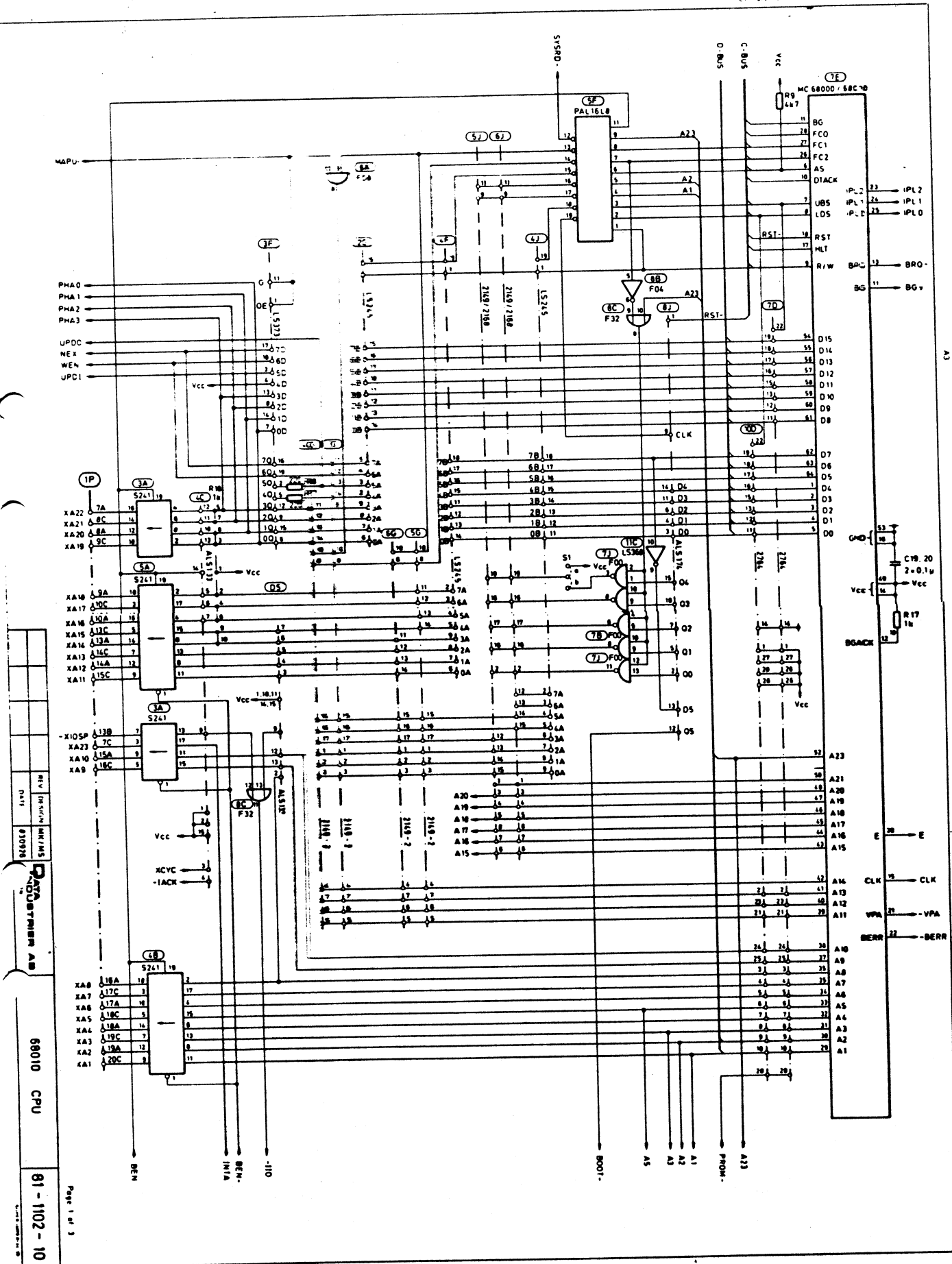


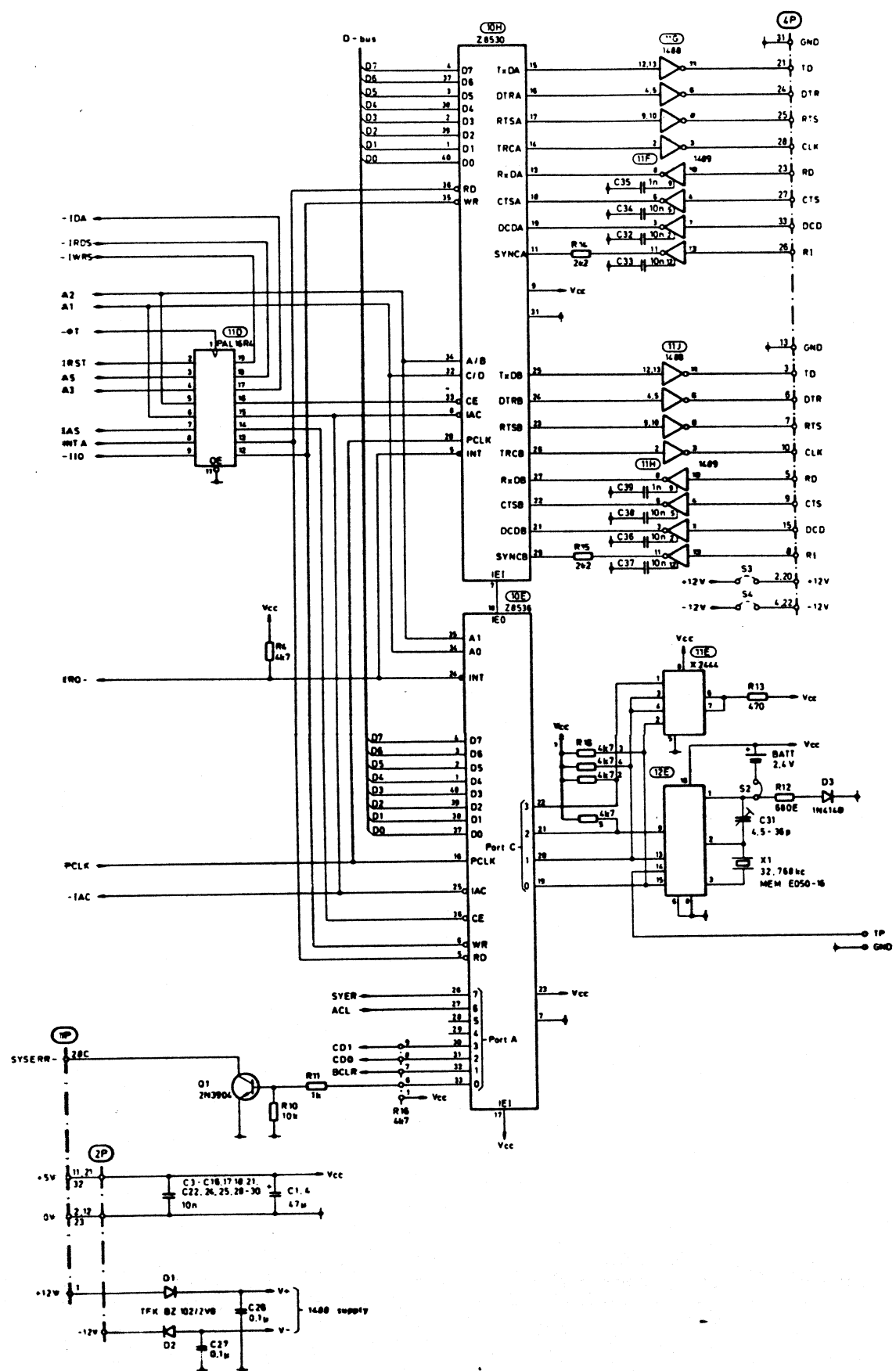


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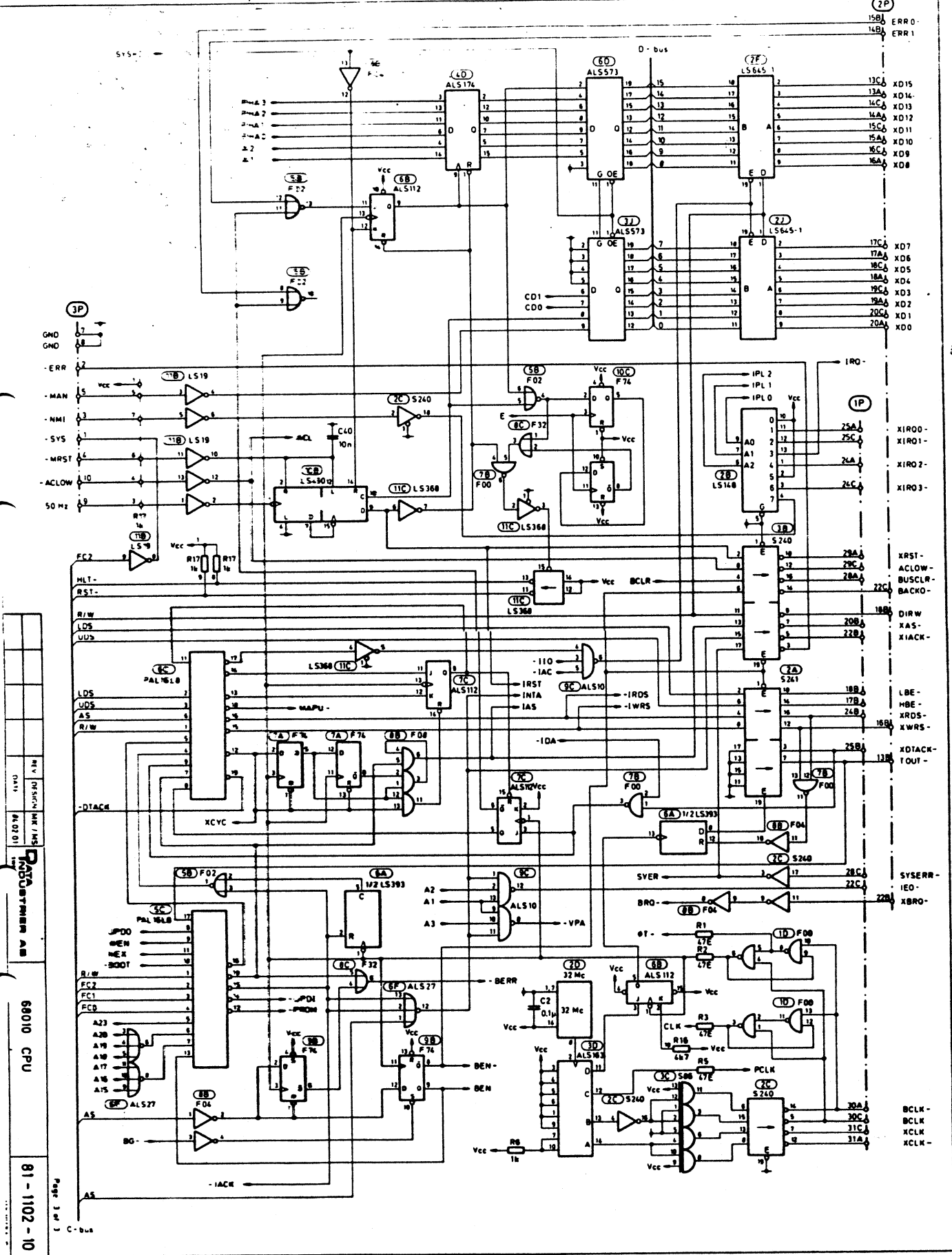


6809 MICROPROCESSOR BOARD Schematic 6809-1001-00





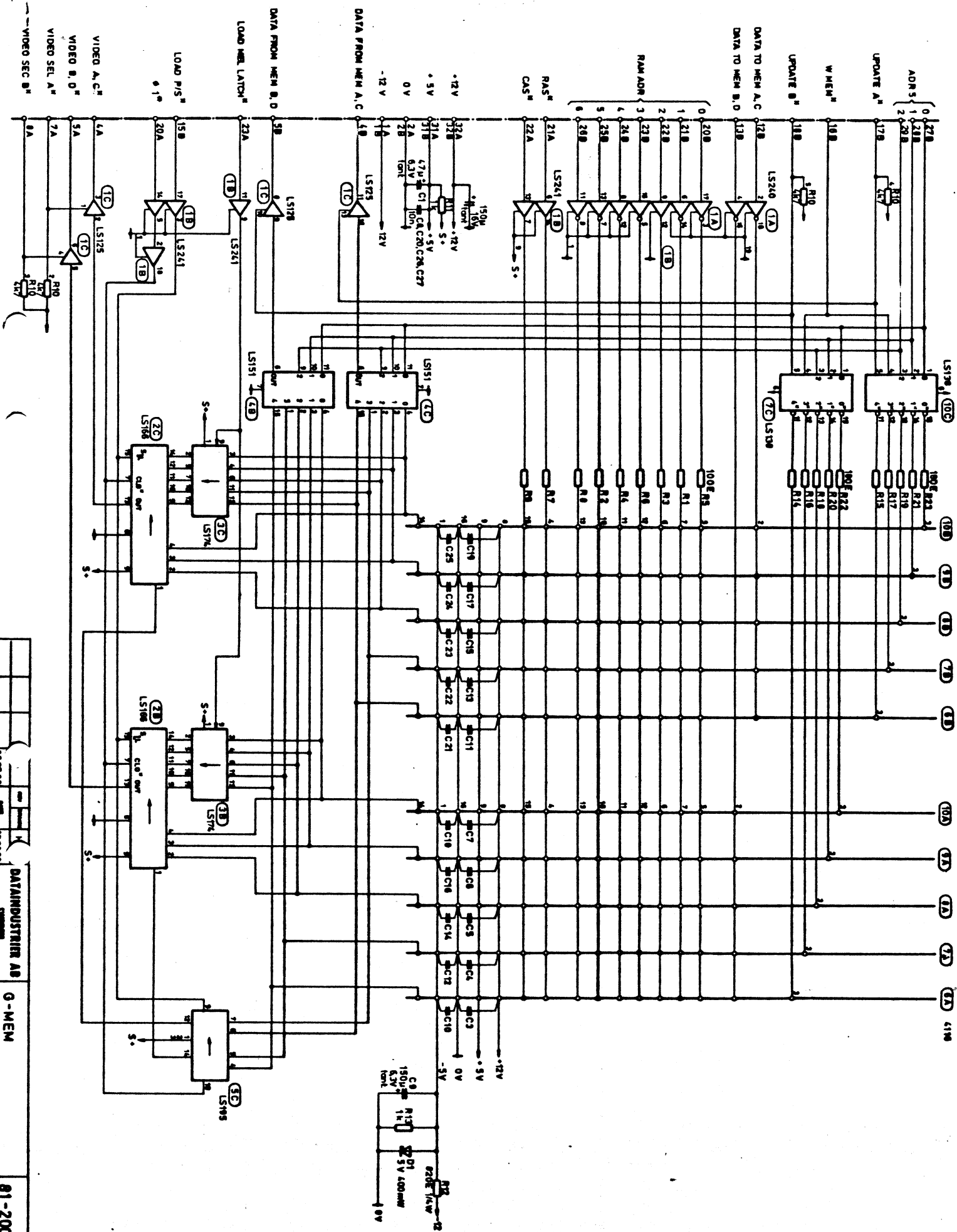
REV	DESIGN	DATE	BY	CHK	APP
01	01	01	01	01	01
DATA					
INDUSTRIAL AB					
68010 CPU					
81-1102-10					

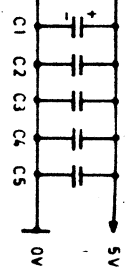


68010 CPU

81-1102-10

Page 3 of 3



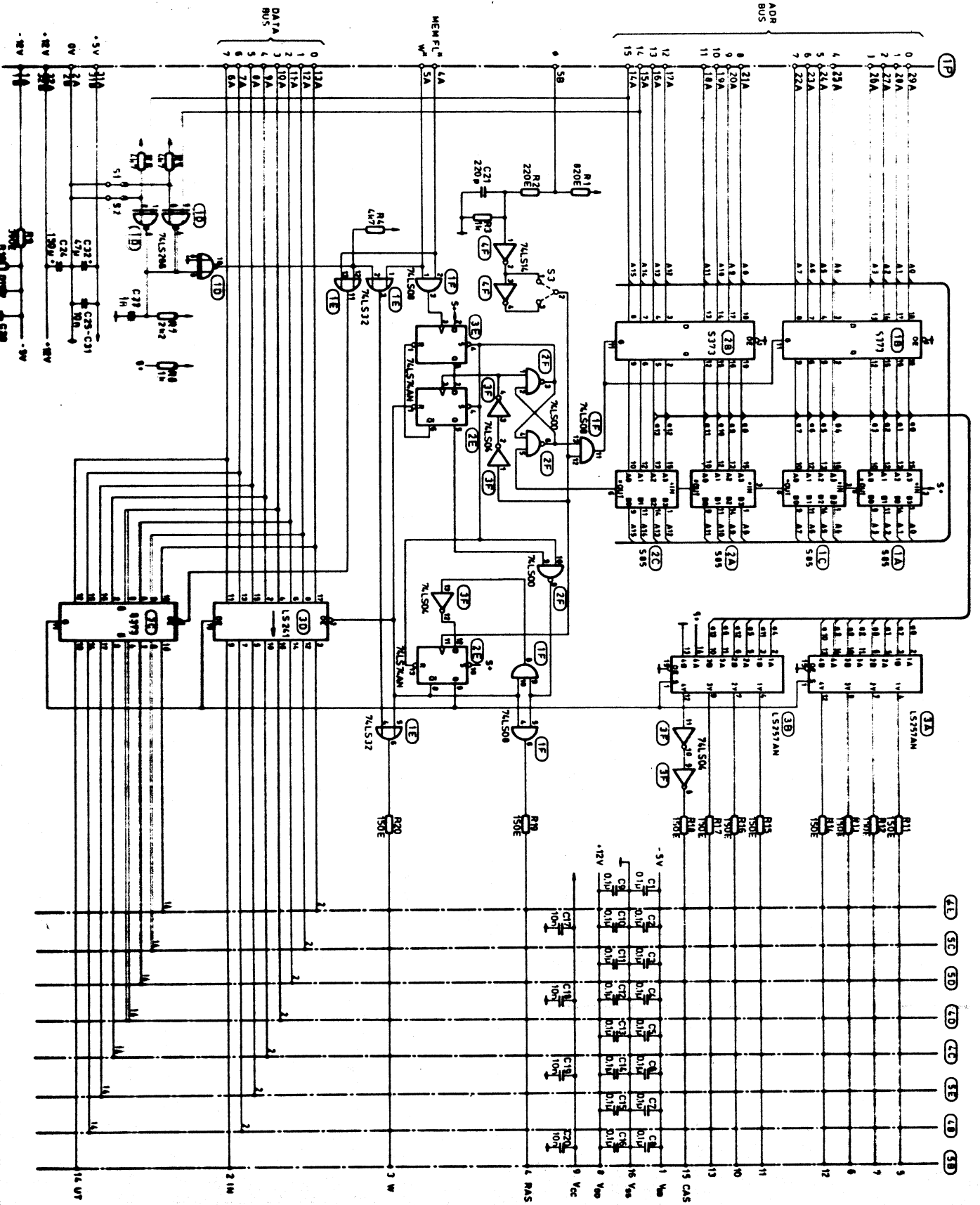


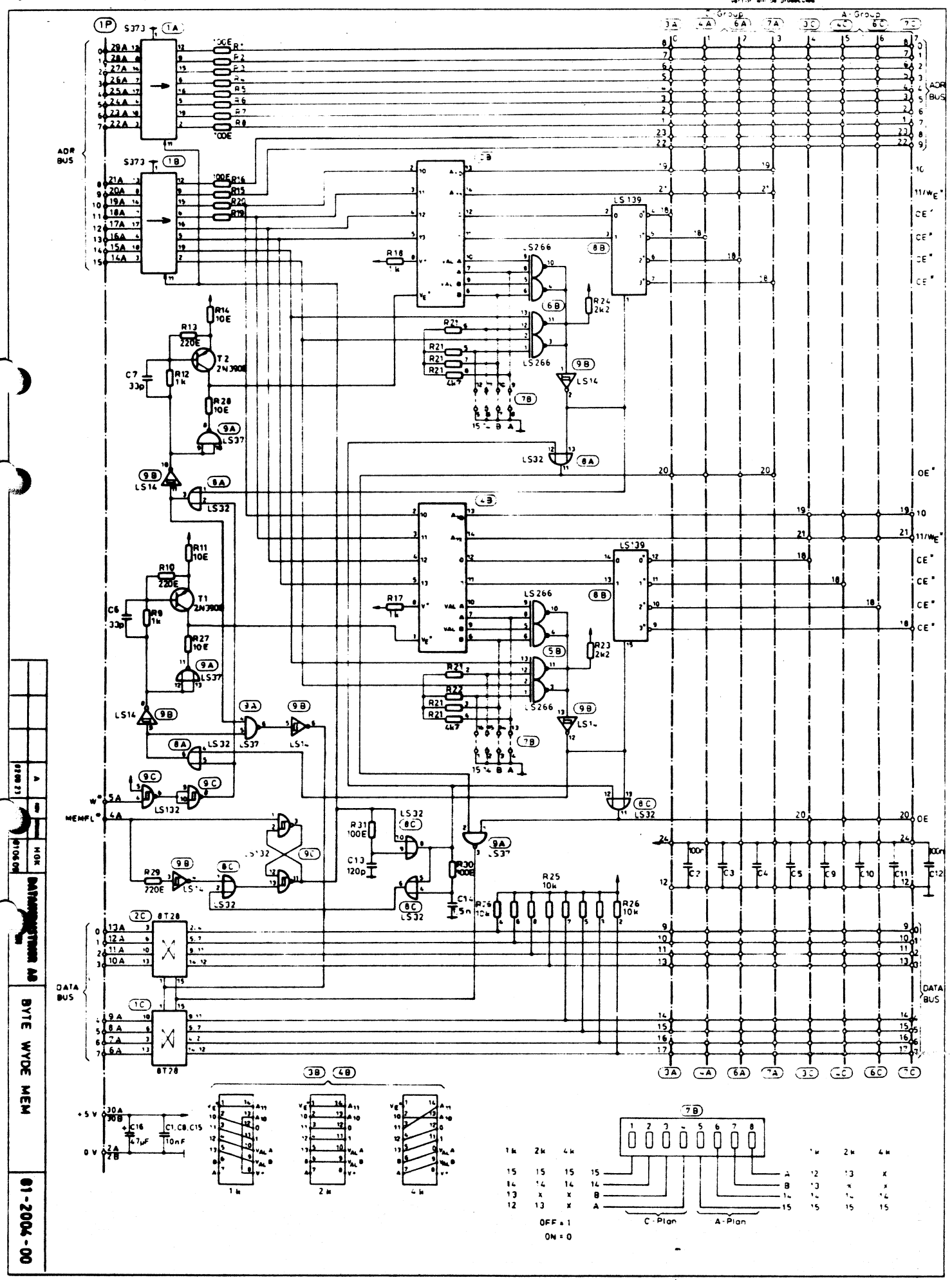
81-2002-90

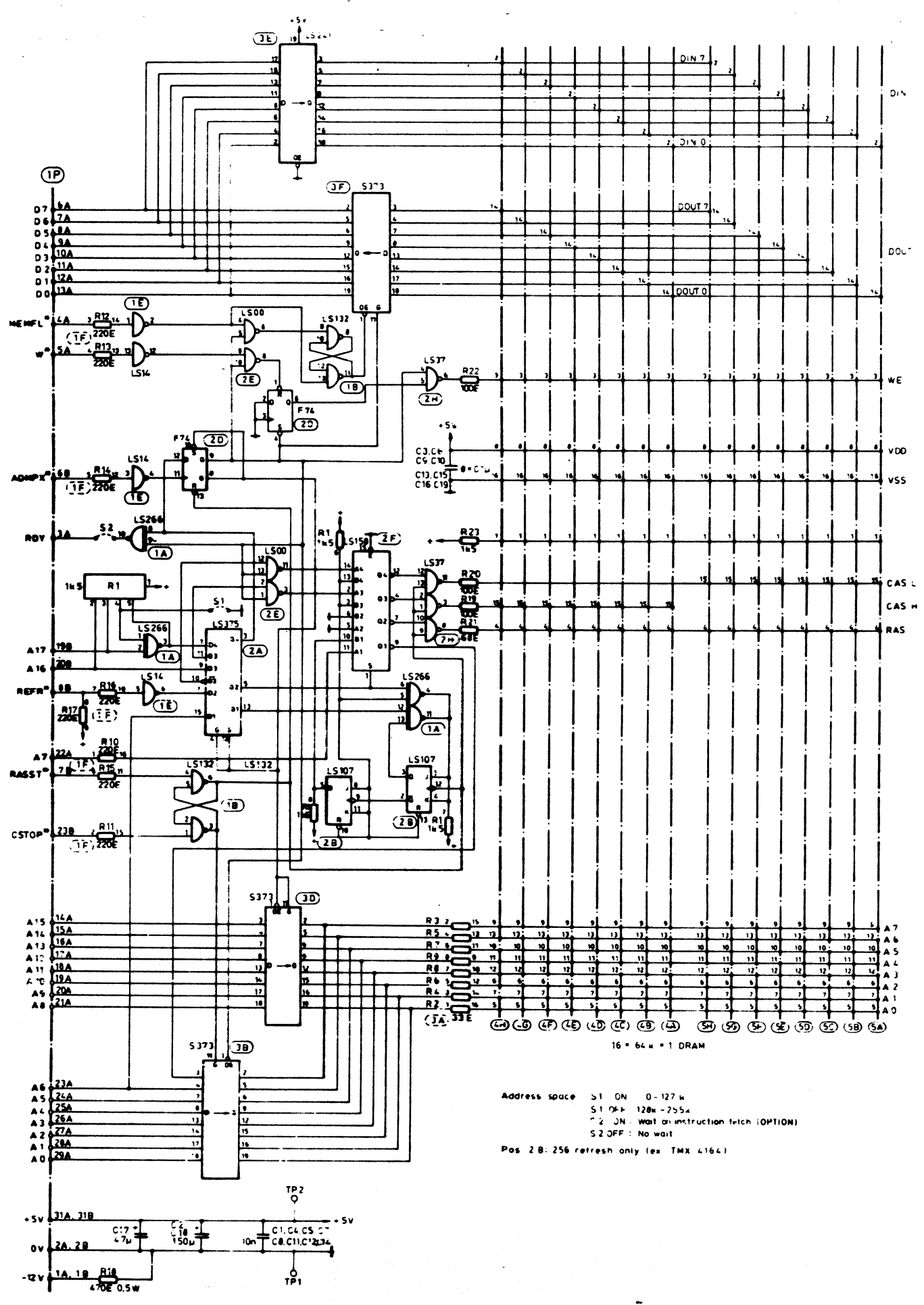
Not: S3 Jumper 1-2 for Data Board 1043 CPU
S3 Jumper 2-3 for ABC 88 and Data Board 1052, 1057 CPU

S2	S1	Base Address
0	0	0000
0	1	0000
1	0	0000
1	1	0000

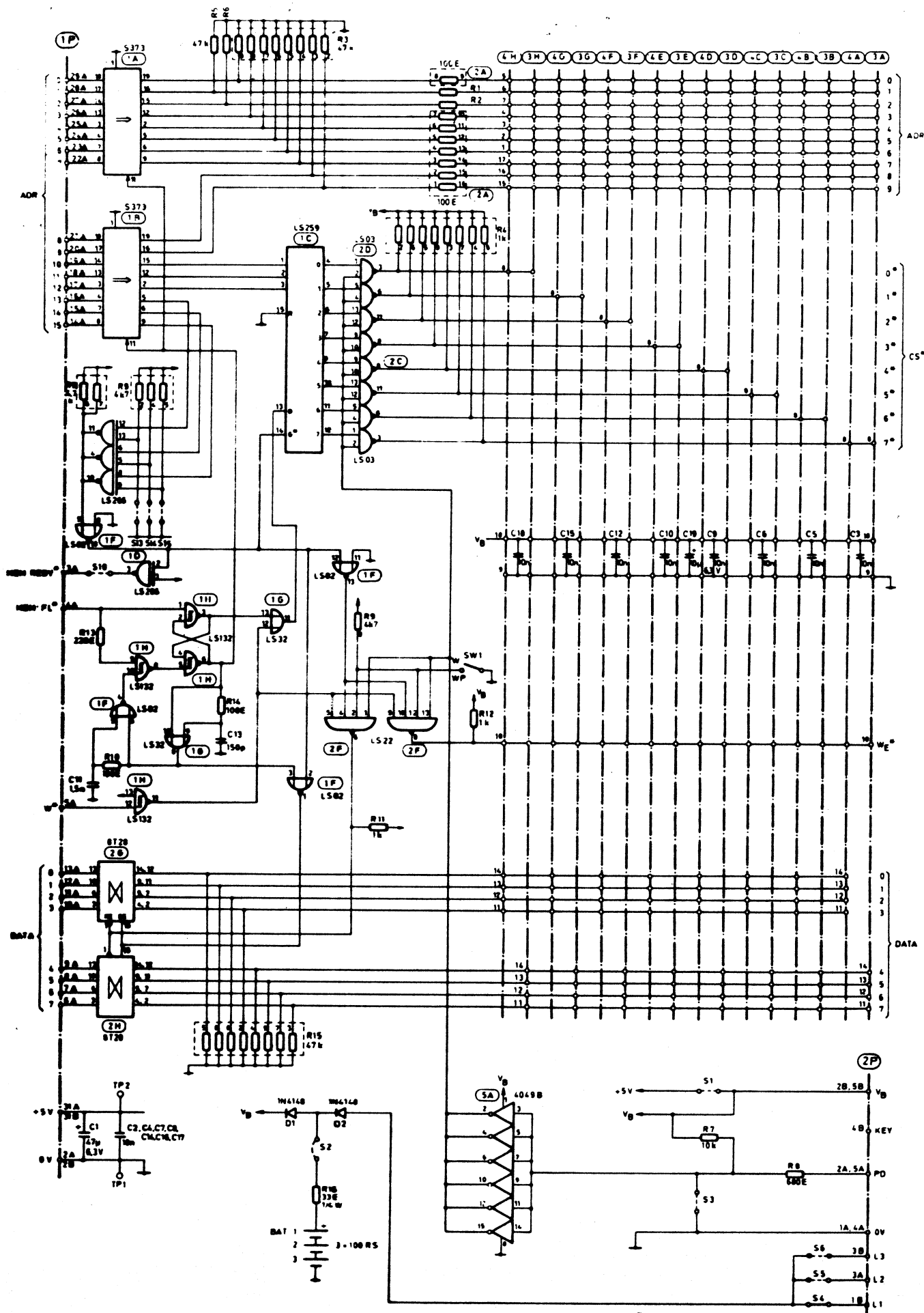
0 - Jumper
1 - No Jumper





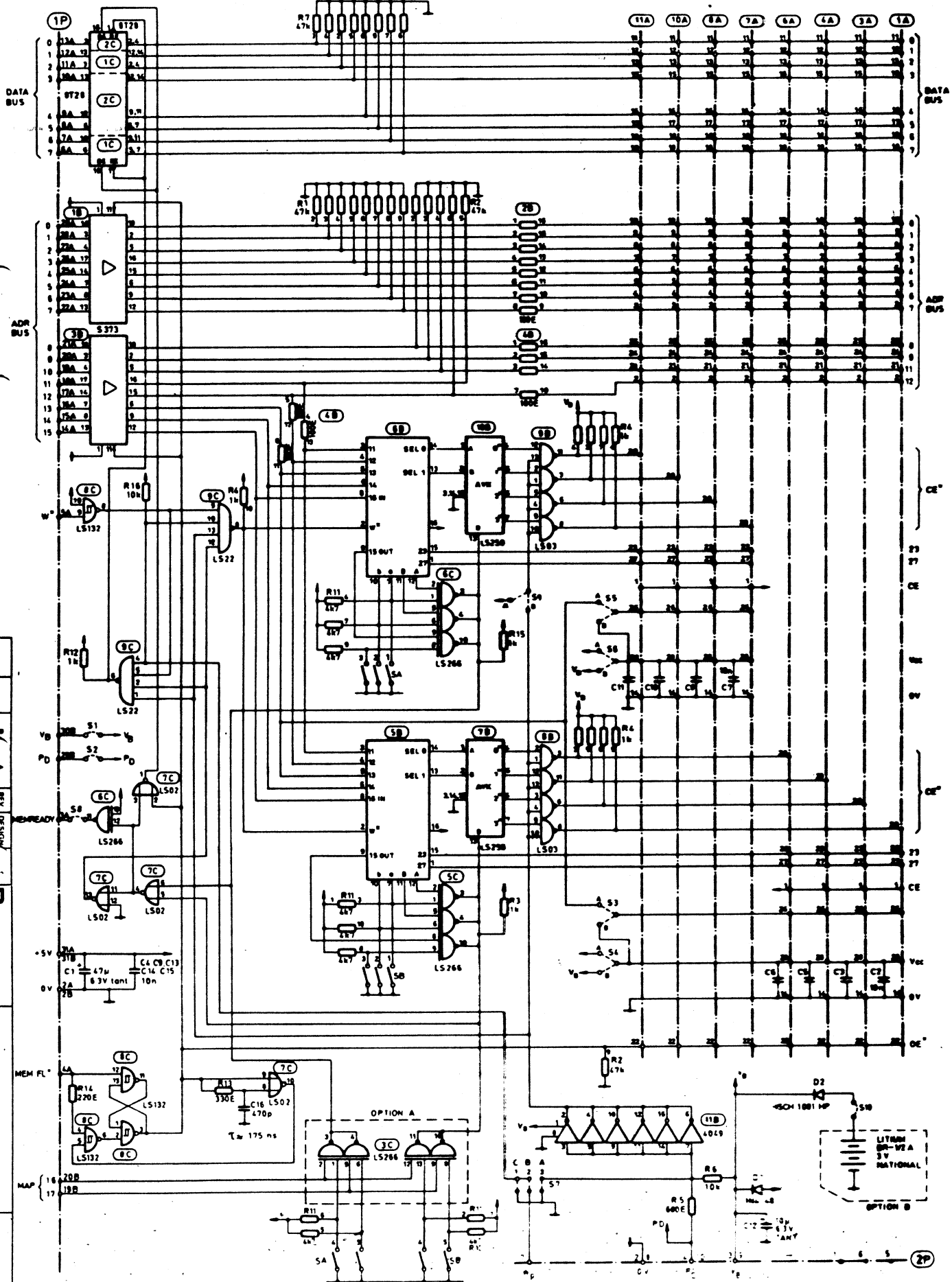




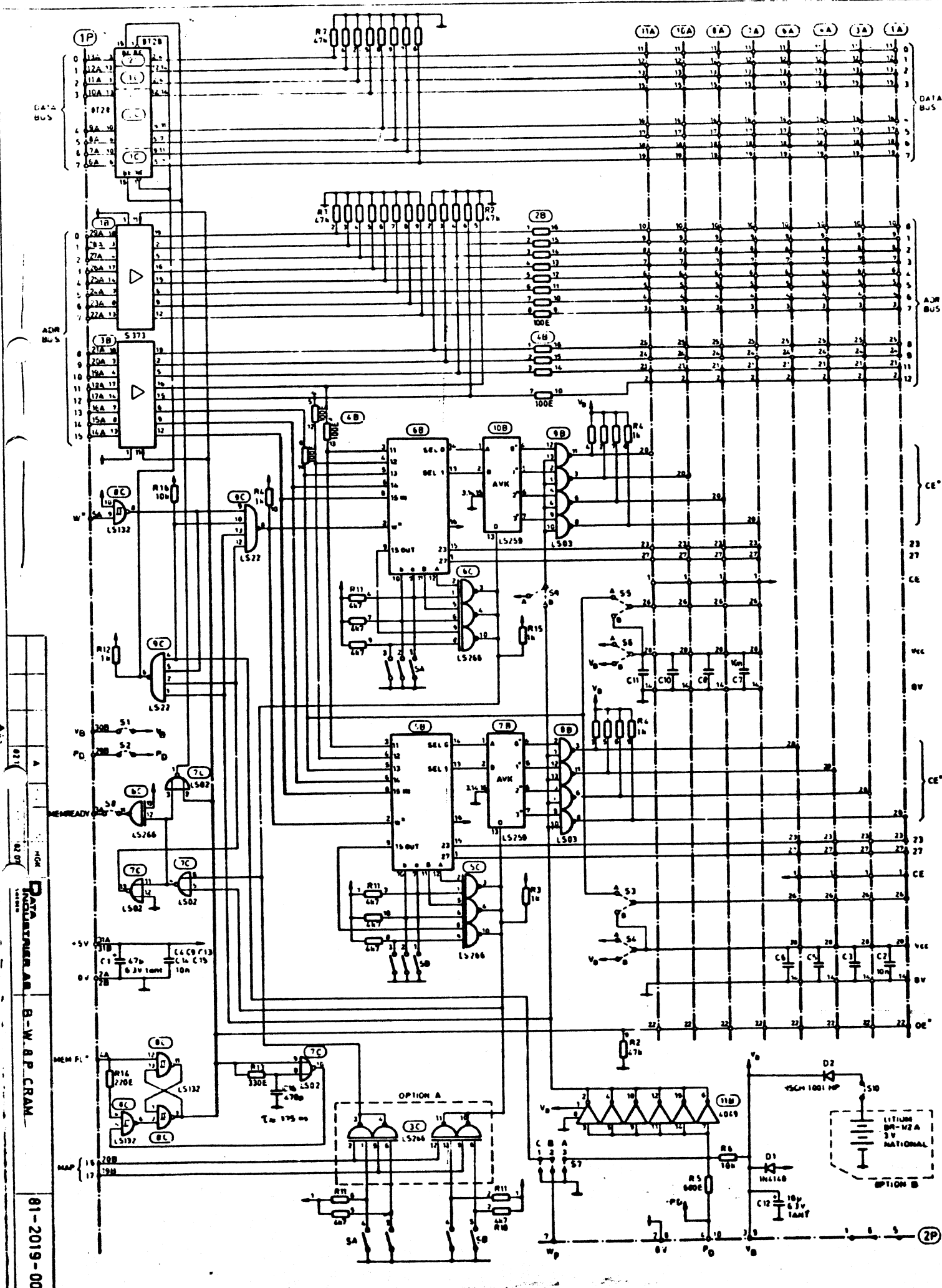


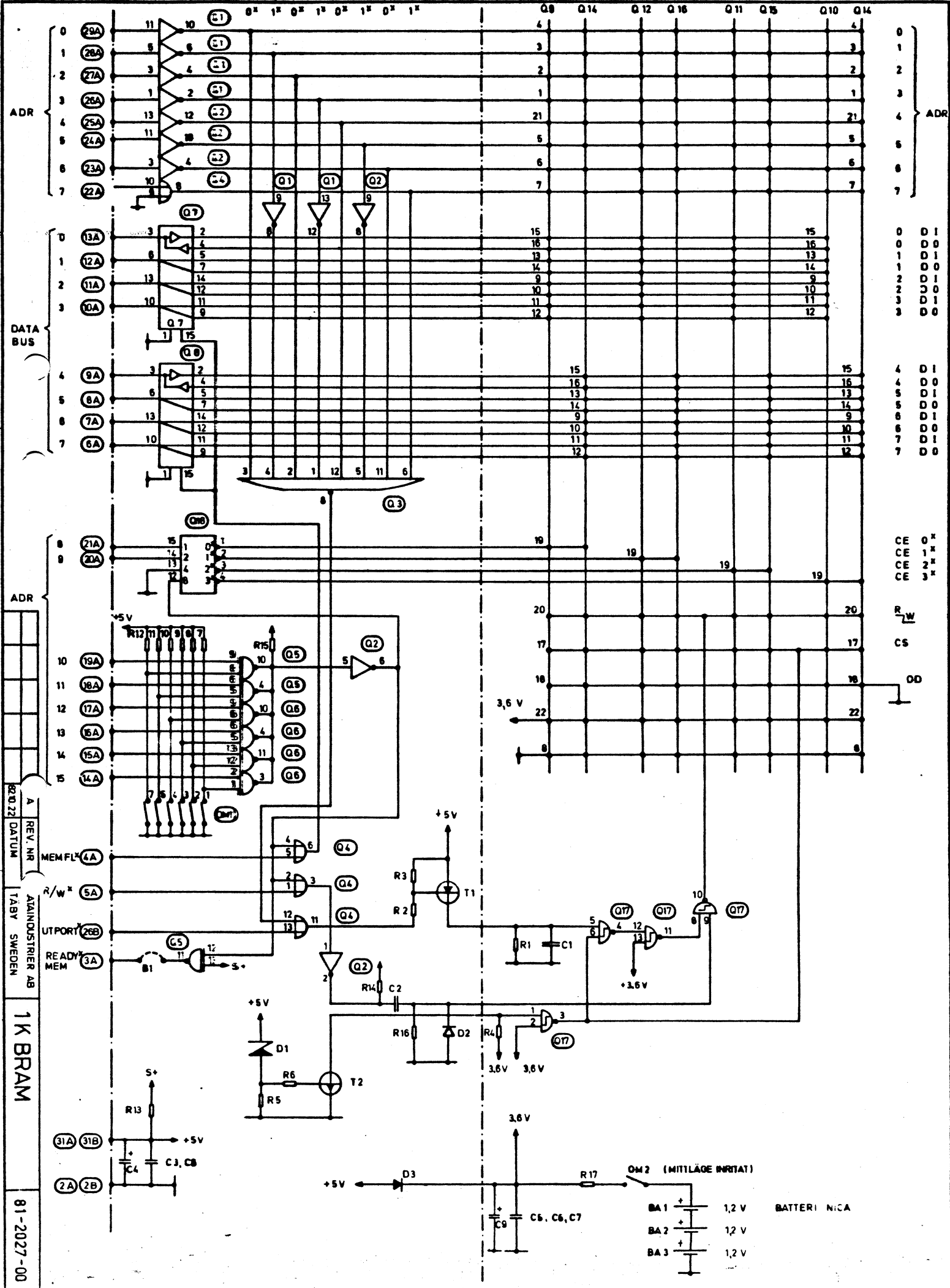
01-2007-00

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81-2009-00





ADR

DATA BUS

ADR

MEMFL (4A)

R/W* (5A)

UTPORT (26B)

READY MEM (3A)

31A 31B

2A 2B

1K BRAM

81-2027-00

CE 0^x

CE 1^x

CE 2^x

CE 3^x

R_W

CS

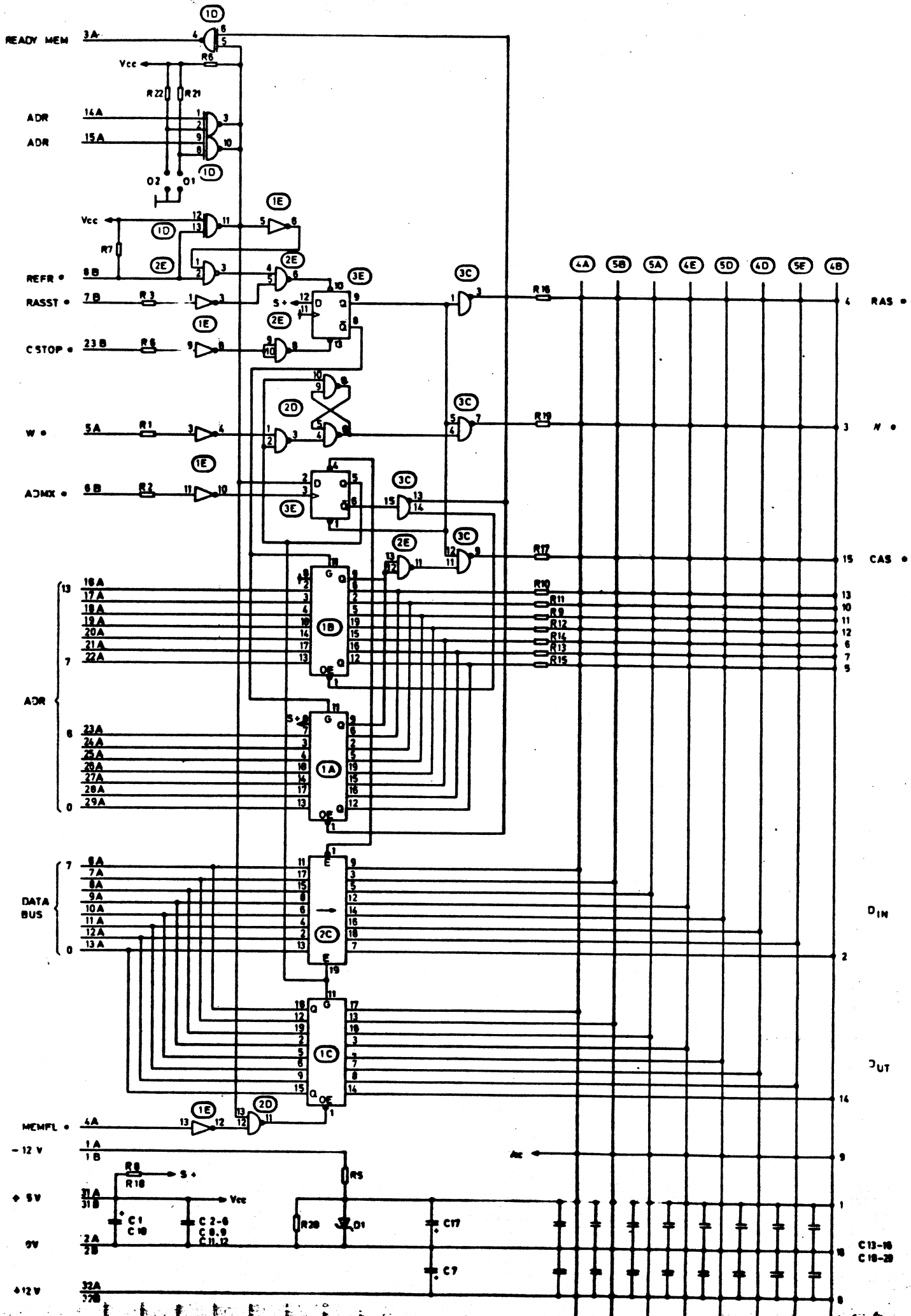
OD

BA 1 1,2 V

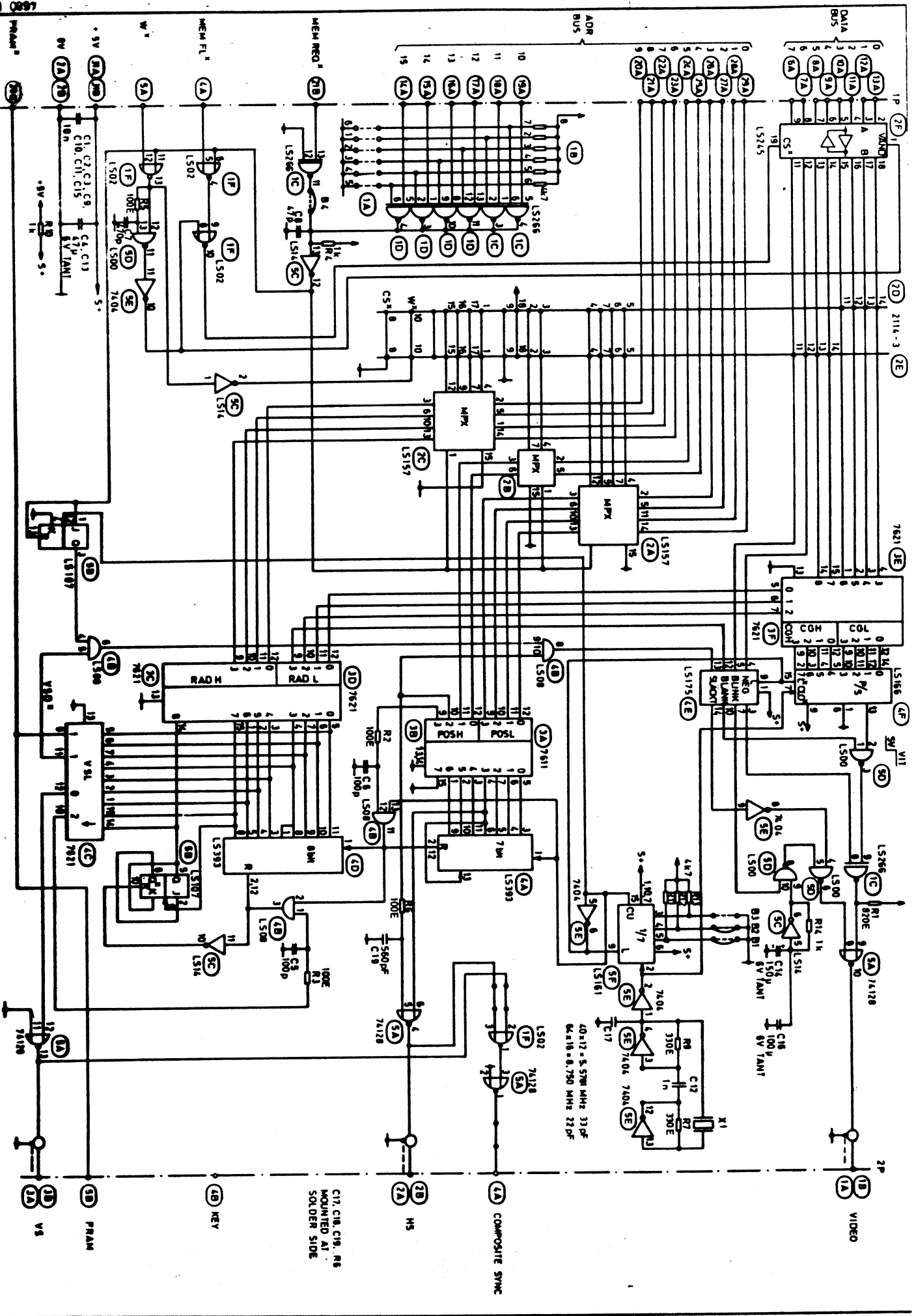
BA 2 1,2 V

BA 3 1,2 V

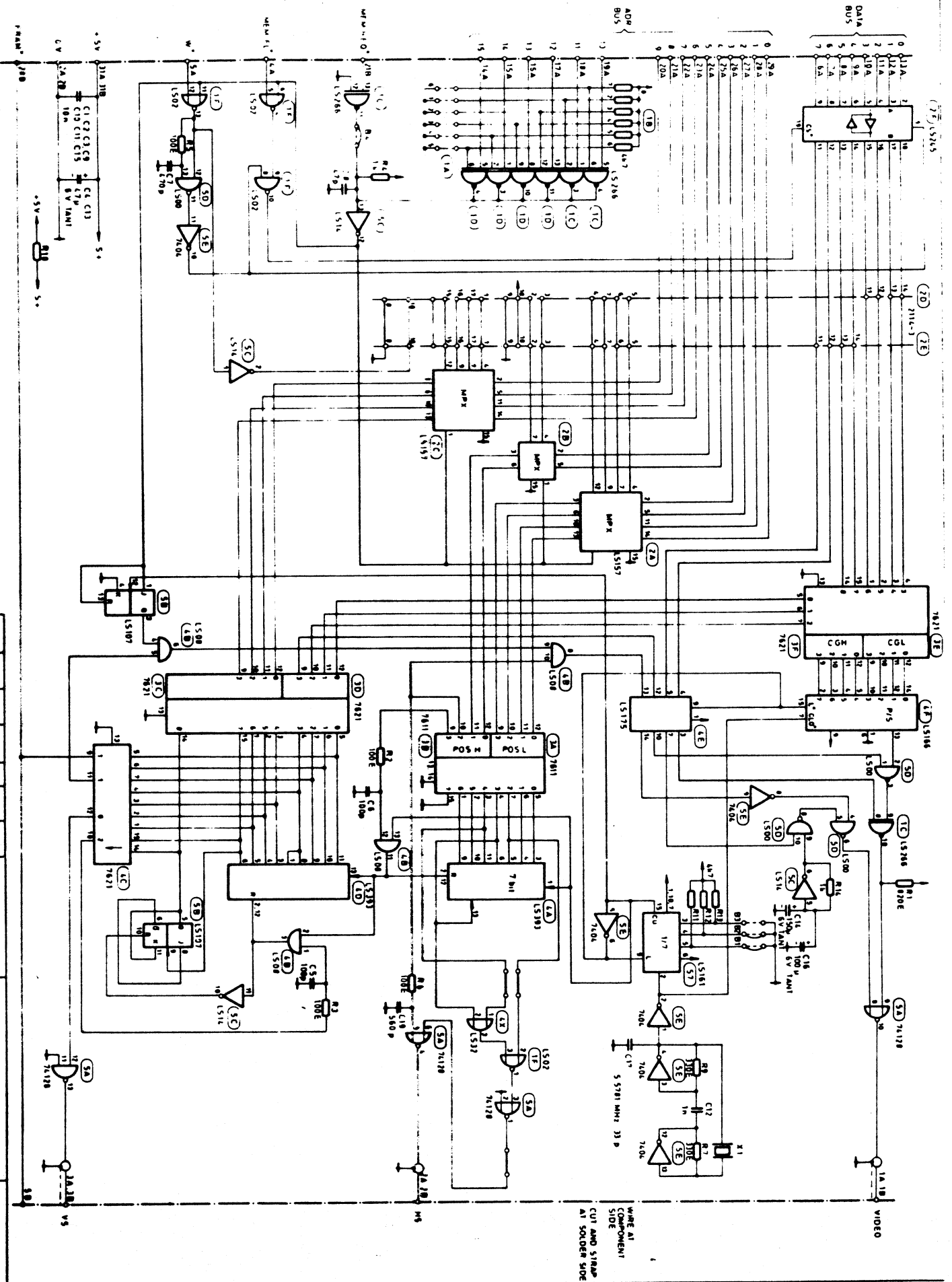
BATTERI NICA



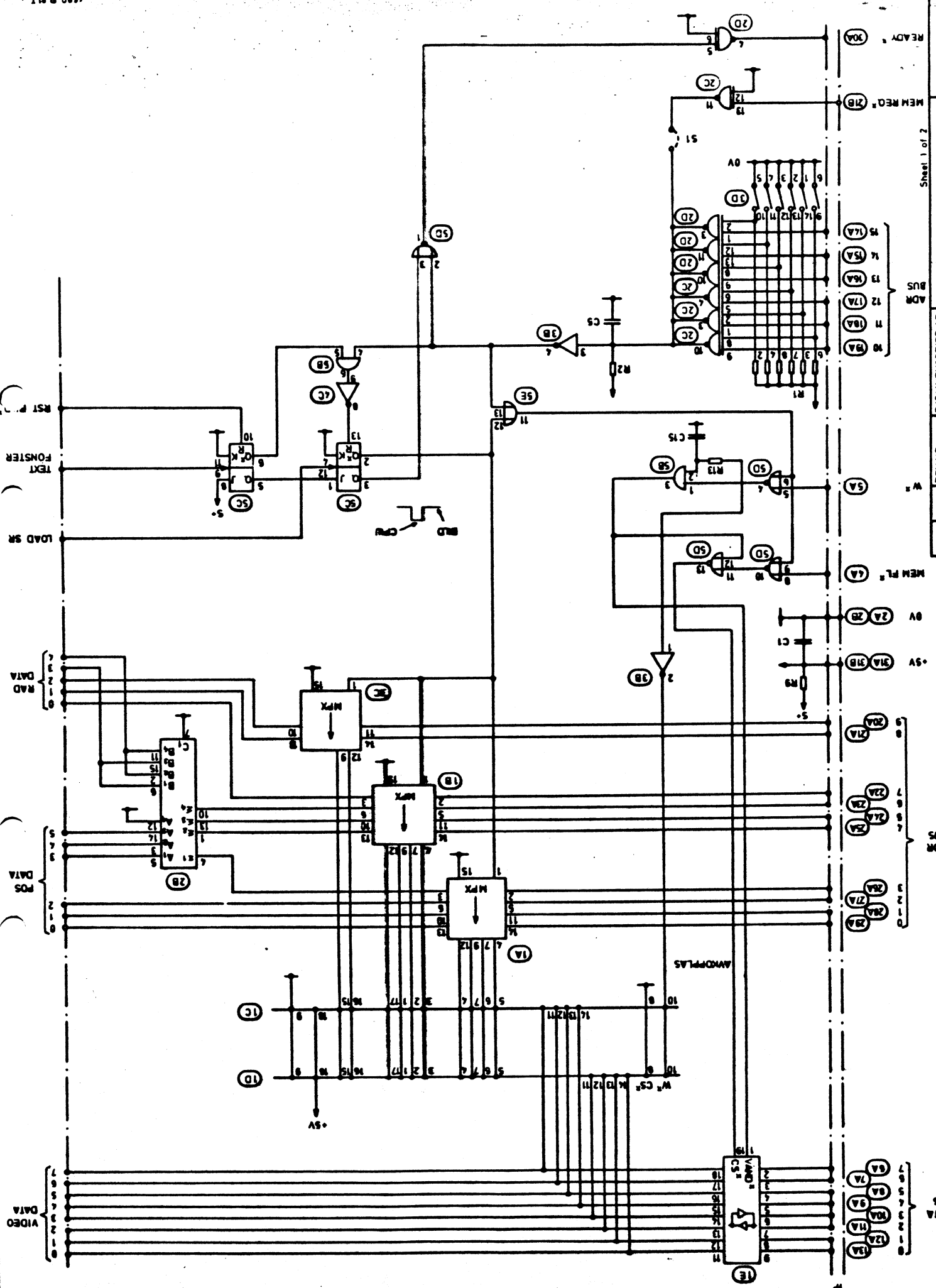
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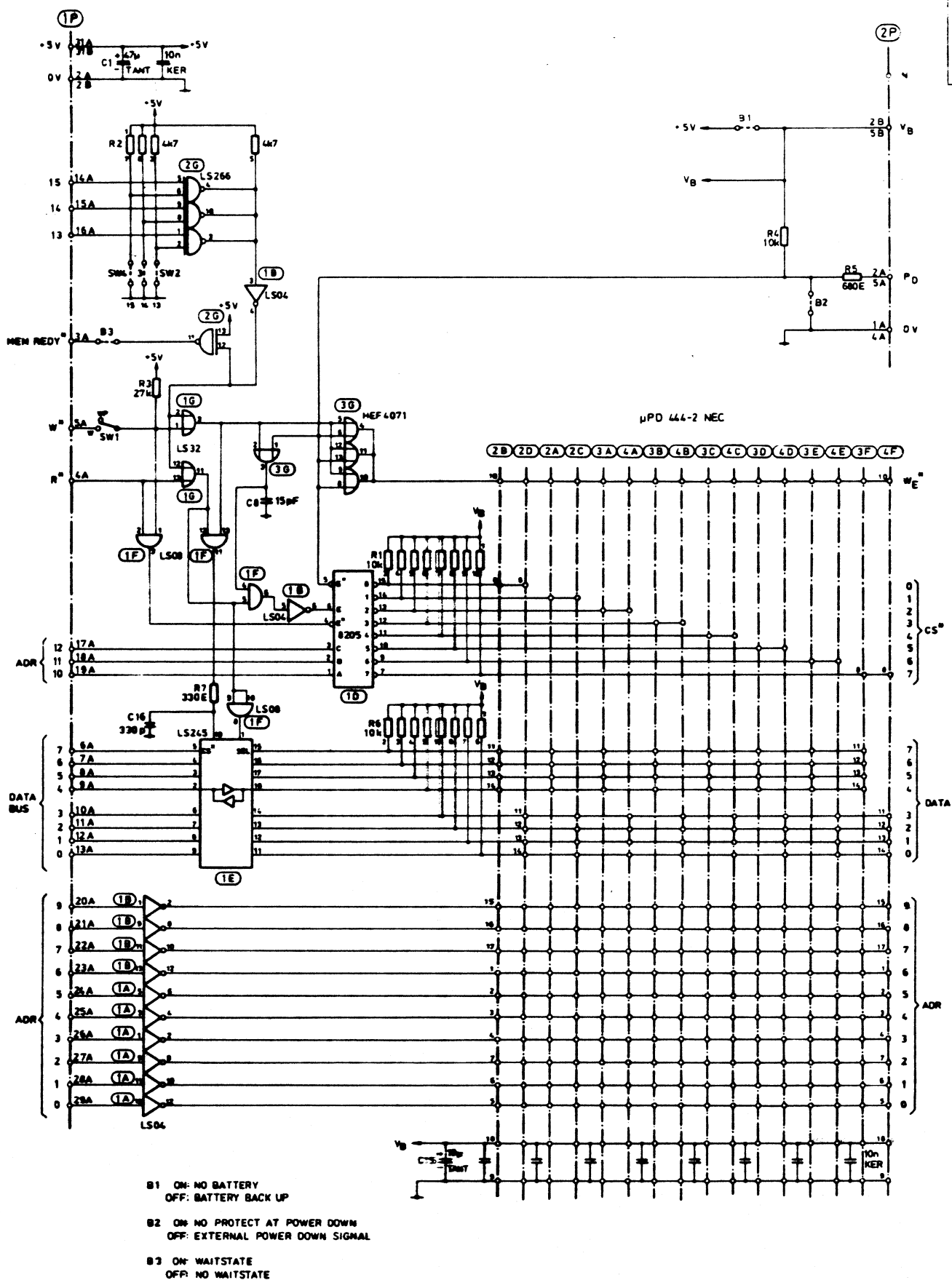


1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	----	-----



NOTE: ALL COMPONENTS CUT AND STRIP AT SOLDER SIDE





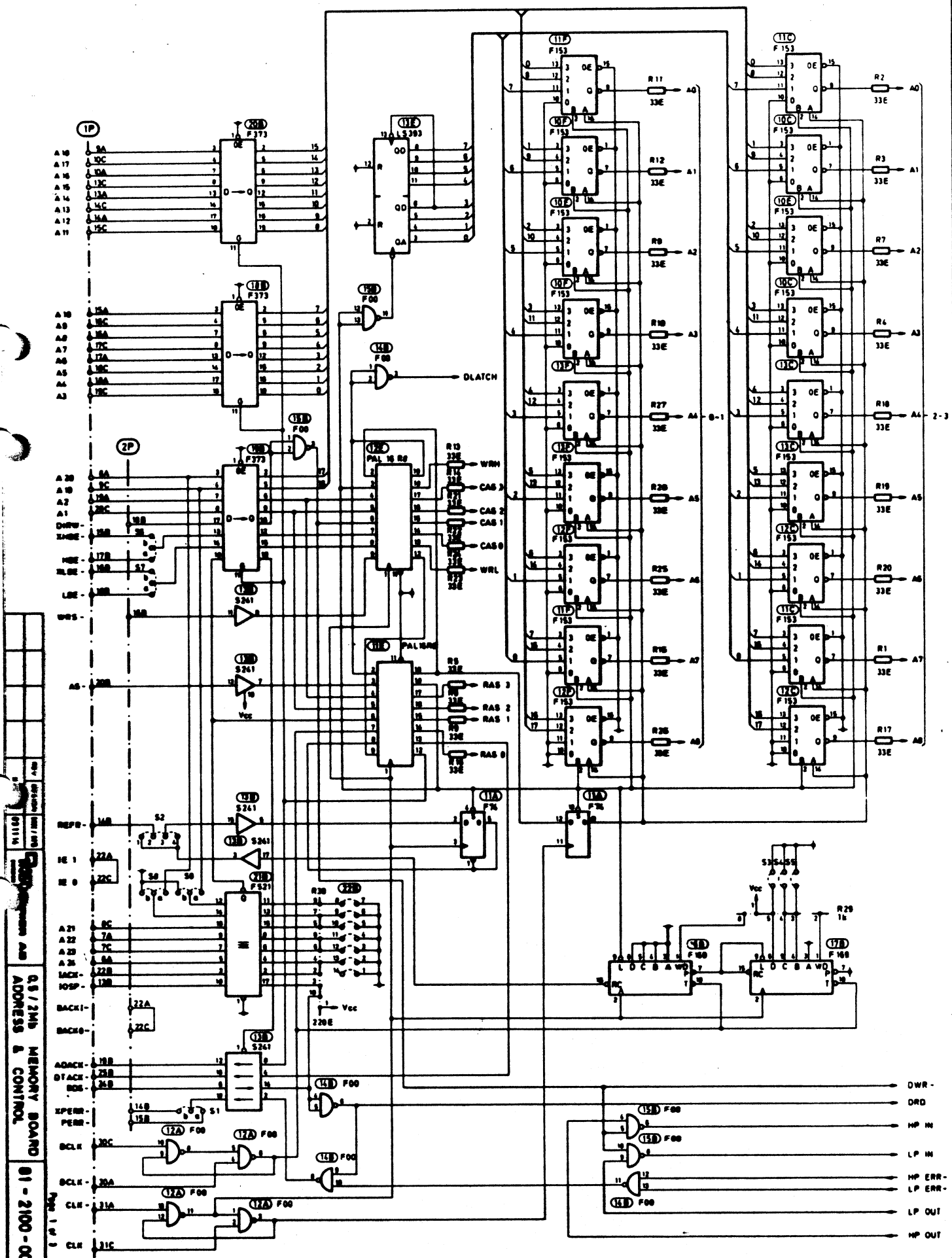
```

B1 ON: NO BATTERY
   OFF: BATTERY BACK UP

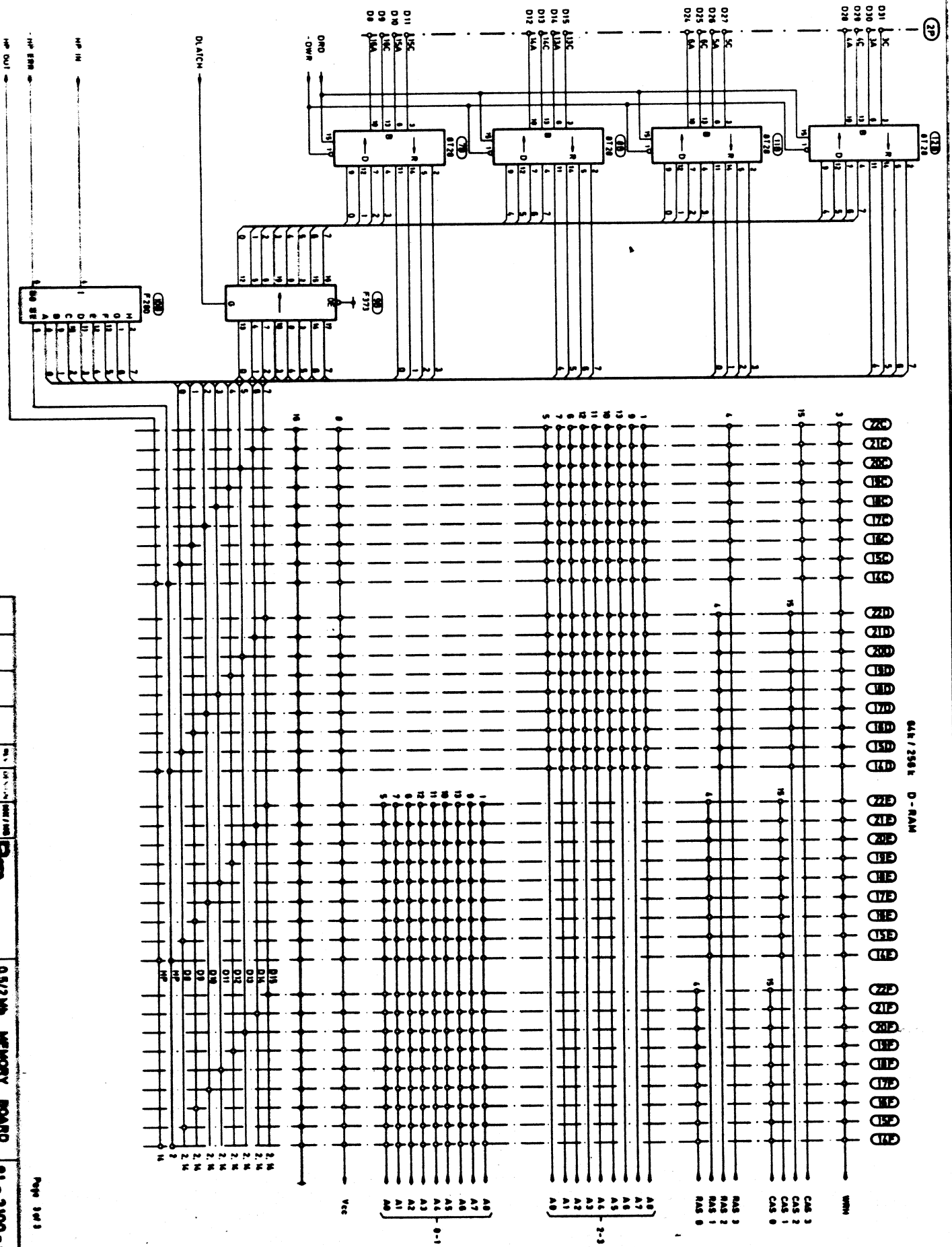
B2 ON: NO PROTECT AT POWER DOWN
   OFF: EXTERNAL POWER DOWN SIGNAL

B3 ON: WAITSTATE
   OFF: NO WAITSTATE

```



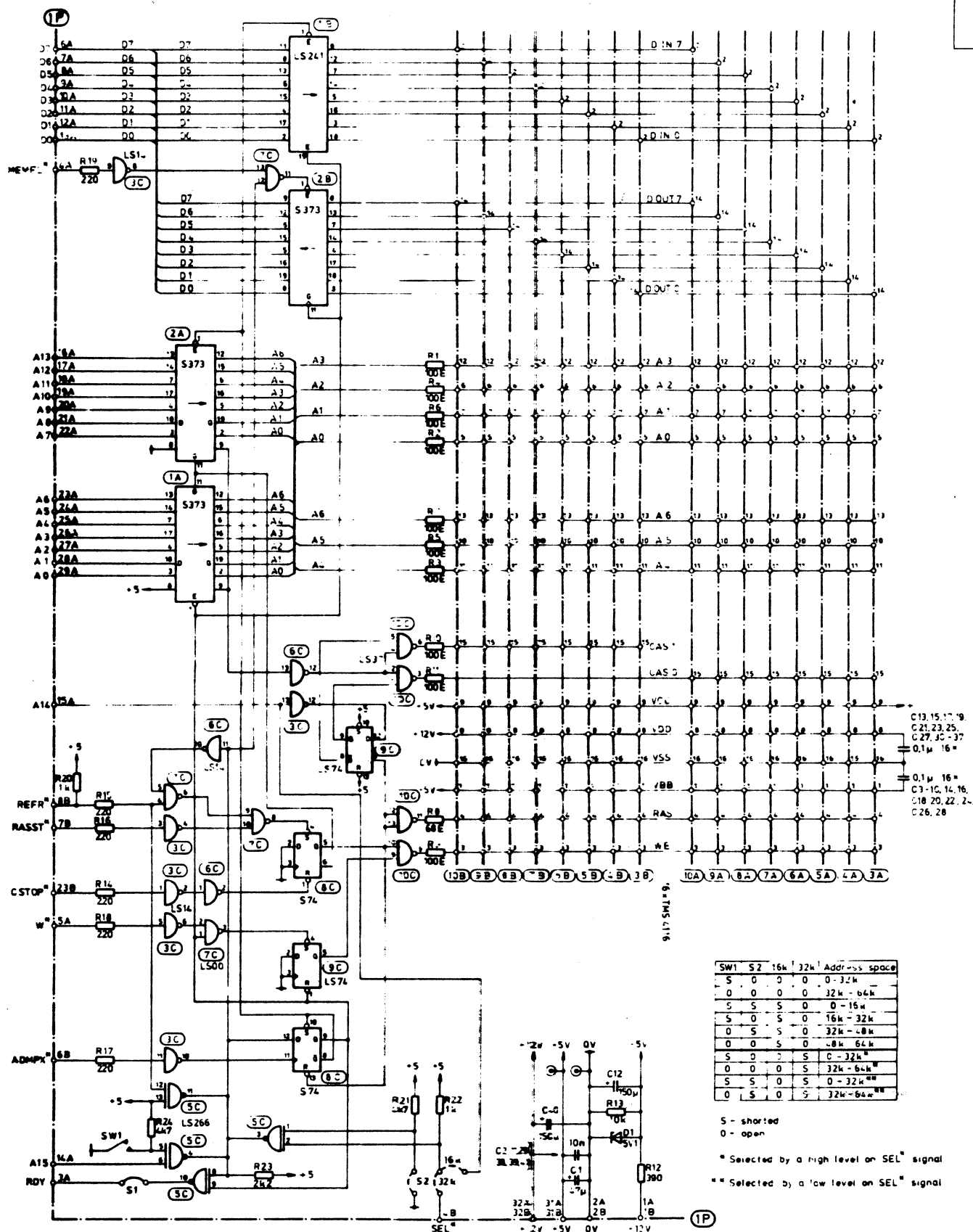


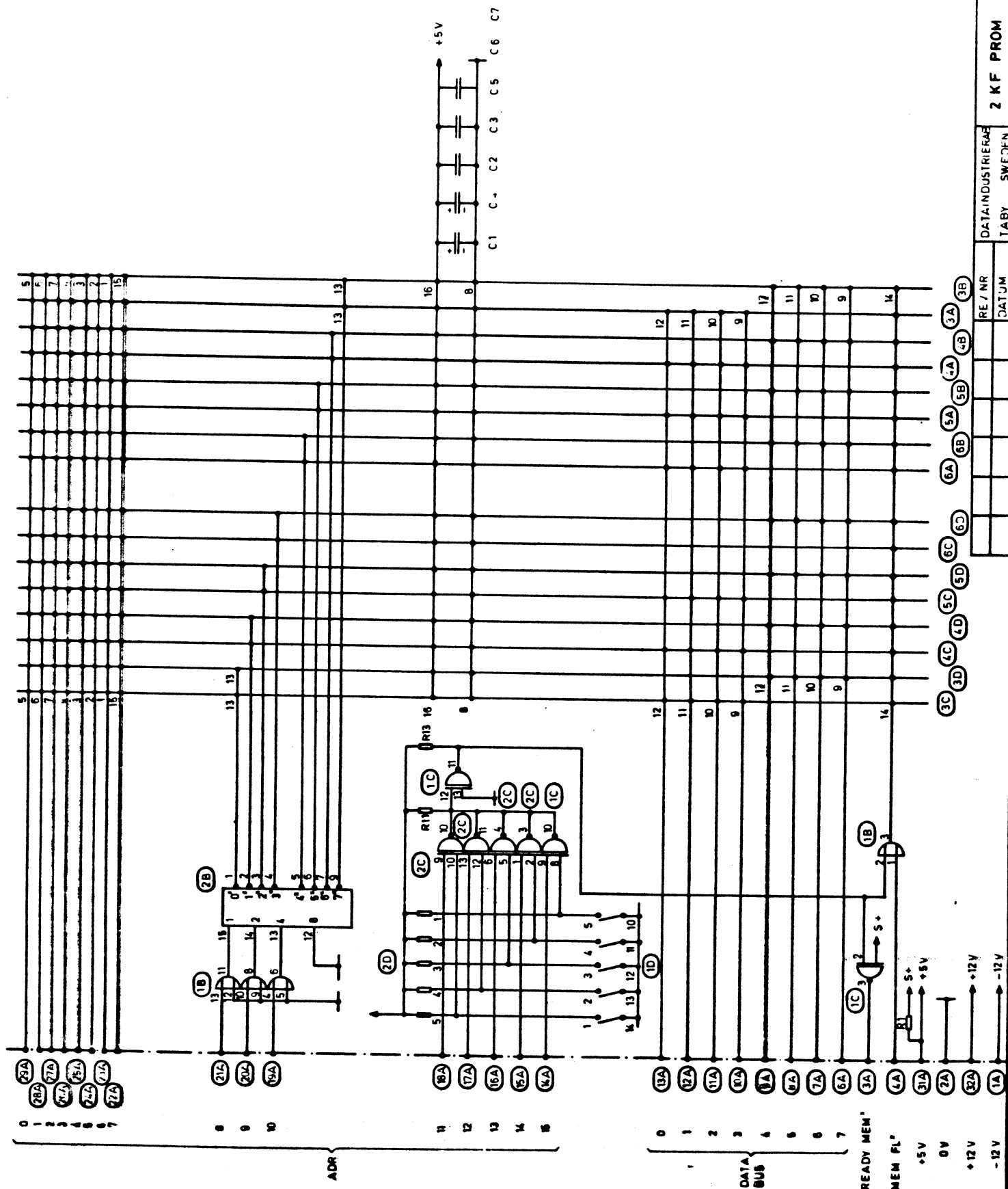


A1

A2

64K / 256K D-RAM

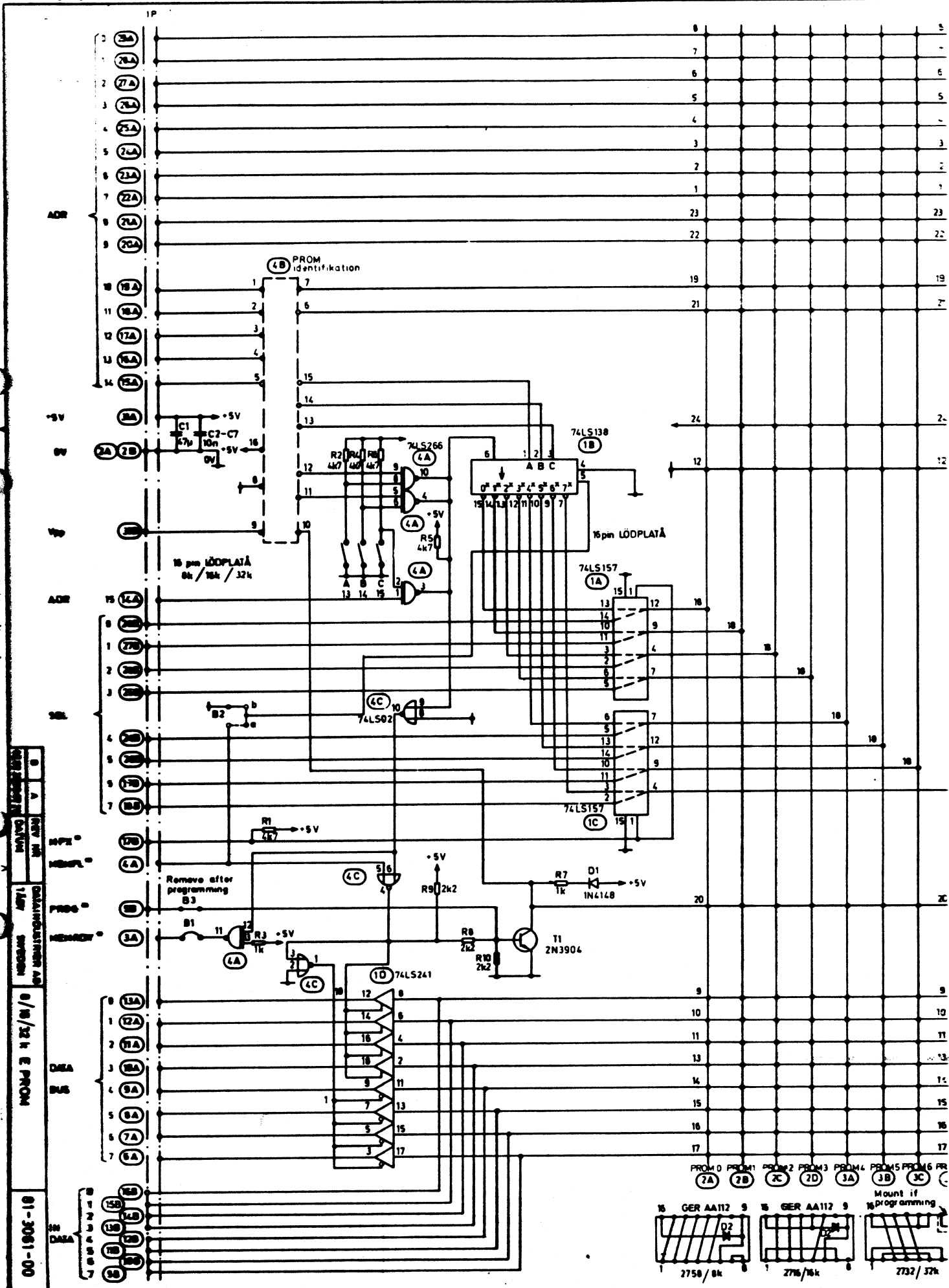


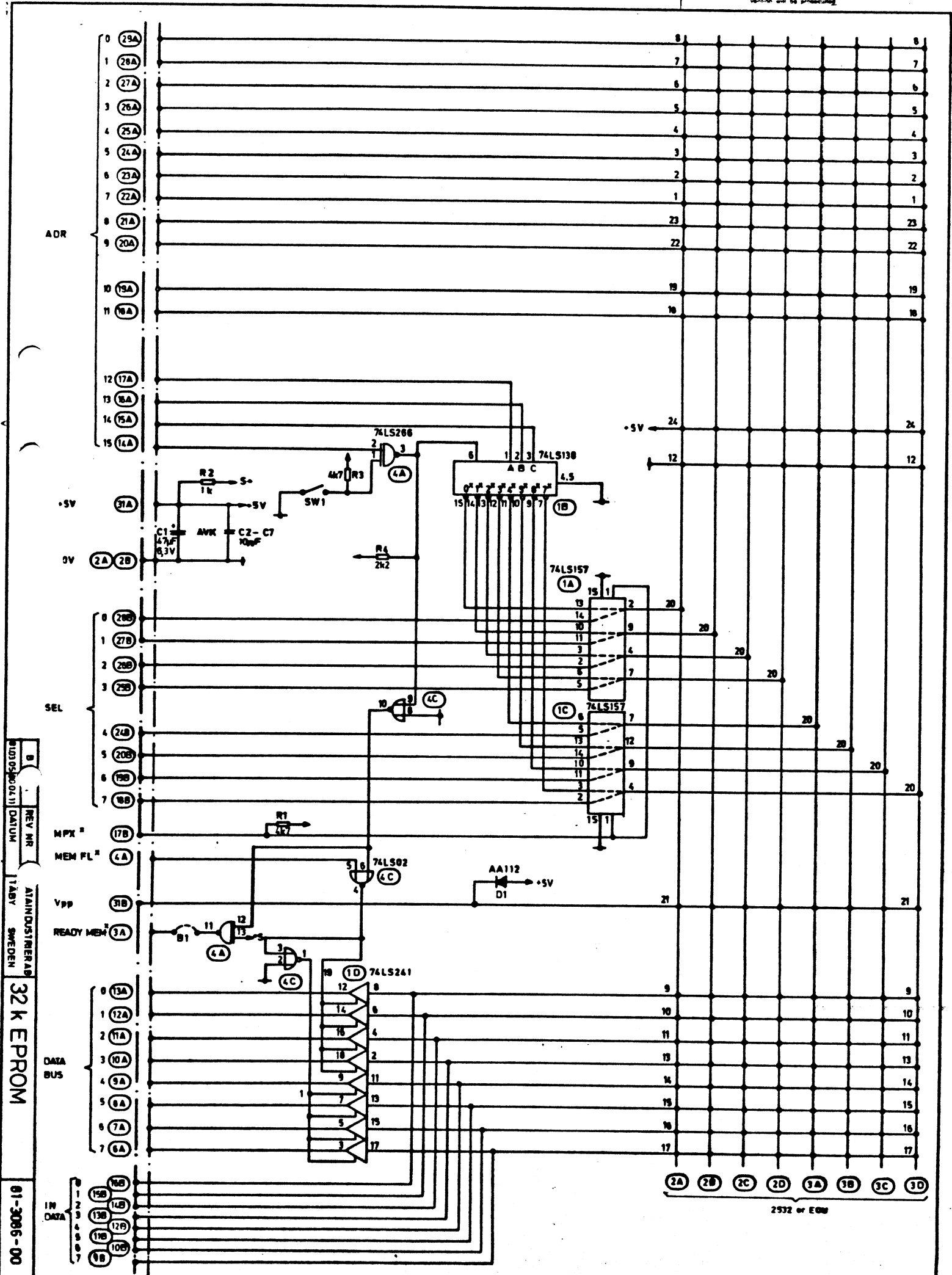




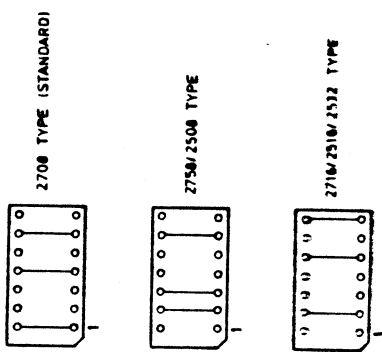
DAVID J. BROWN
B-W 8 P FROM

81-3009-00

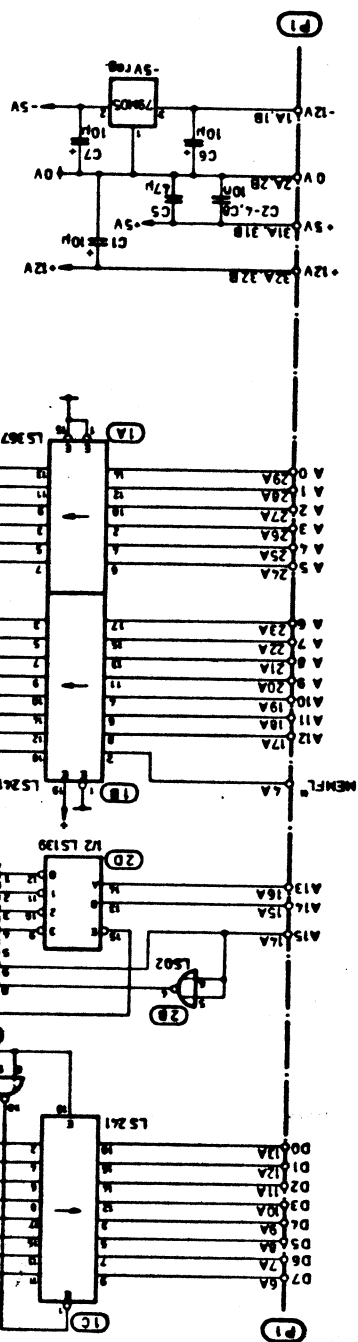




32K EPROM
61-3066-00

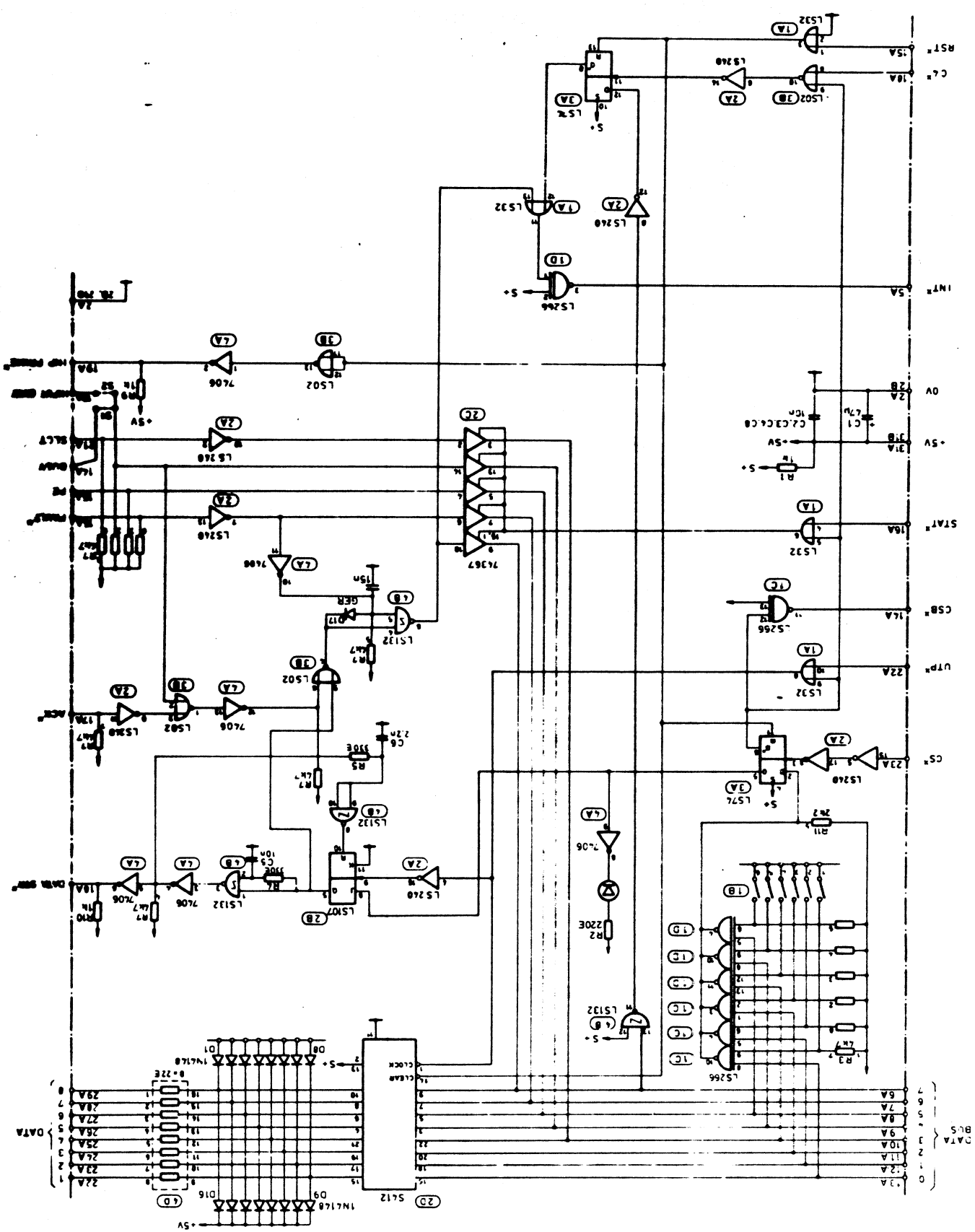


TYPE PLUGS:





DATAINDUSTRIE AB
PRINTER INTERFACE
CENTRONICS



Pinout table for the DB-25 connector:

Pin	Signal
1	1A
2	2A
3	3A
4	4A
5	5A
6	6A
7	7A
8	8A
9	9A
10	10A
11	11A
12	12A
13	13A
14	14A
15	15A
16	16A
17	17A
18	18A
19	19A
20	20A
21	21A
22	22A
23	23A
24	24A

Pinout table for the DB-5 connector:

Pin	Signal
1	1A
2	2A
3	3A
4	4A
5	5A

Pinout table for the DB-25 connector (continued):

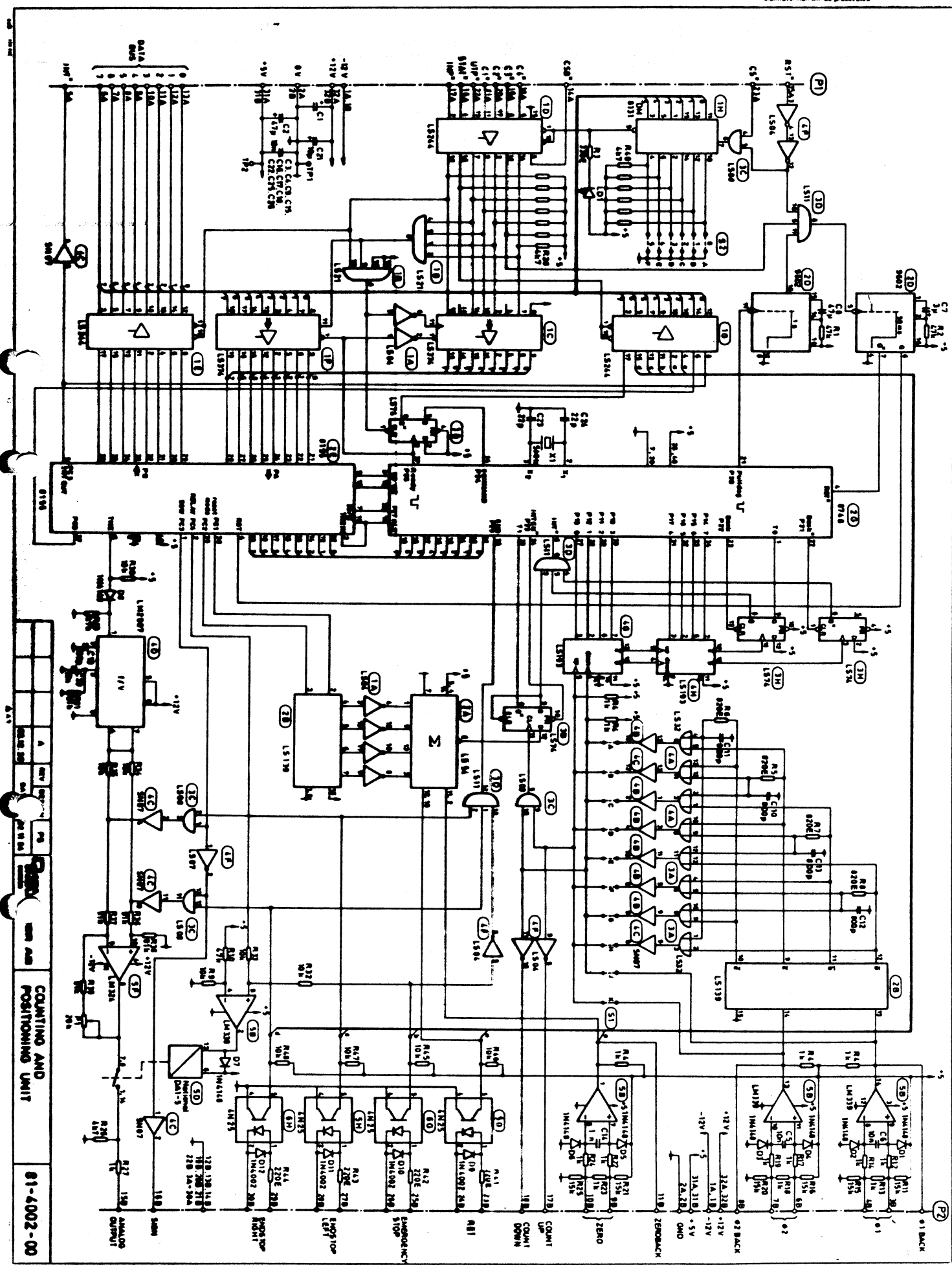
Pin	Signal
25	25A

Pinout table for the DB-5 connector (continued):

Pin	Signal
6	6A
7	7A
8	8A
9	9A
10	10A

Pinout table for the DB-25 connector (continued):

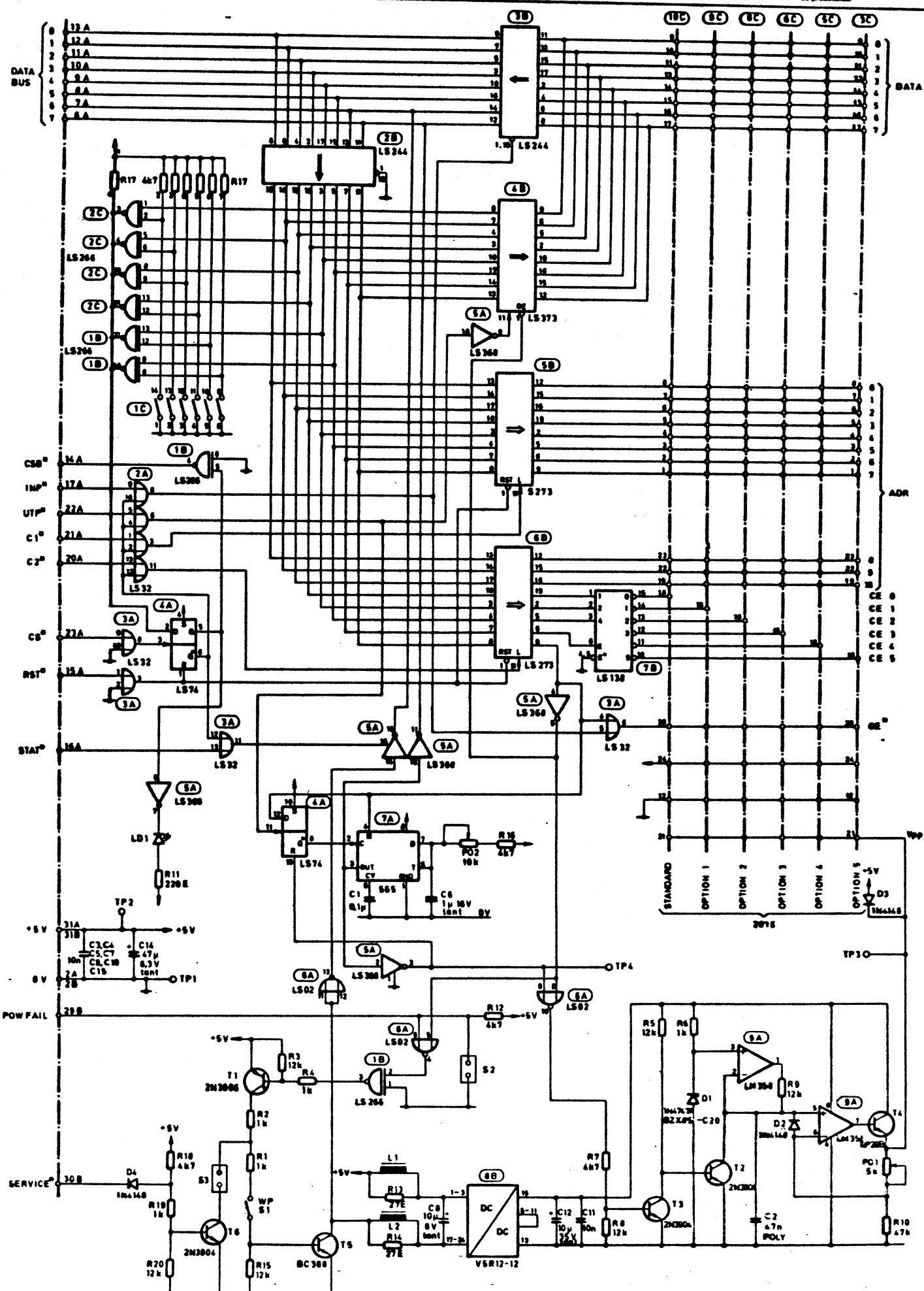
Pin	Signal
11	11A
12	12A
13	13A
14	14A
15	15A



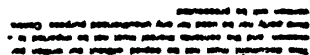
COUNTING AND
POSITIONING UNIT

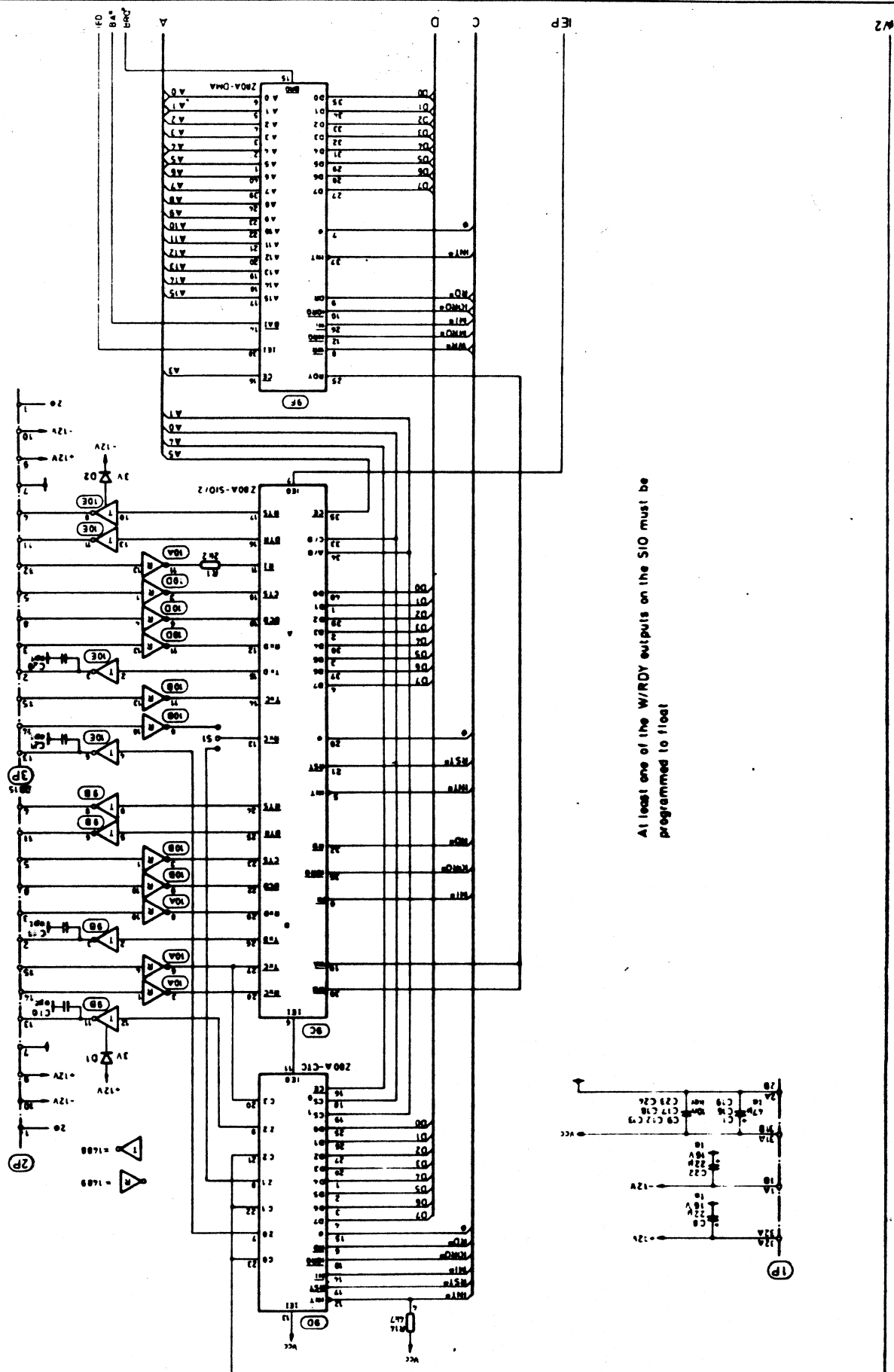
81-4002-00

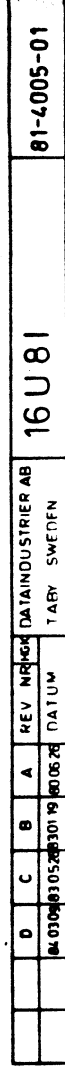
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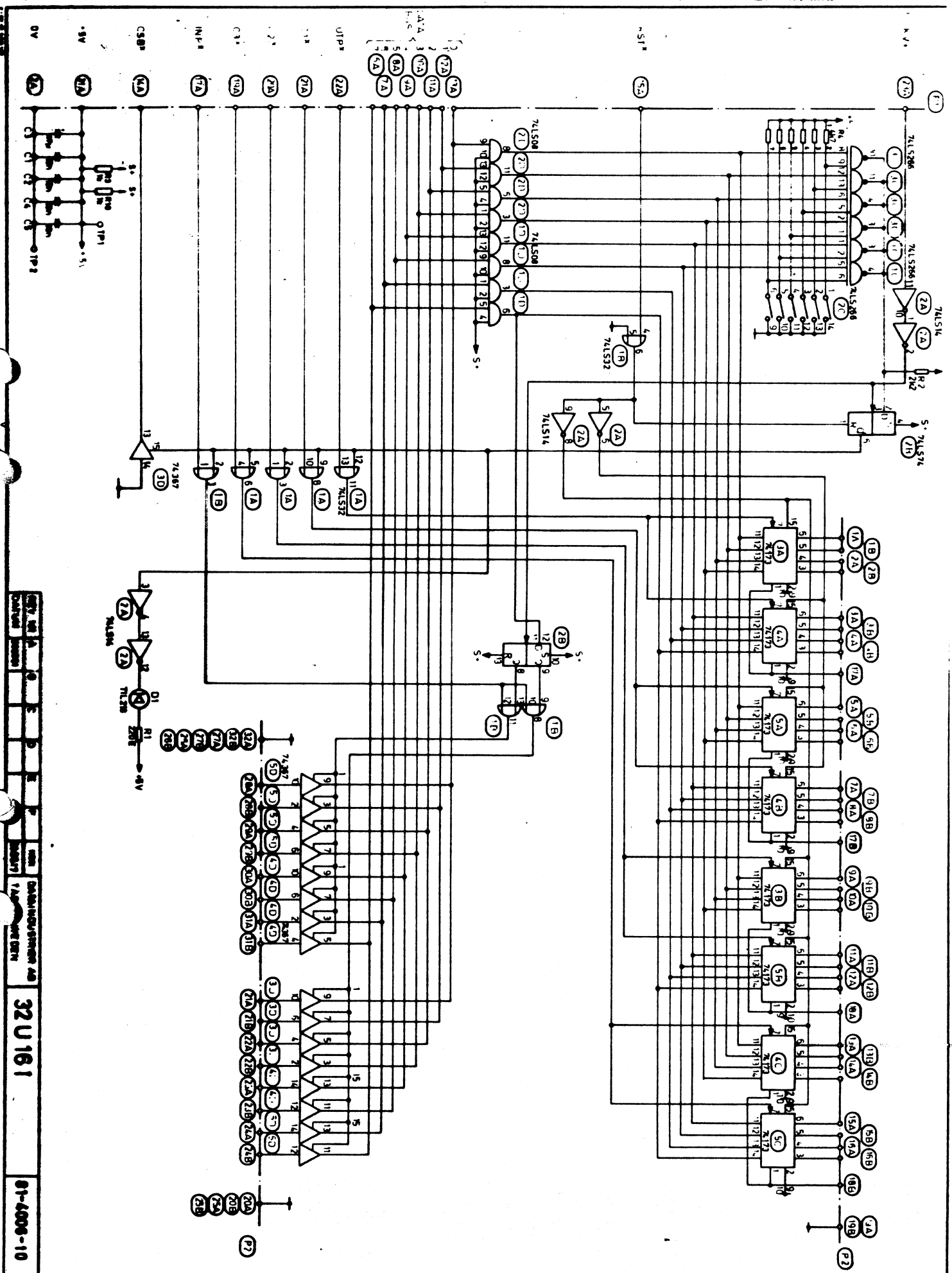
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 DATE: 81-4003-00
 81-4003-00





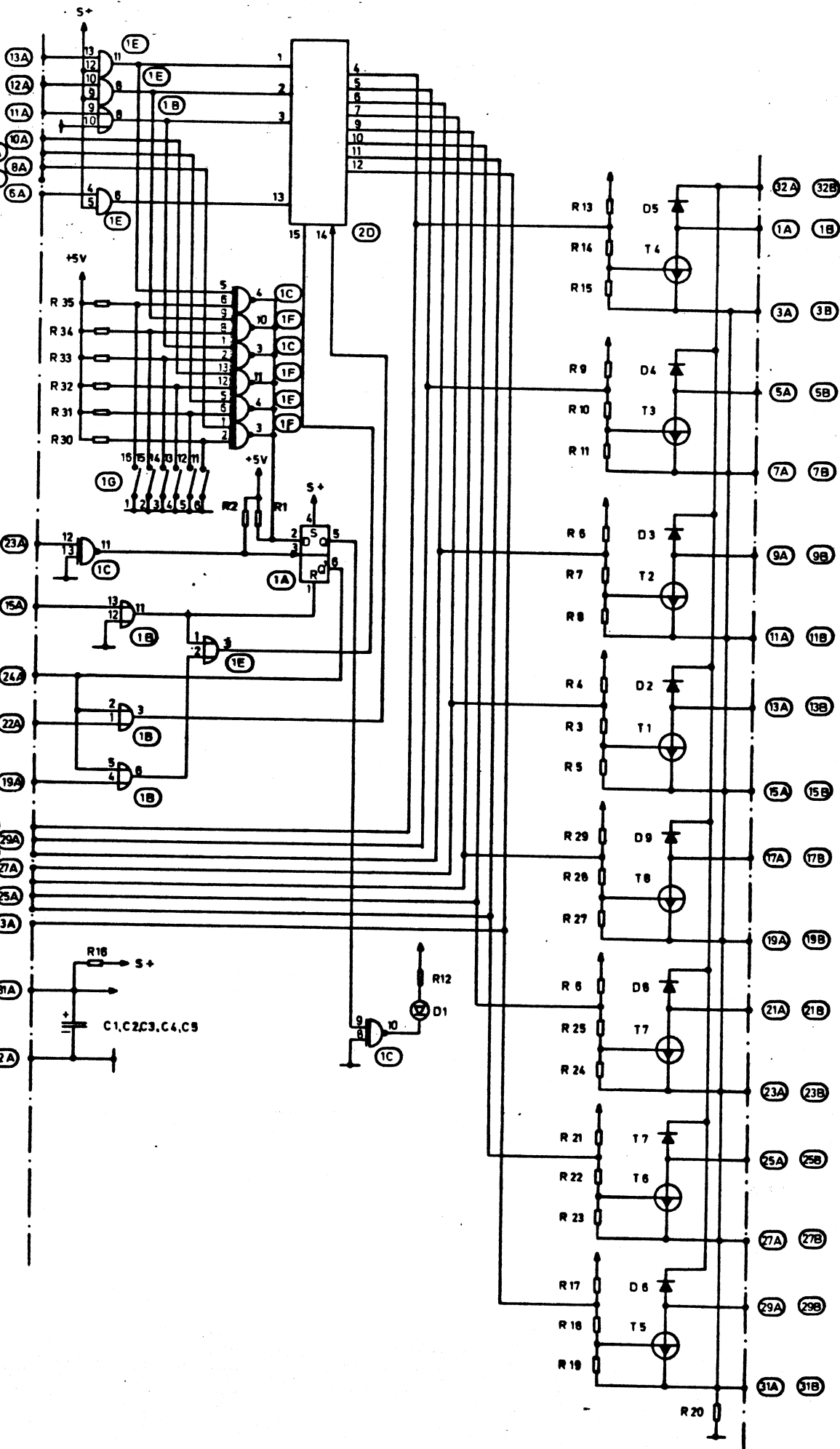


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DATA
BUS

0
1
2
3
4
5
6
7



CS¹

RST¹

CSS¹

UTP¹

C3¹

OP

0
1
2
3
4
5
6
7

+5V

0V

R16 S+

C1,C2,C3,C4,C5

R12 D1
1C

R6

R25

R24

R21

R22

R23

R17

R18

R19

R20

R13

R14

R15

R9

R10

R11

R6

R7

R8

R4

R3

R5

R29

R28

R27

R6

R25

R24

R21

R22

R23

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R3

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R17

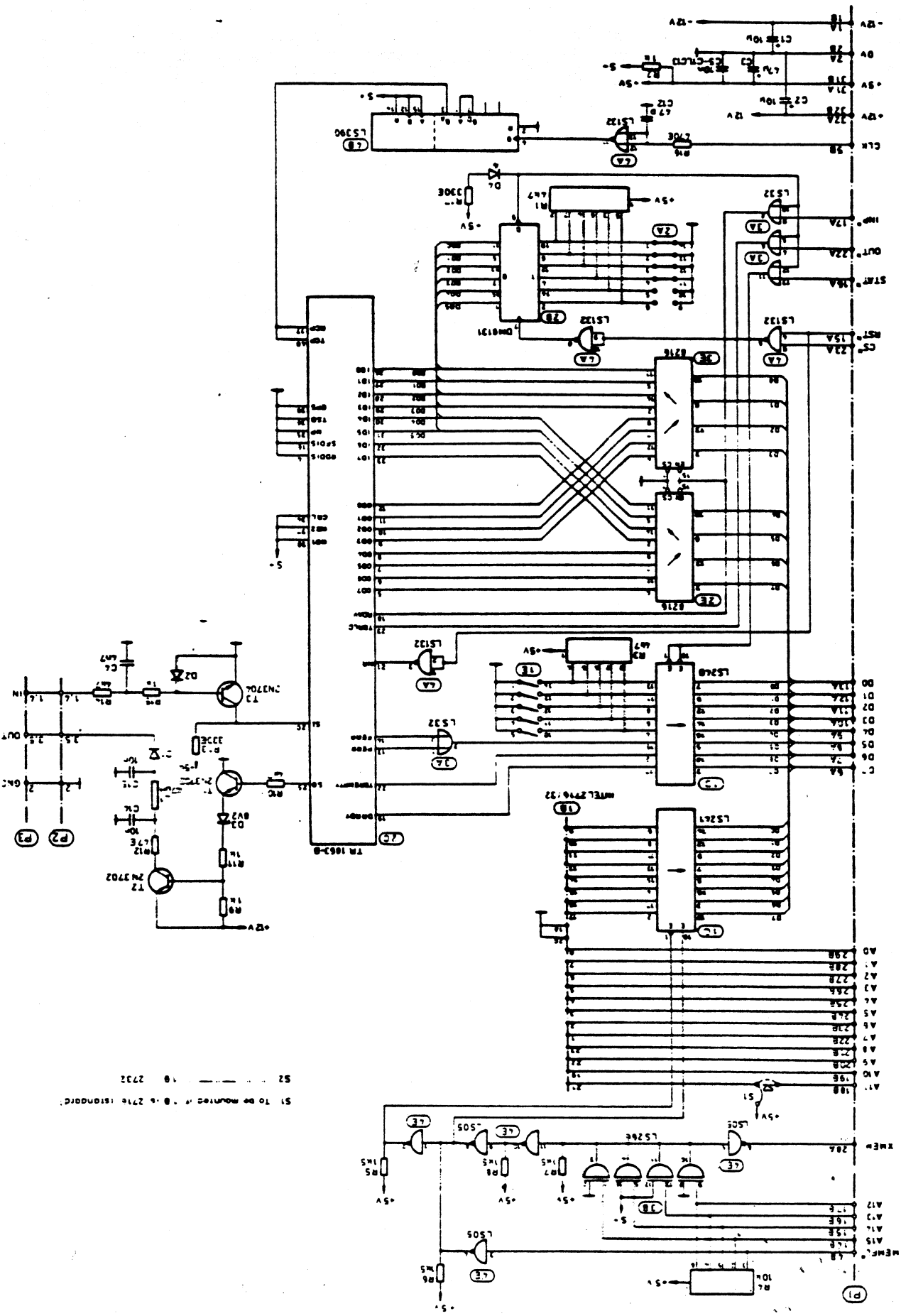
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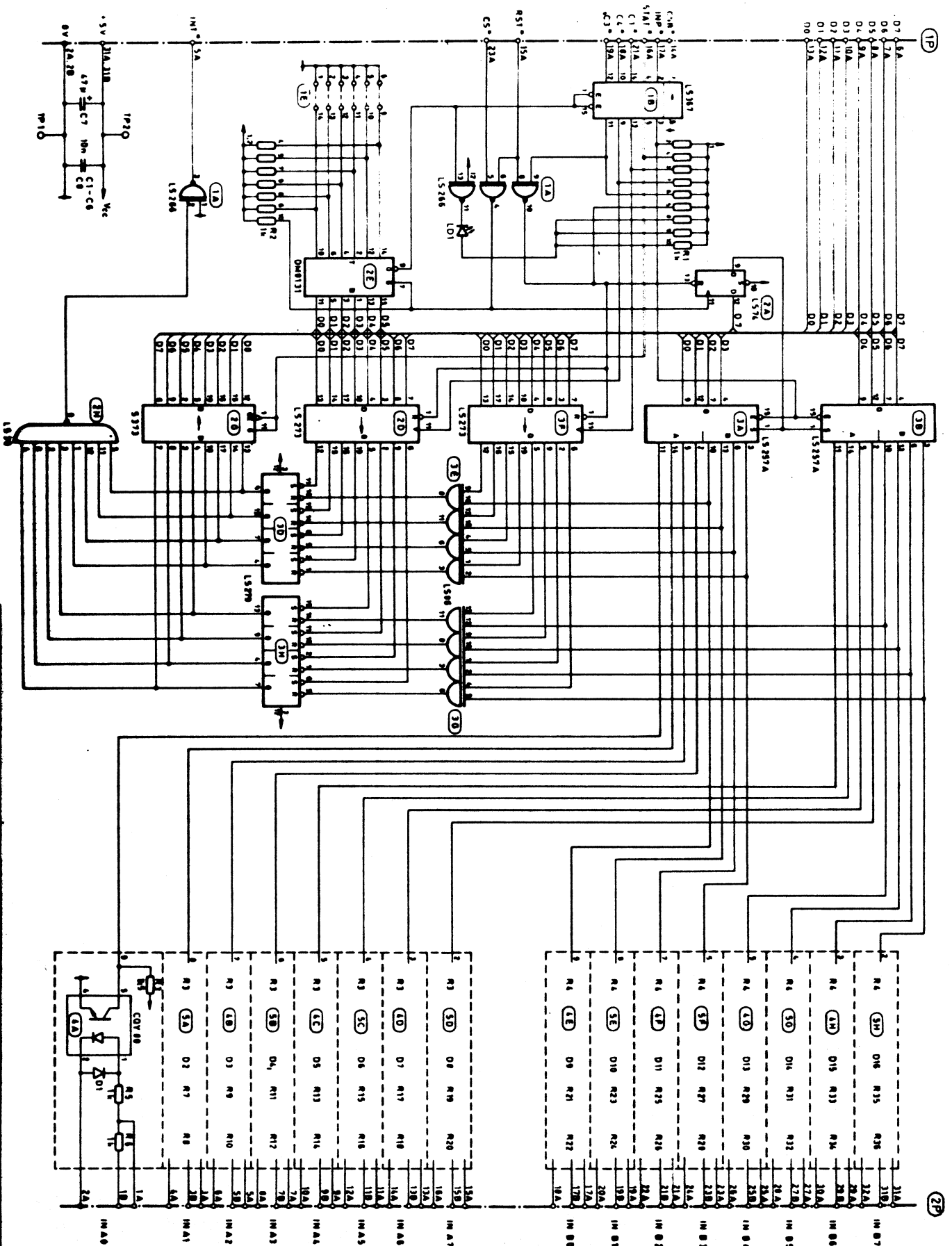
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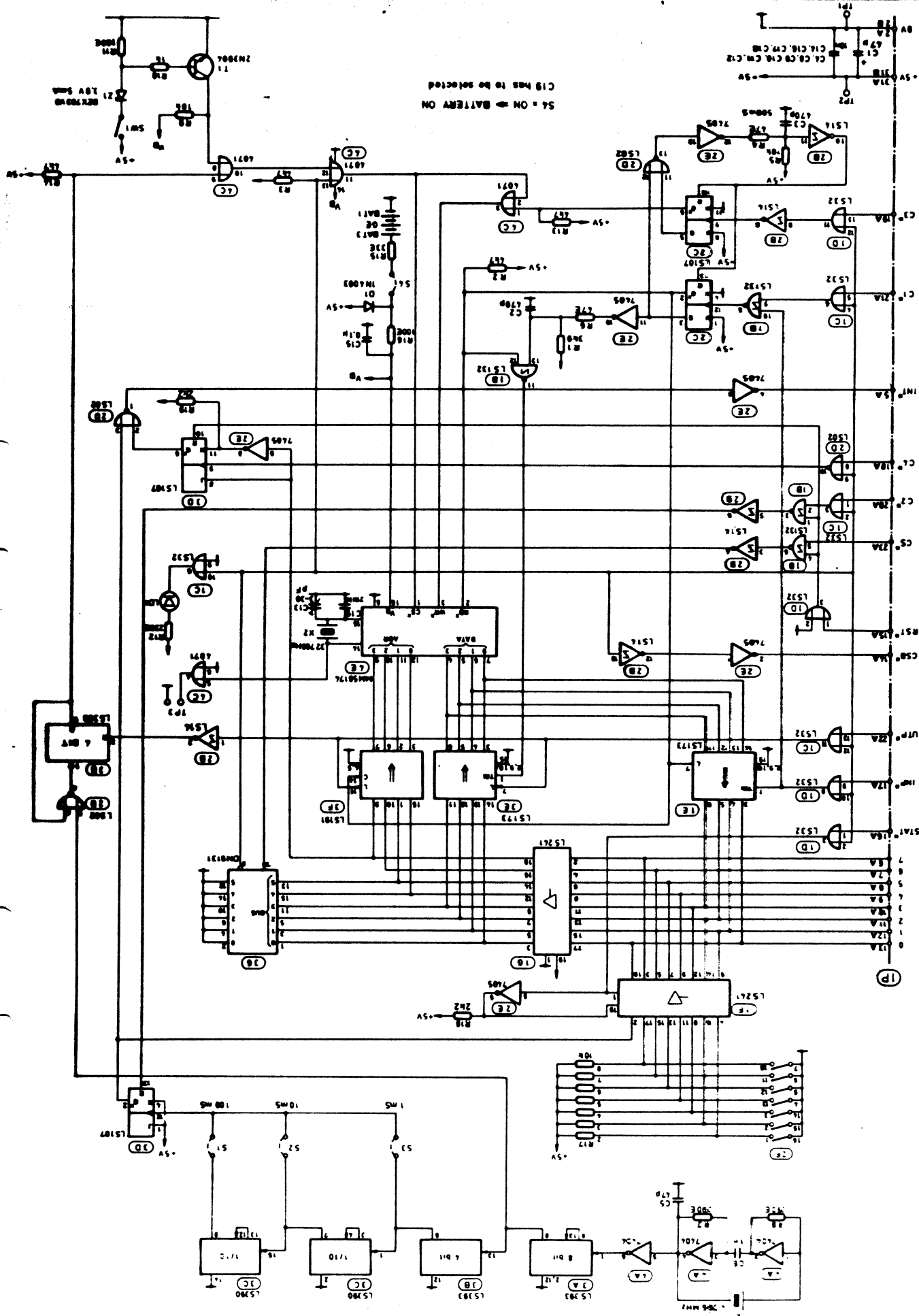
R14</



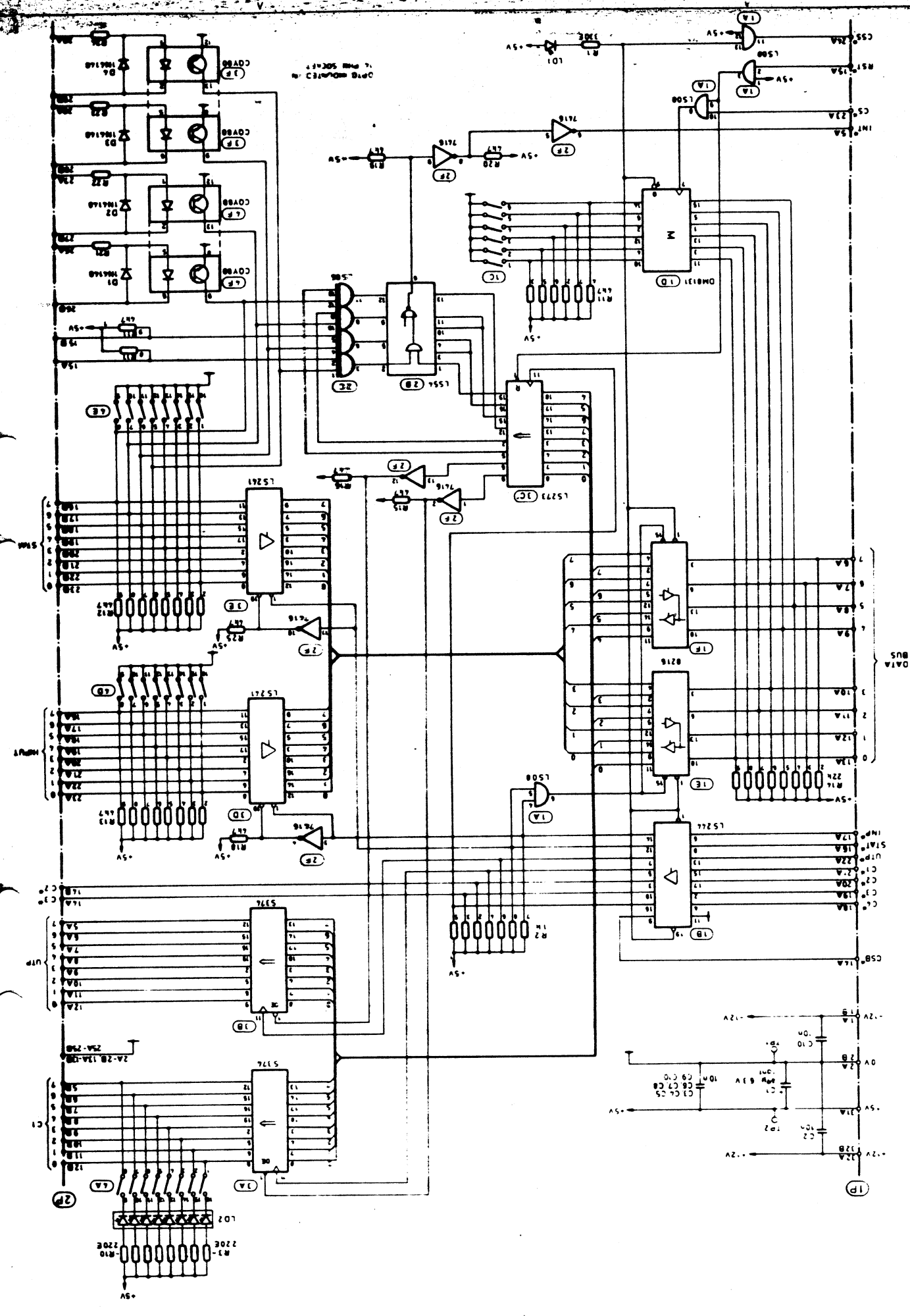
S1 TO BE MOUNTED ON 8.6 2716 STANDARD
S2 18 2732

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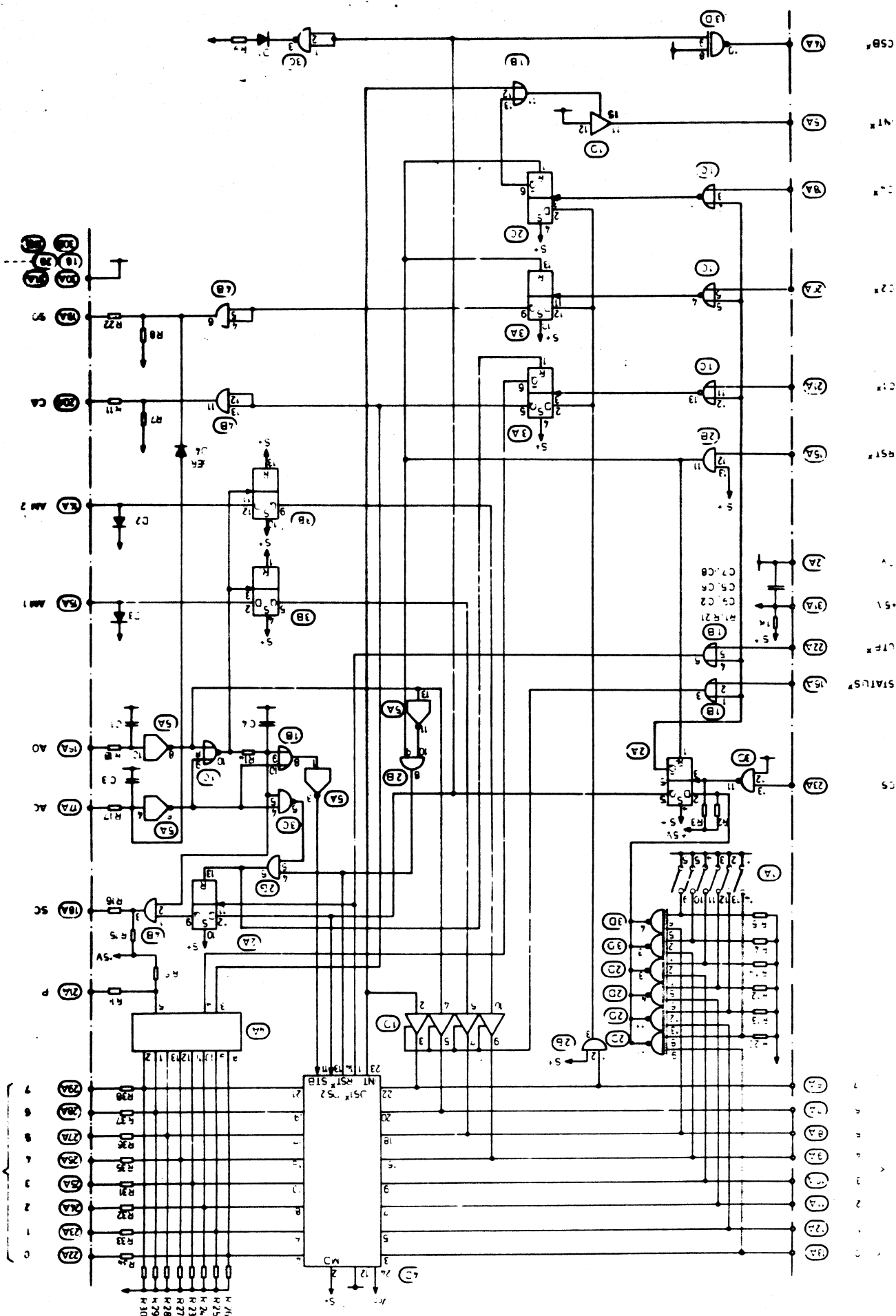


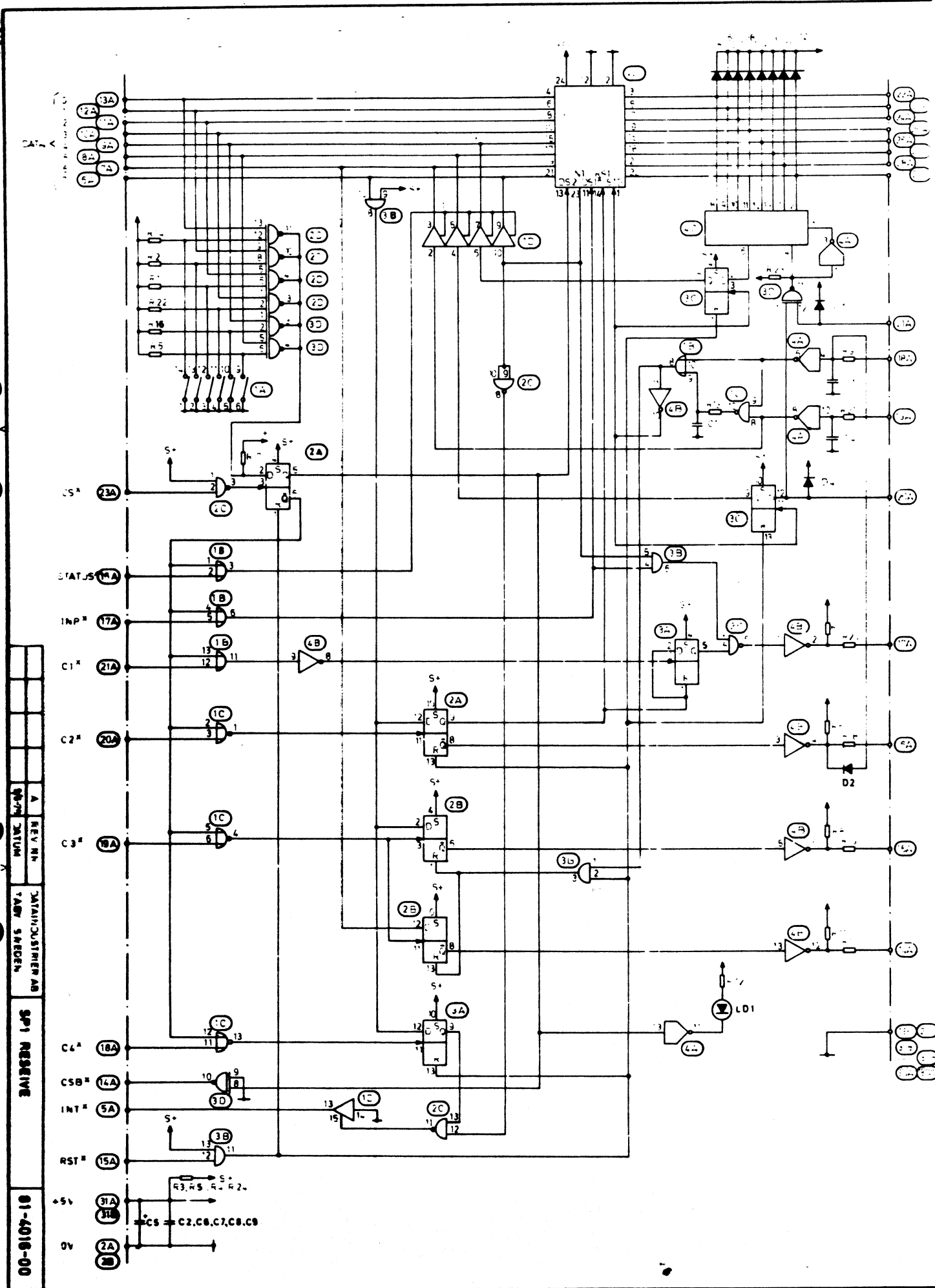
Approved on 20/01/2001

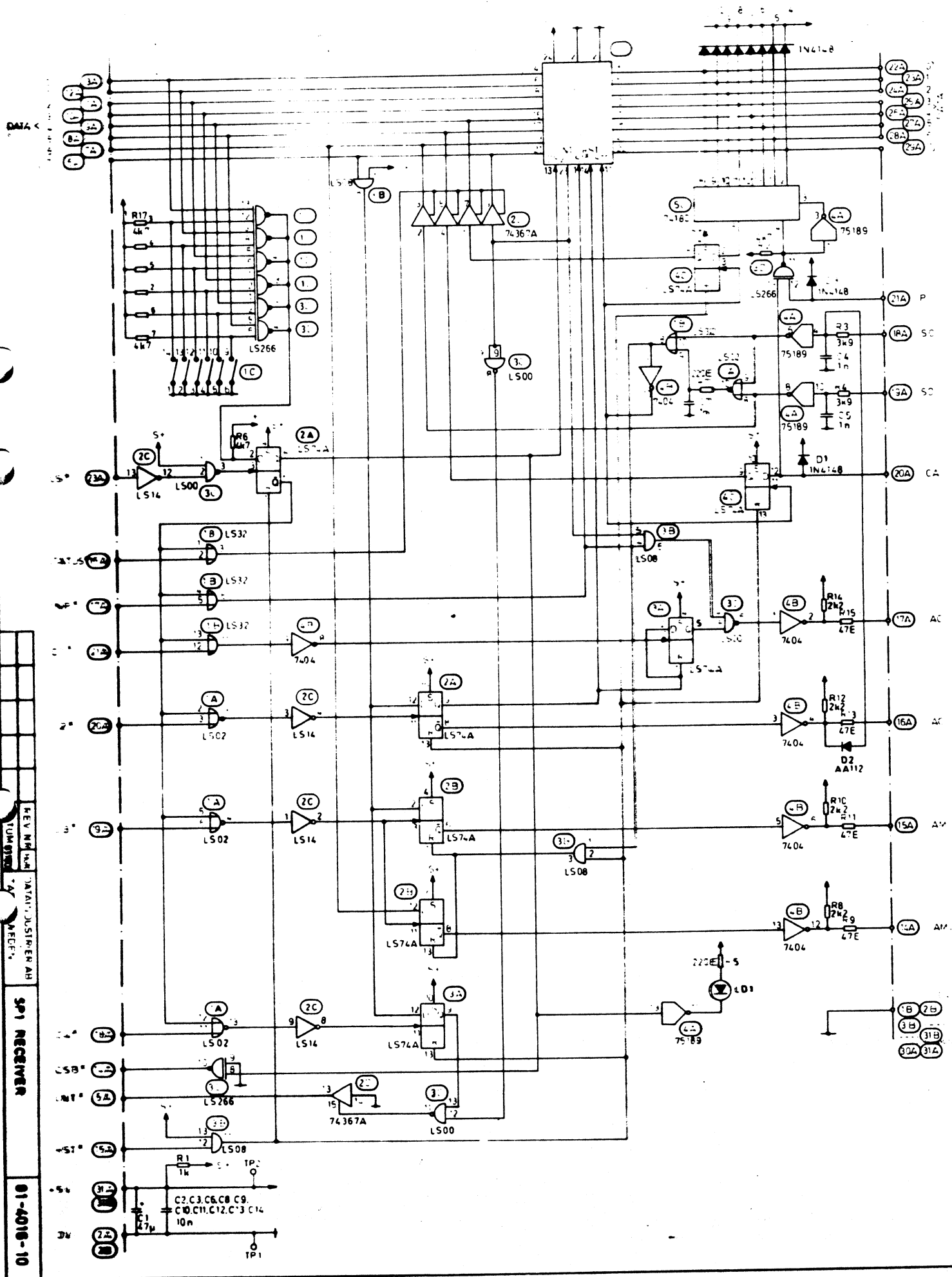


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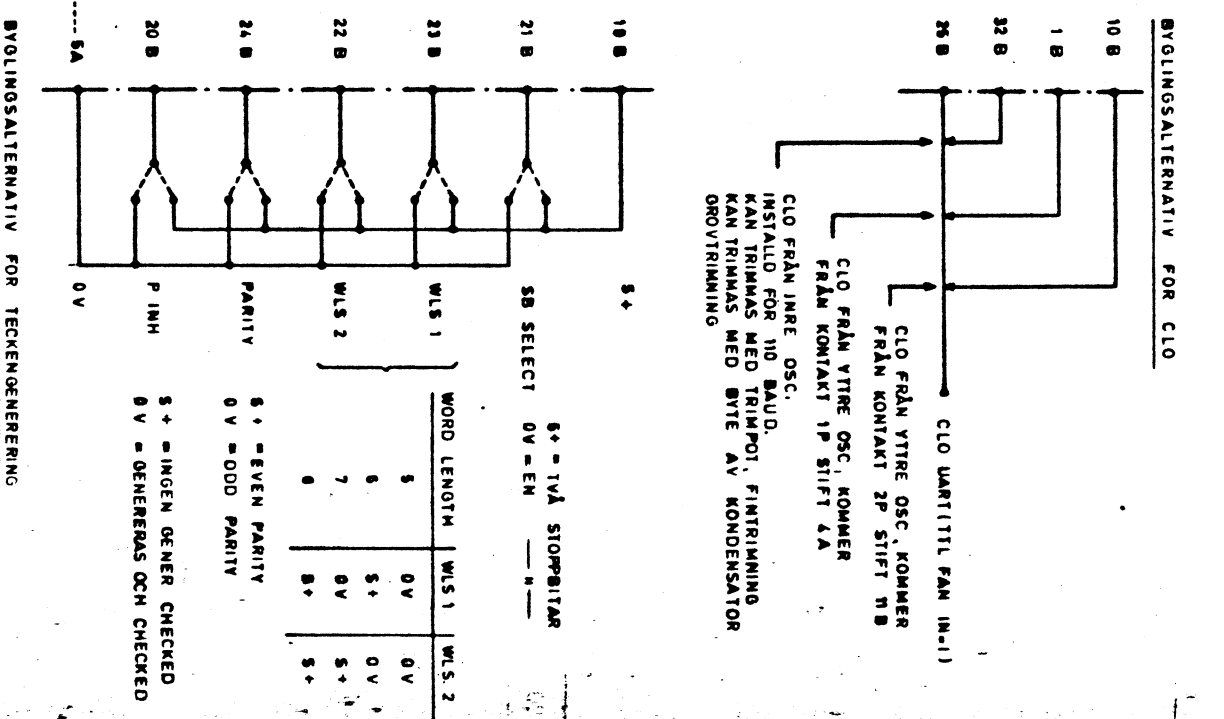
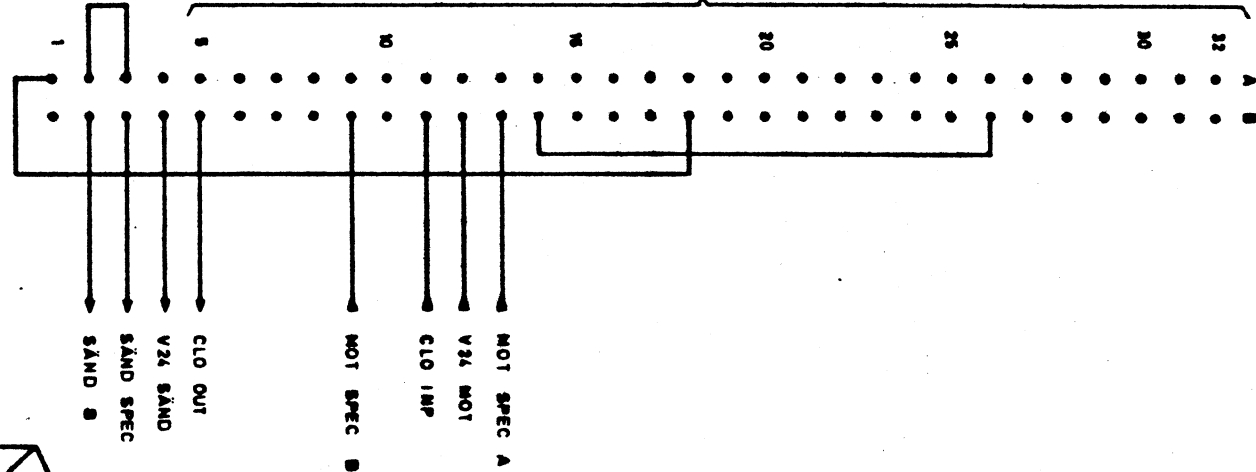
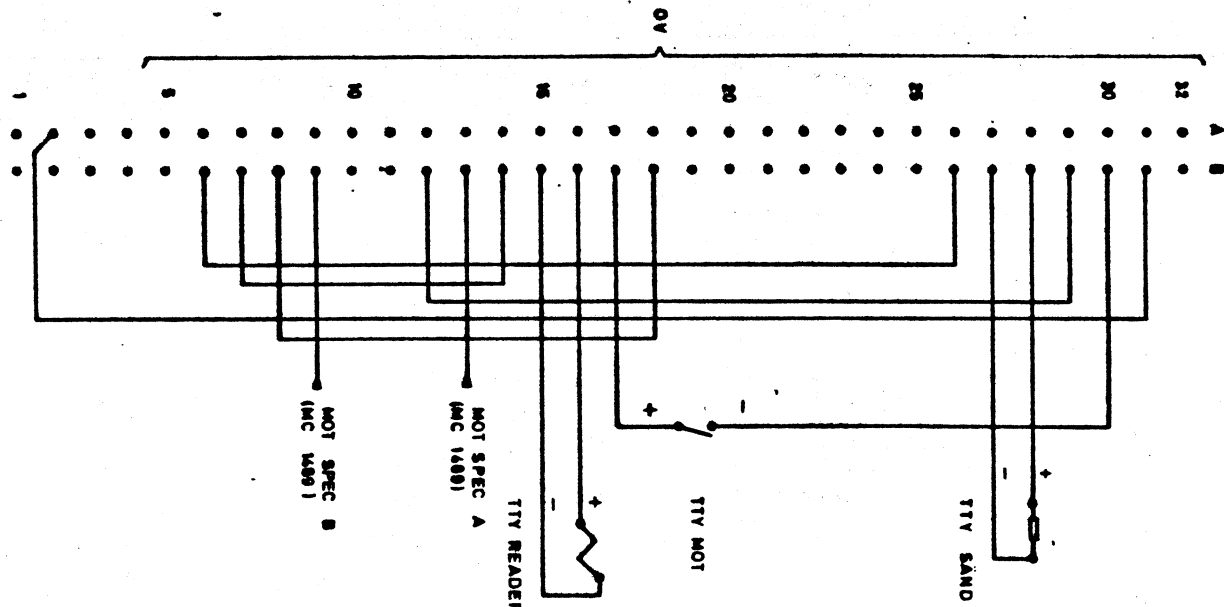




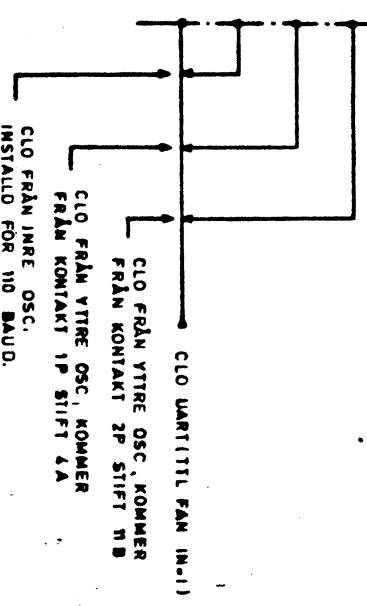








BYGLINGSALTERNATIV FÖR CLO



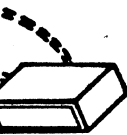
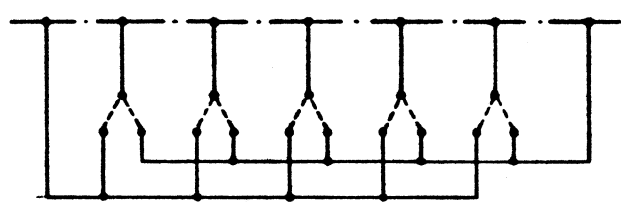
S+ = TVÅ STOPPBITAR
0V = EN — N —

WORD LENGTH	WLS 1	WLS 2
5	0V	0V
6	S+	0V
7	0V	S+
8	S+	S+

S+ = EVEN PARITY
0V = ODD PARITY

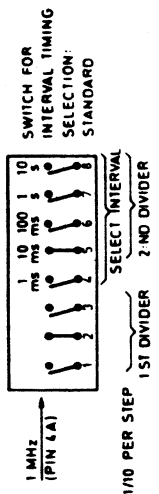
S+ = INGEN GENER CHECKED
0V = GENERERAS OCH CHECKED

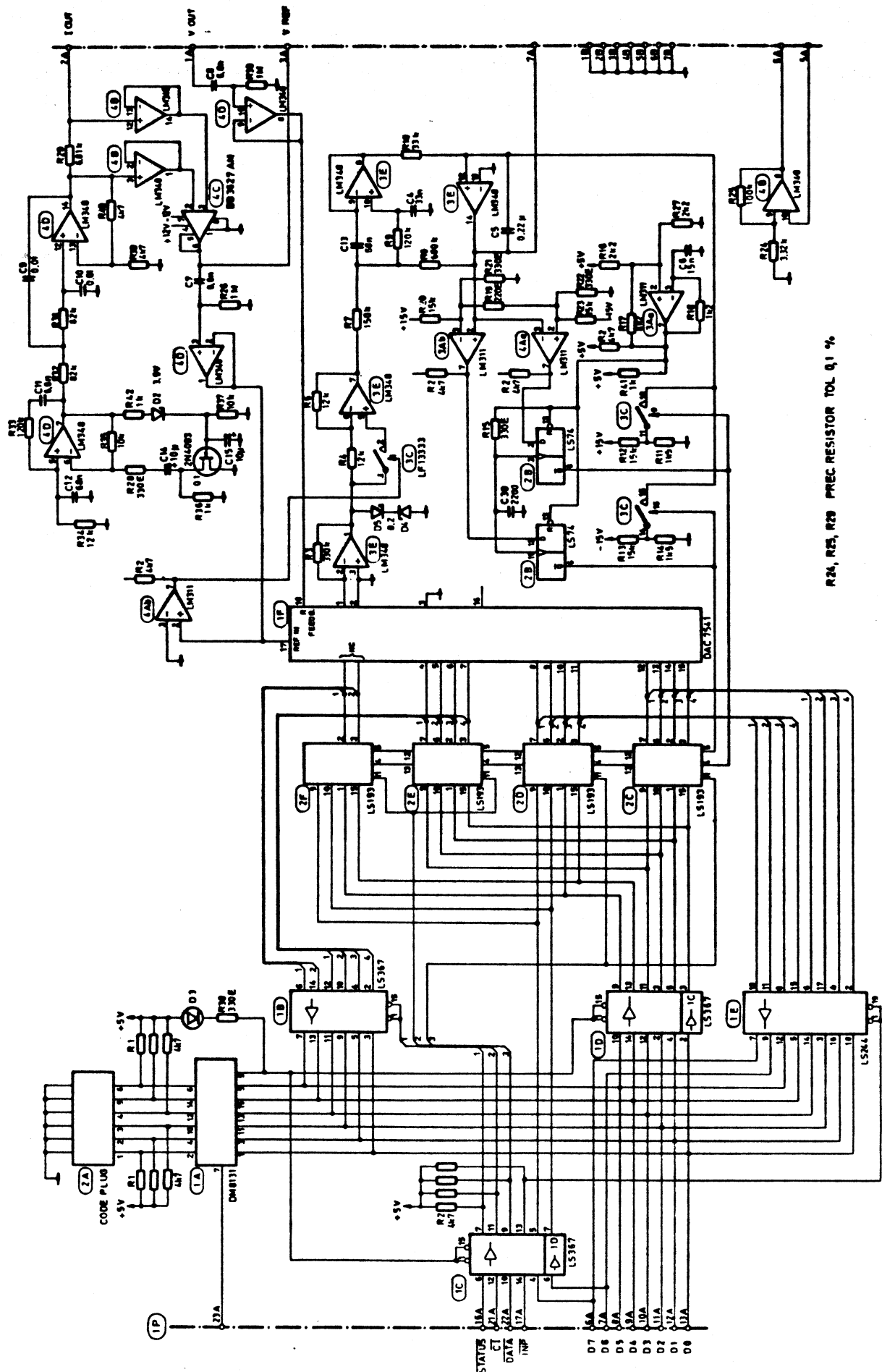
BYGLINGSALTERNATIV FÖR TECKENGENERERING



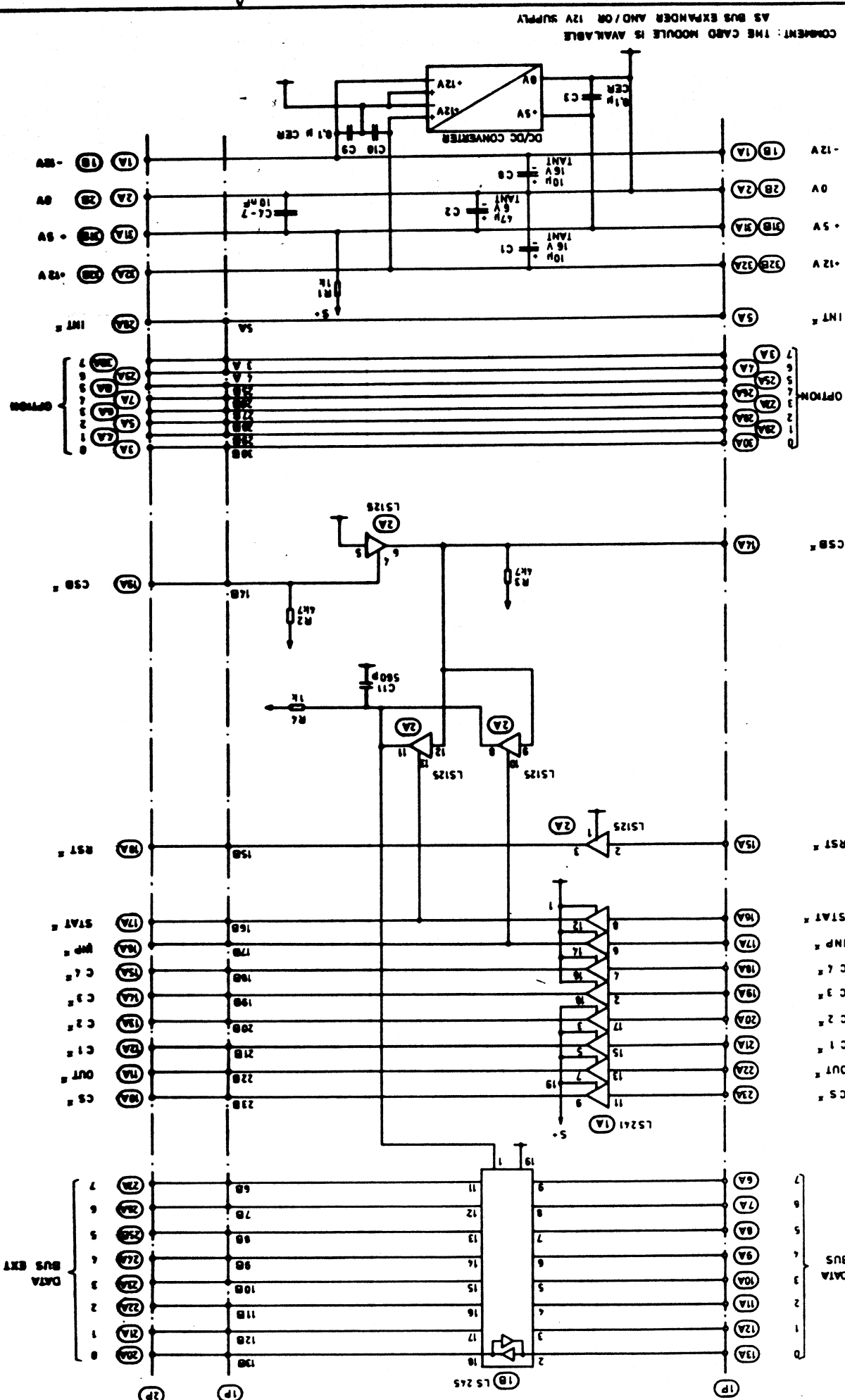
V24/RS 232

20 mA CURRENT LOOP



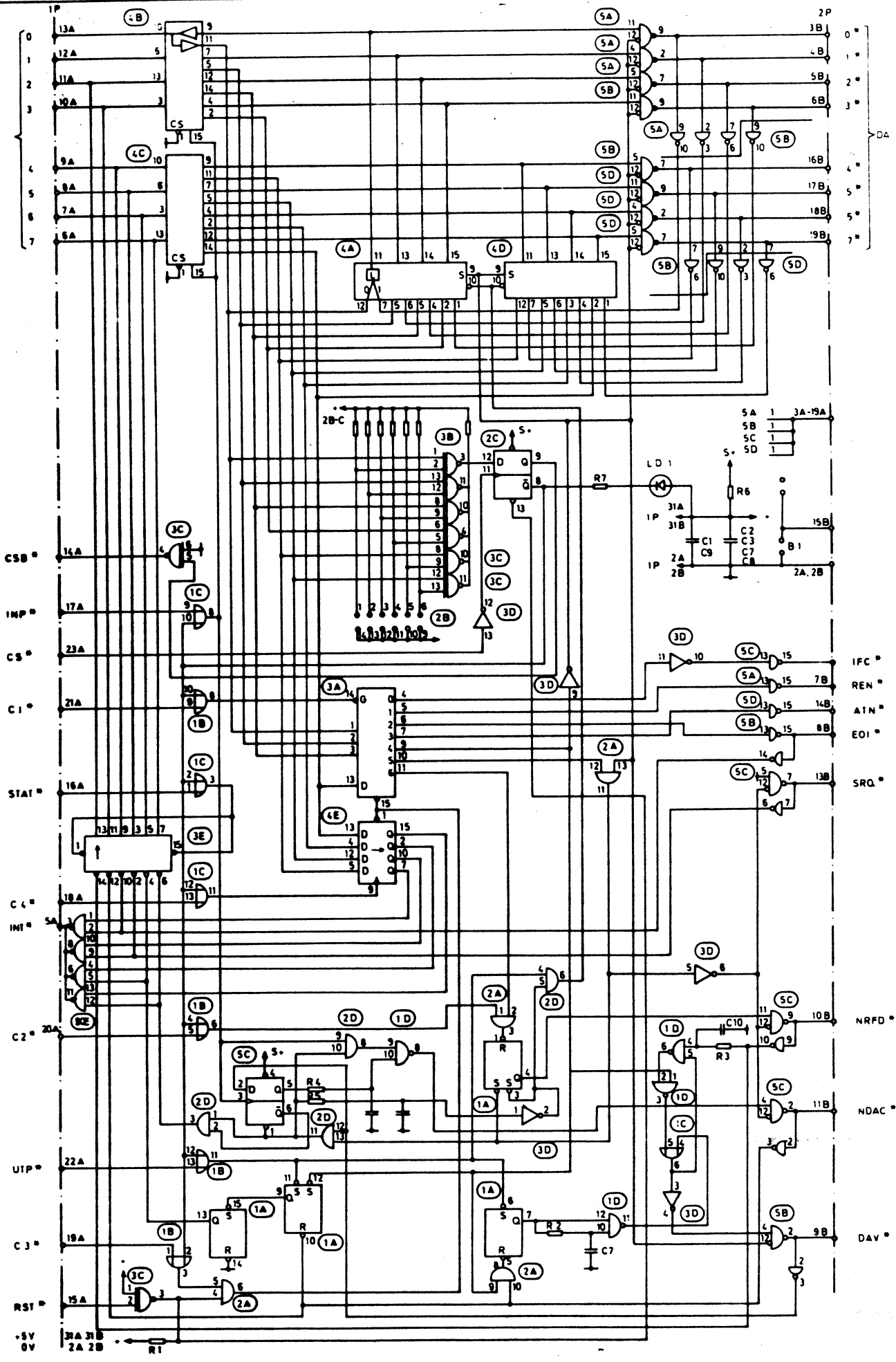


R24, R25, R26 PREC RESISTOR TOL 0.1 %



COMMENT: THE CARD MODULE IS AVAILABLE AS BUS EXPANDER AND/OR 12V SUPPLY

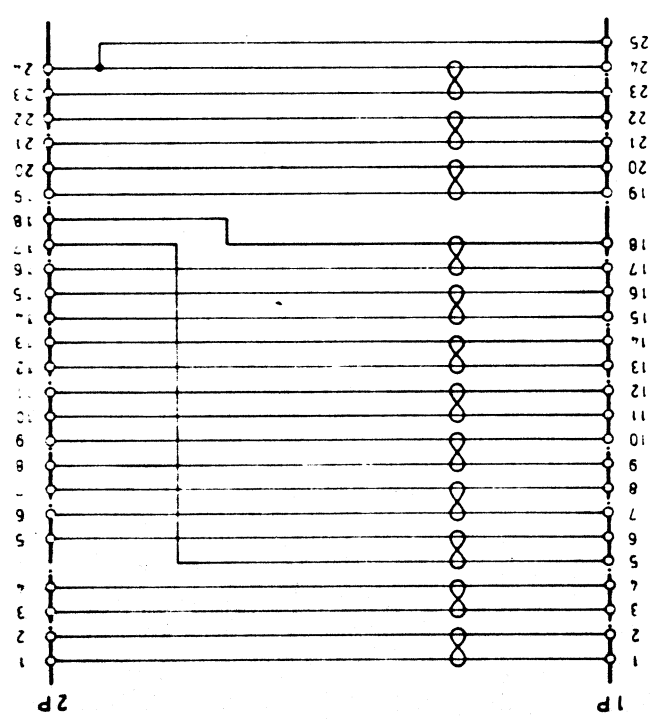
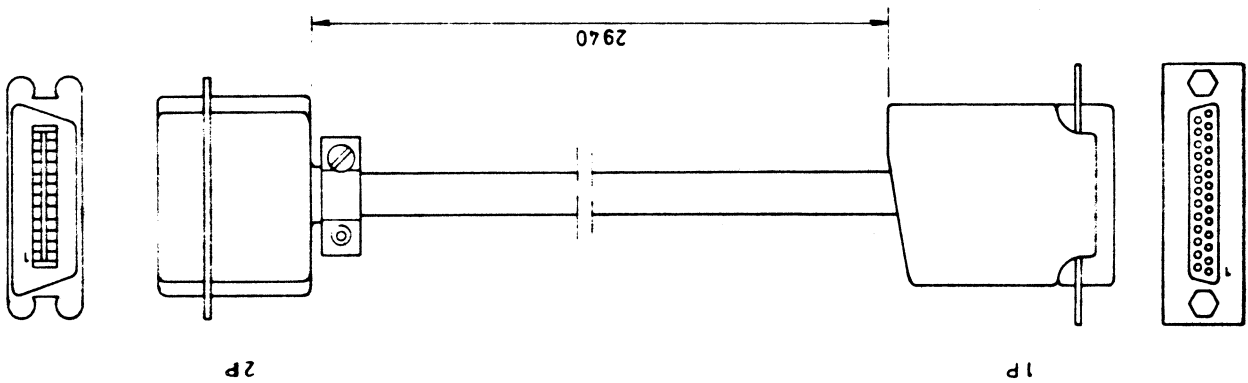
DATA BUS



DAIINDUSINER AB		IEC INTERFACE	
1875 SWEDEN		01-4028-00	

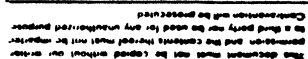
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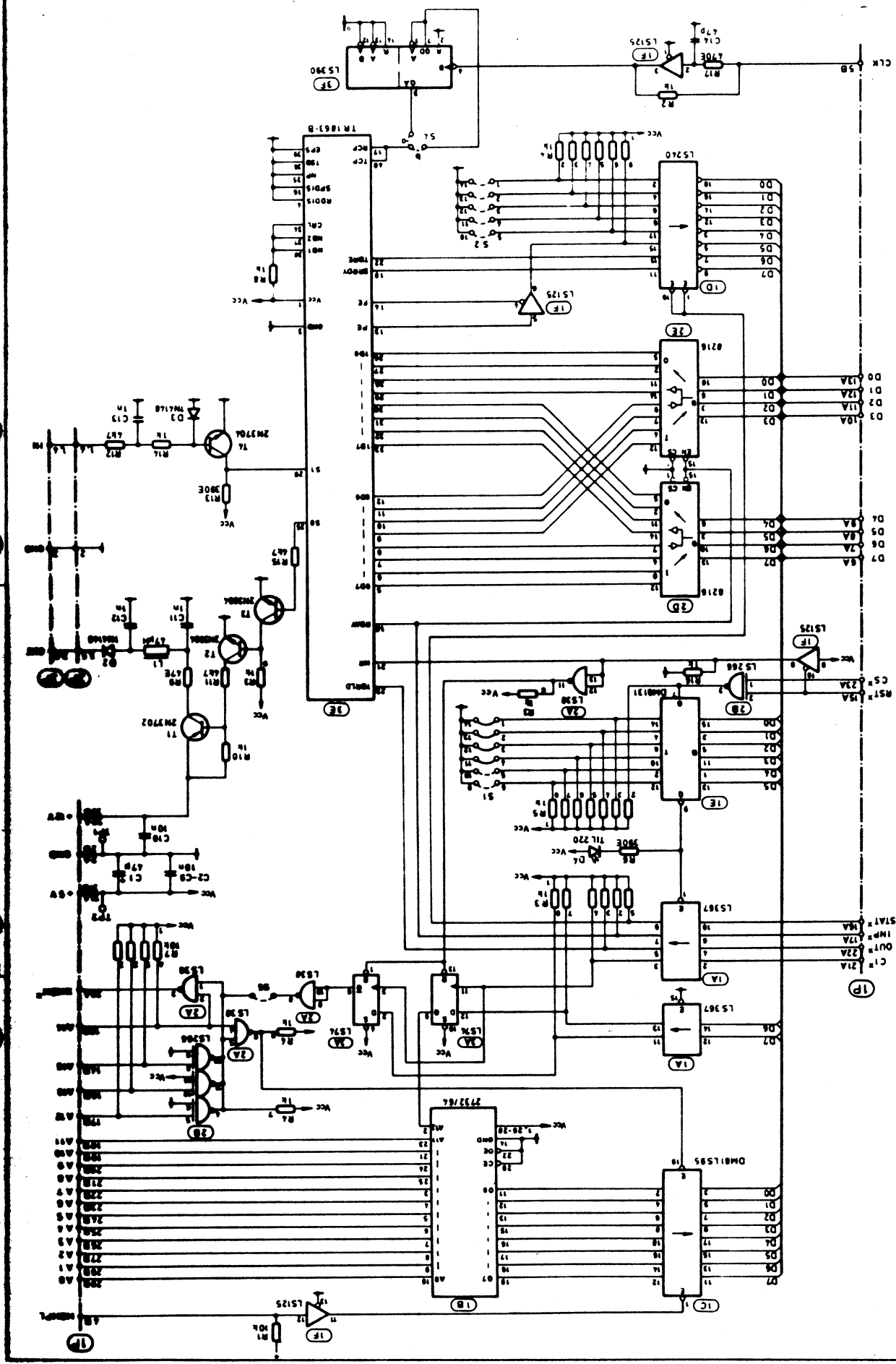
Pos	Pcs	Le...	A...	Name	Spec...
1P	1			Kabel	FKKX 15 * 2 * 0.22
2P	1			Skrusats	205980-1 AMP
	1			Köpo	205718-1 AMP
	1			Kontakt	Amphenol 57-30240
	1			Kontakt	DB 255 AMP
IEC 625 - IEEE 488					
81-6703-00					



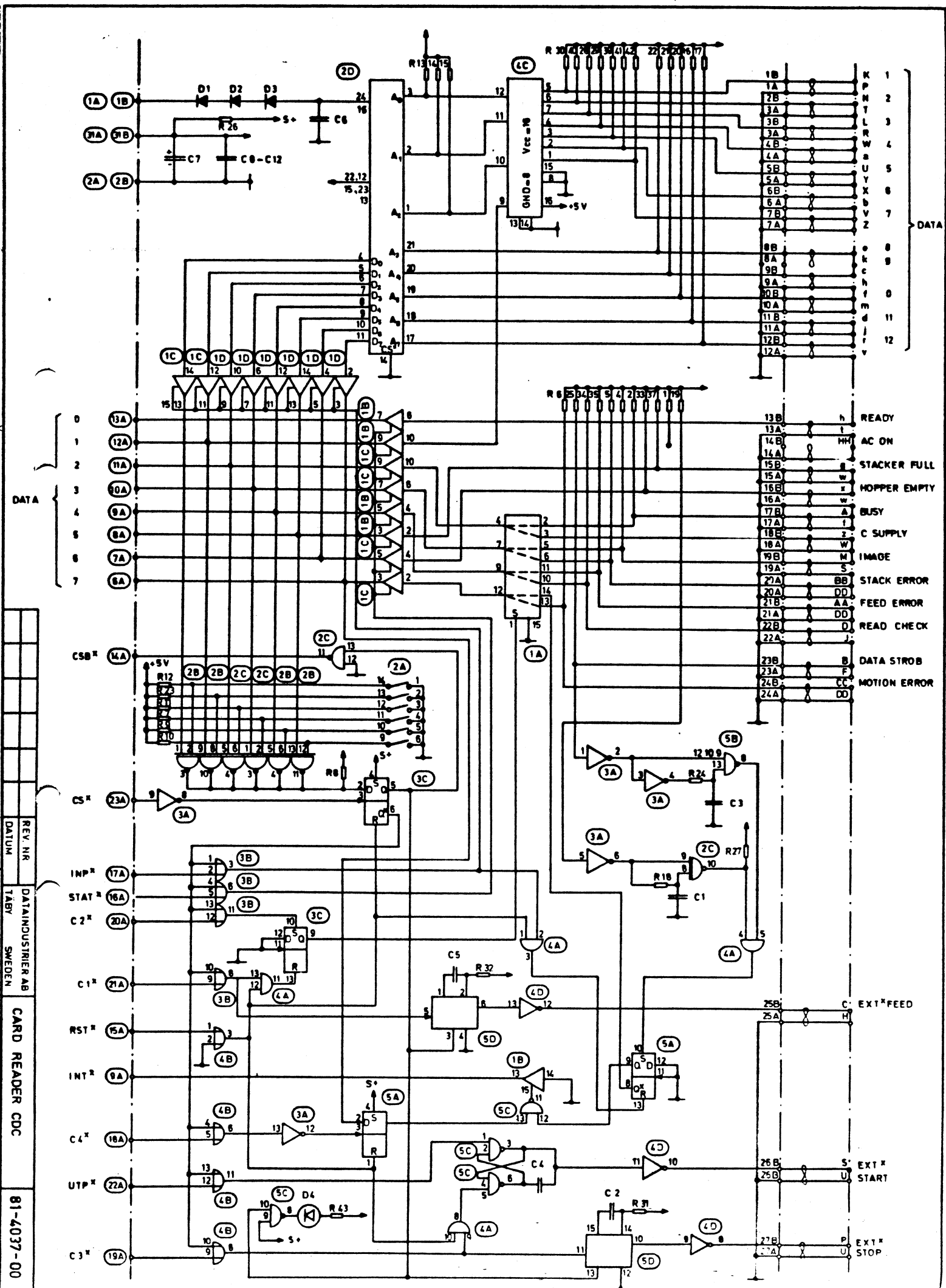
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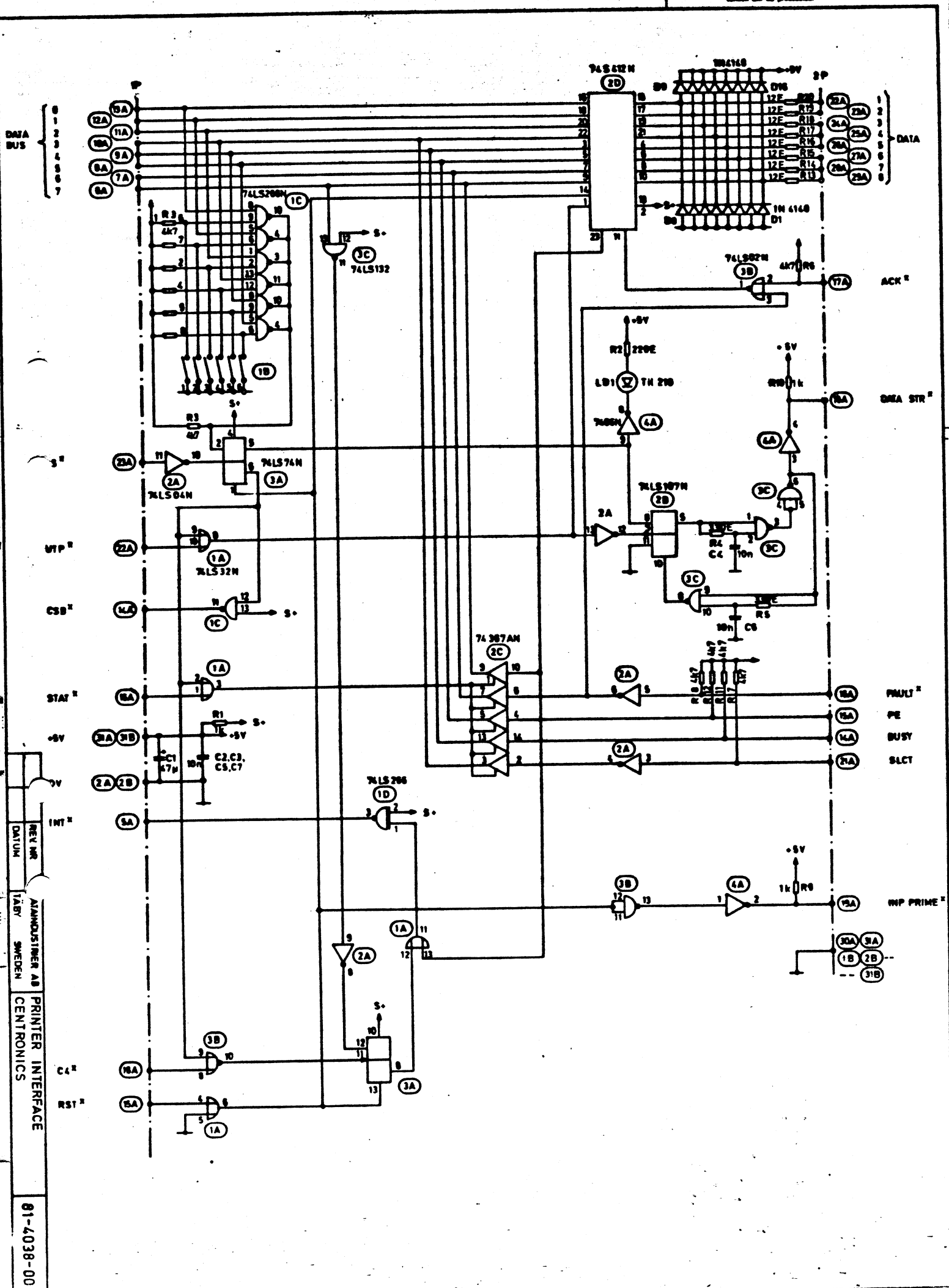






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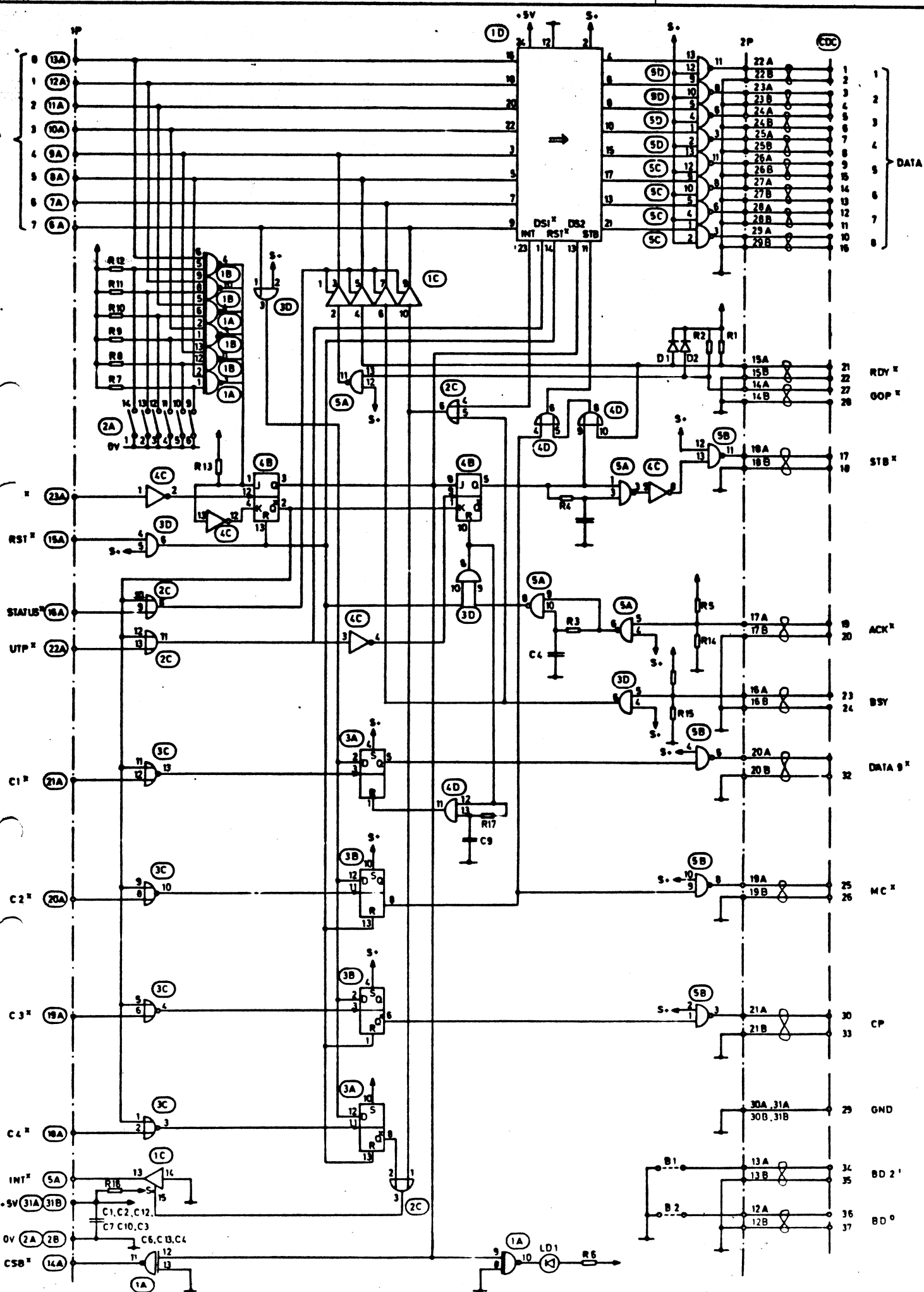
DATA BUS

REV. NR.

DAINDUSTRIERAB

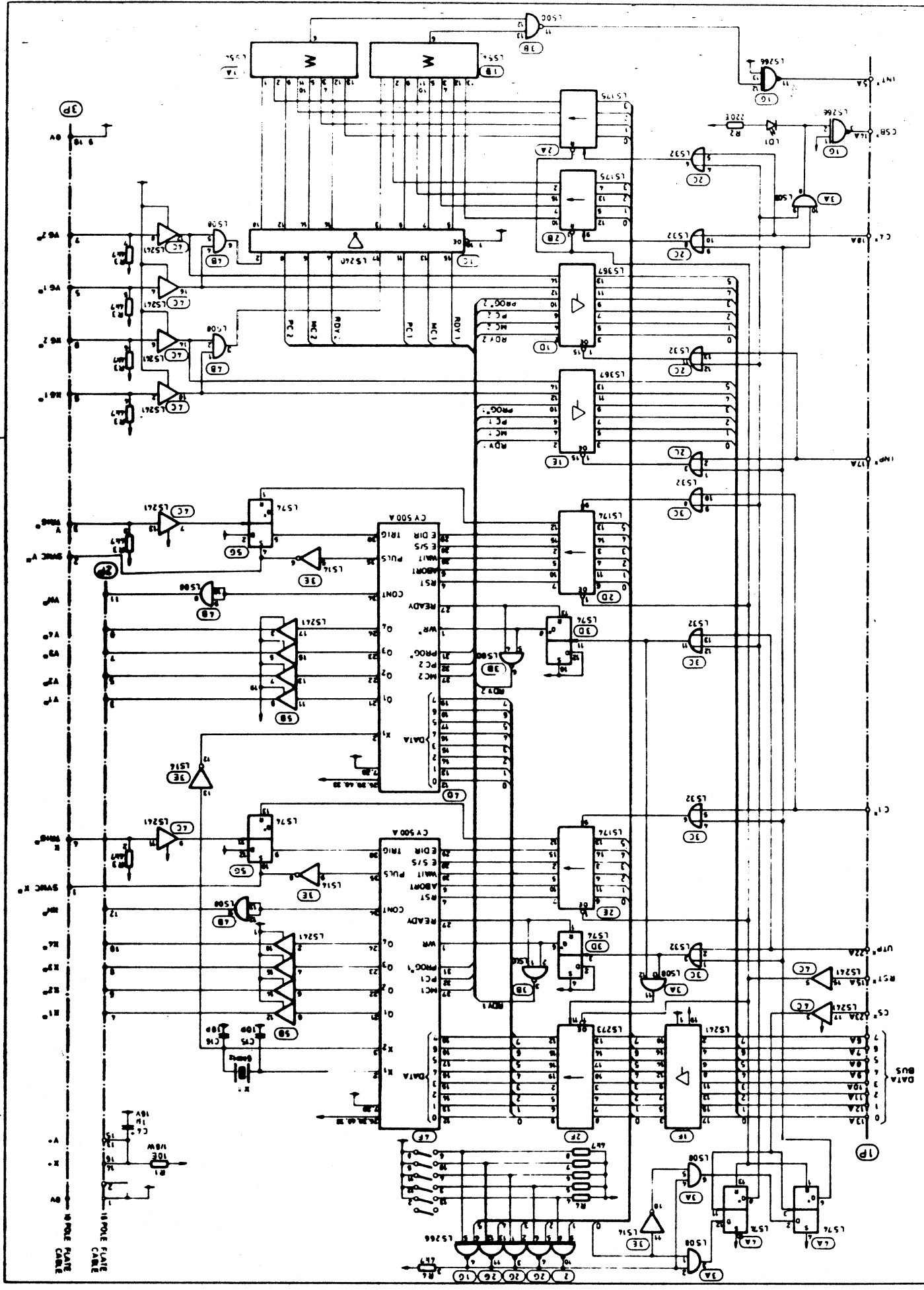
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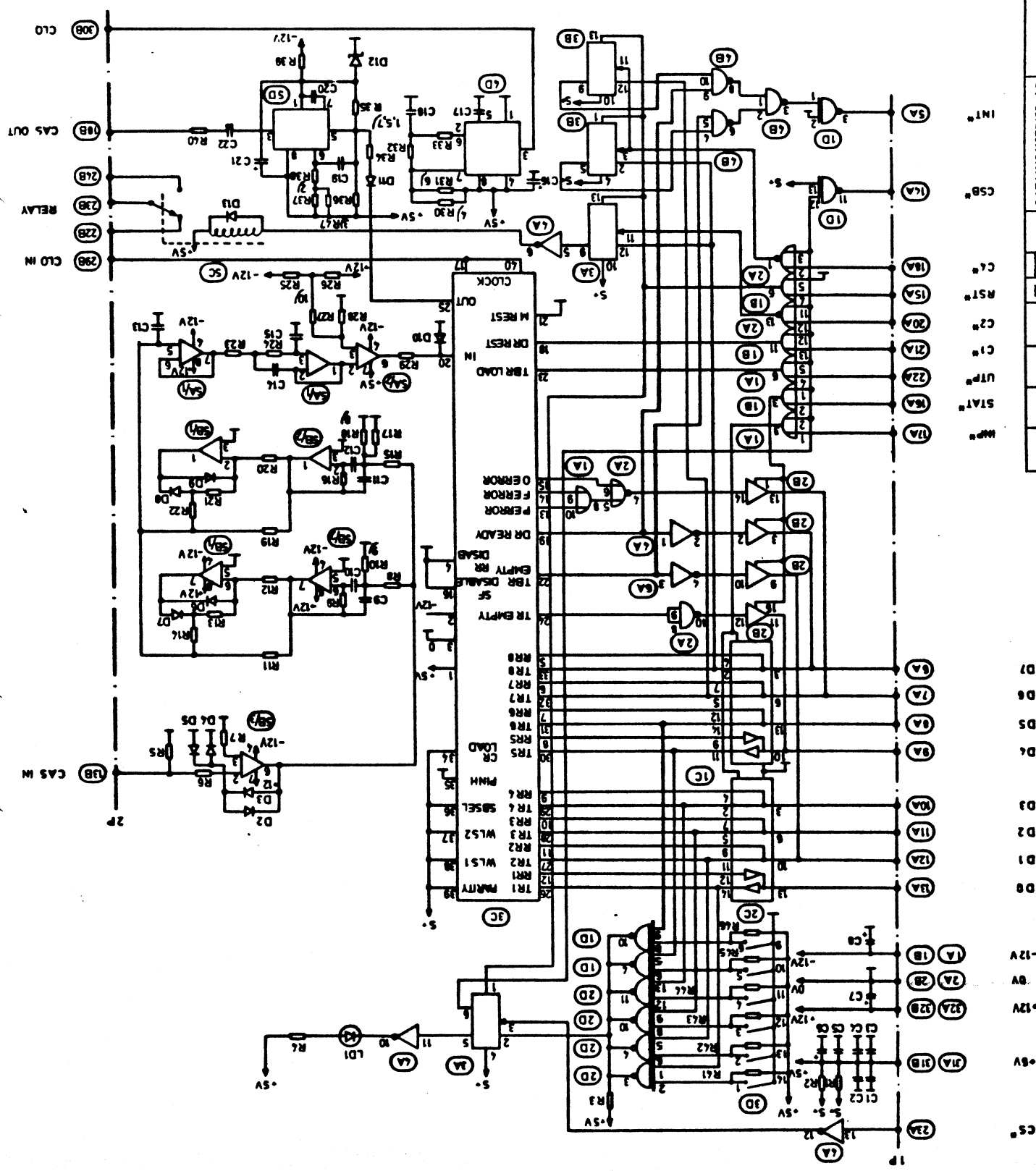
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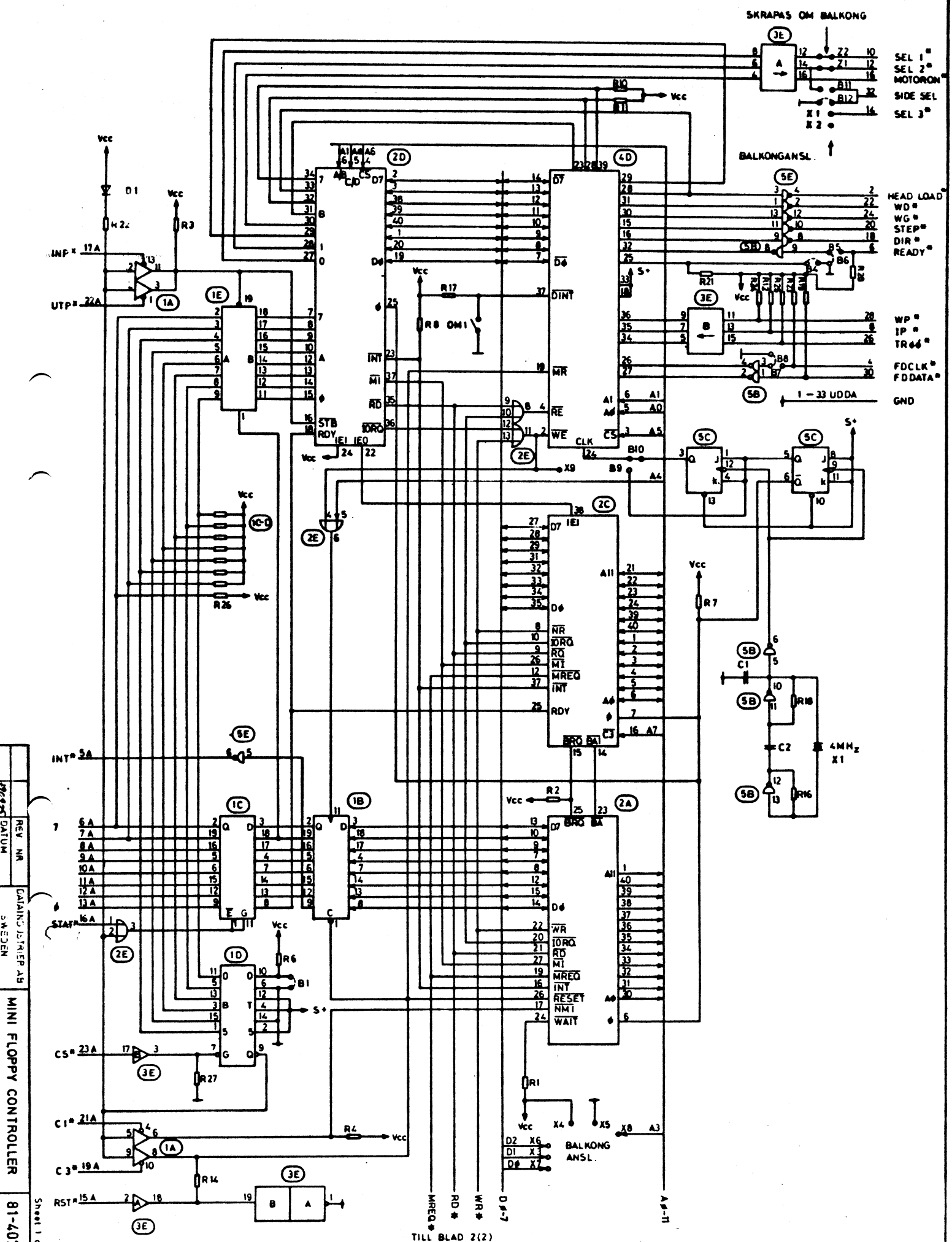
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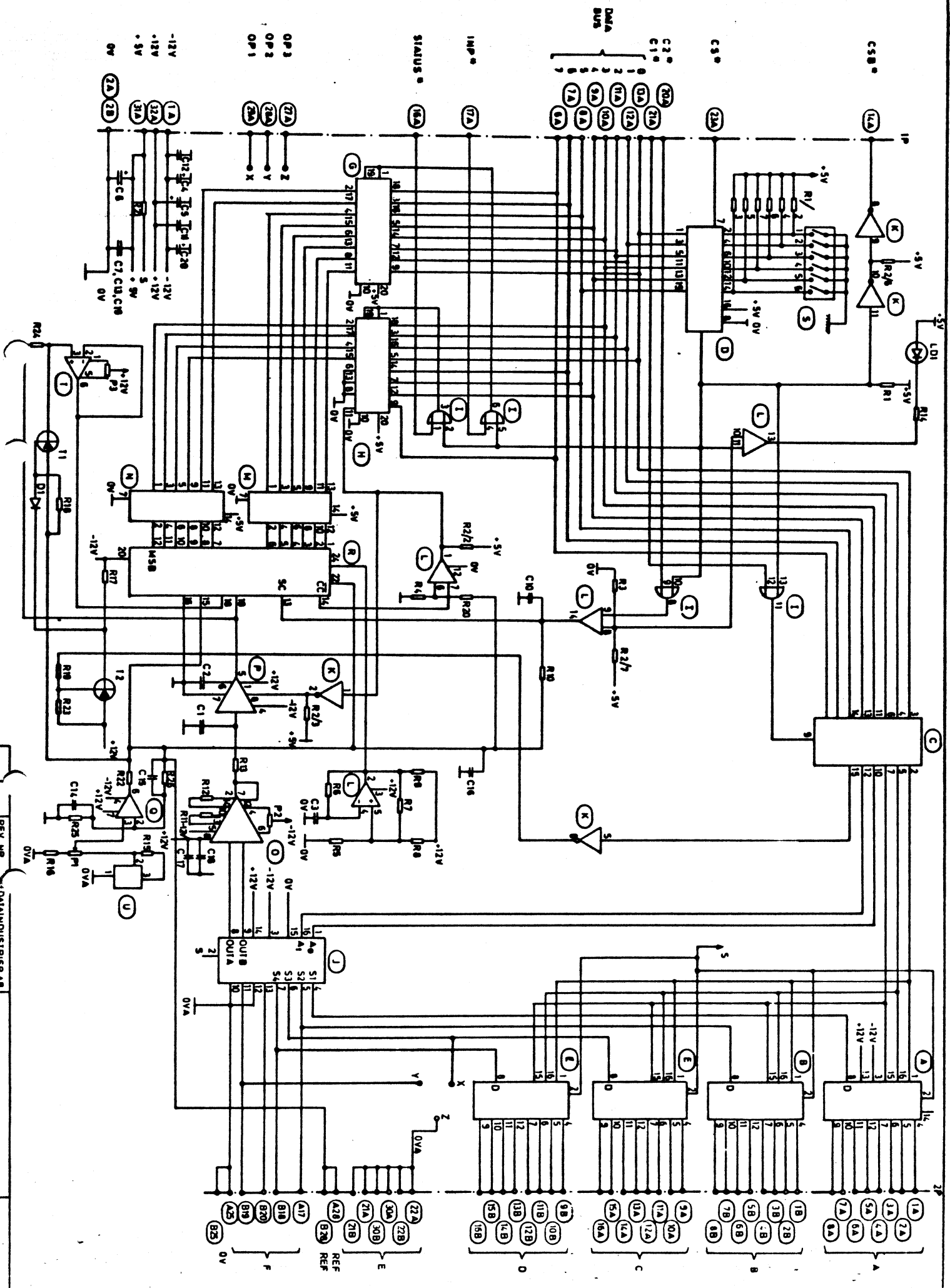
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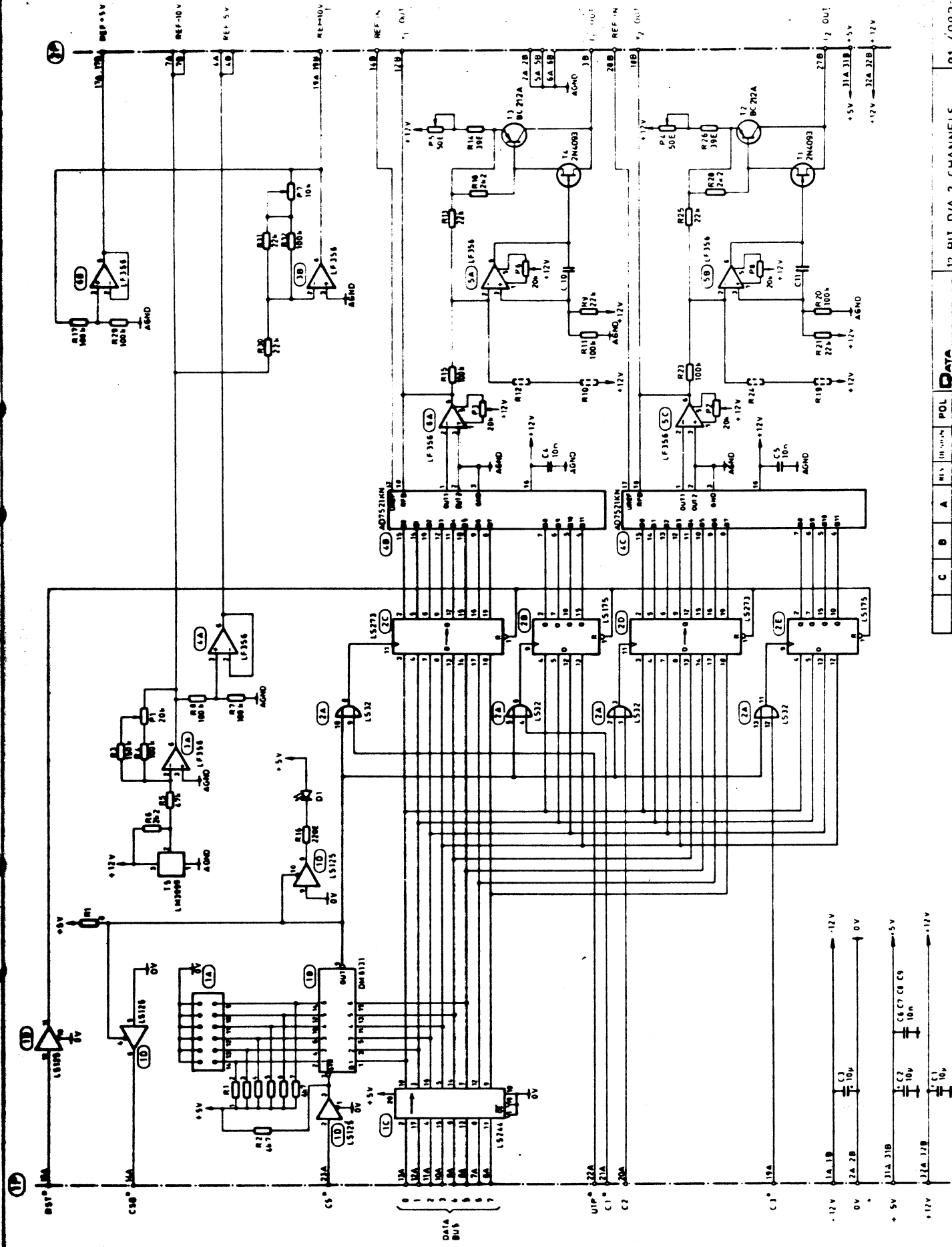




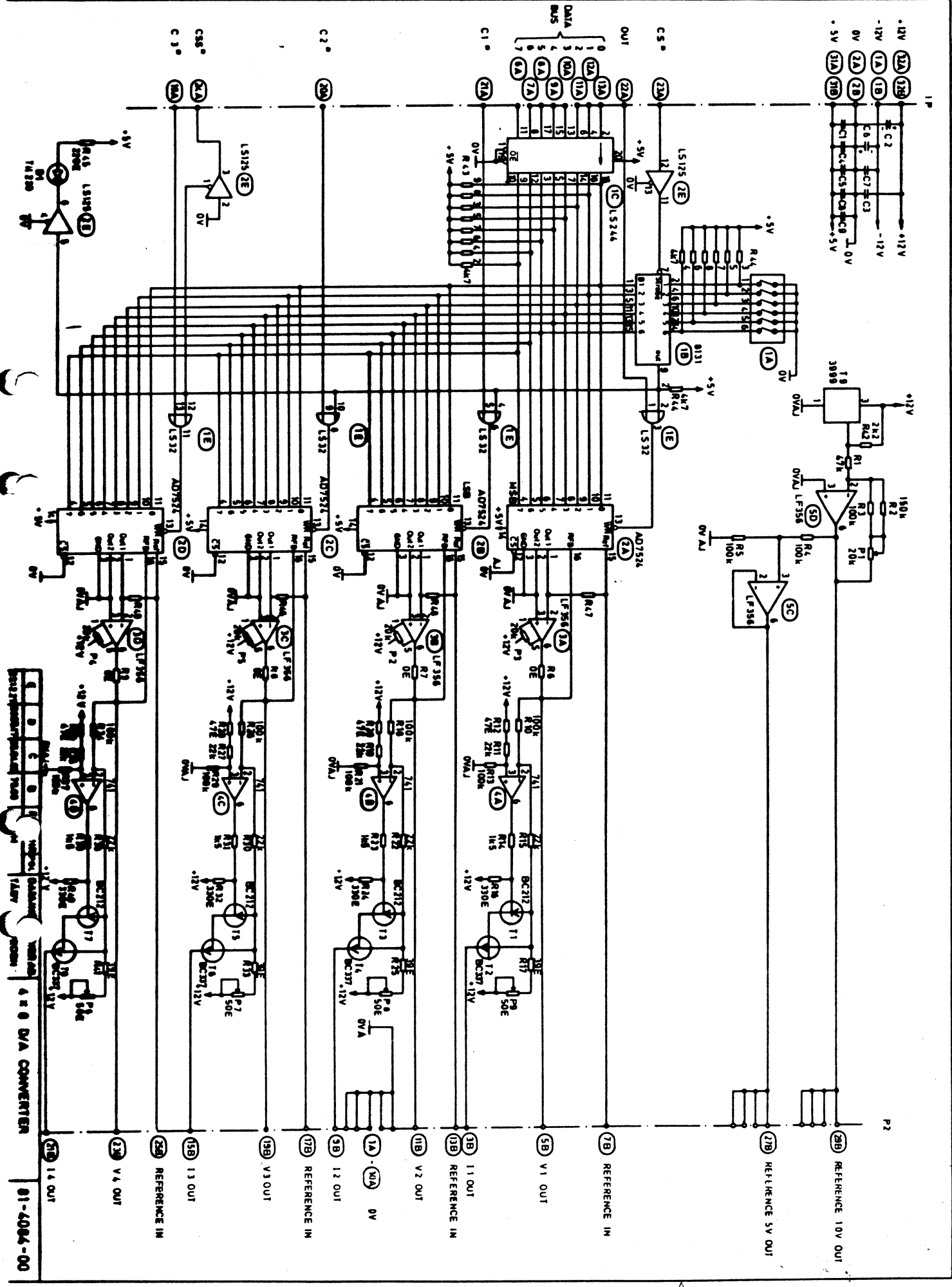
1 R 36 BESTÄMMER SPACE + 2,275 KHZ. DEN KAN ERSÄTJAS AV POTENTIOMETER. SE ÄVEN 3.
2 R 37 OCH R 38 BESTÄMMER MARK + 2,125 KHZ. R 37 KAN ERSÄTJAS AV POTENTIOMETER. R 38 KAN ERSÄTJAS AV BYGEL. SE ÄVEN 3.
3 R 47 ÄR PARALLELL MÖJISÄND TILL R 37. KAN ERSÄTJAS AV BYGEL.
4 R 30 OCH R 31 KAN ERSÄTJAS AV POTENTIOMETER.
5 PLATS FÖR PARALLELL MÖJISÄND TILL R 34.
6 BYGEL ELLER SERIEMÖJISÄND FÖR R 31.
7 BYGEL ELLER SERIEMÖJISÄND FÖR R 34.
8 R 18, VÄRD FÖR INK. MARK + 2,125 KHZ. KAN ERSÄTJAS AV POTENTIOMETER (MÖJISÄND R 17).
9 R 10, VÄRD FÖR INK. MARK + 2,975 KHZ. KAN ERSÄTJAS AV POTENTIOMETER (MÖJISÄND R 10).
10 R 25 OCH R 26 BESTÄMMER FÖR OFFSET. KAN ERSÄTJAS AV POTENTIOMETER (MÖJISÄND R 25/26).



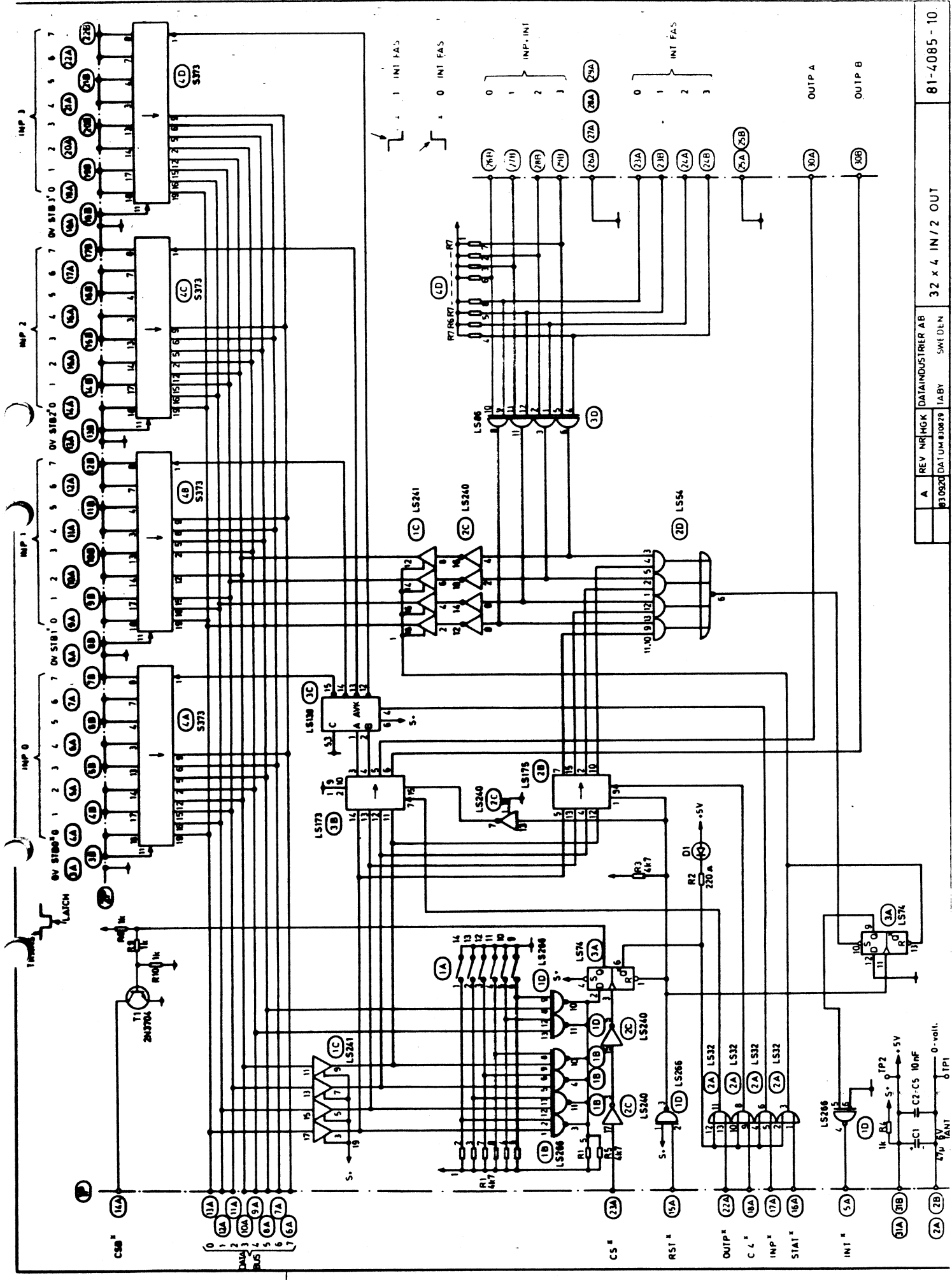


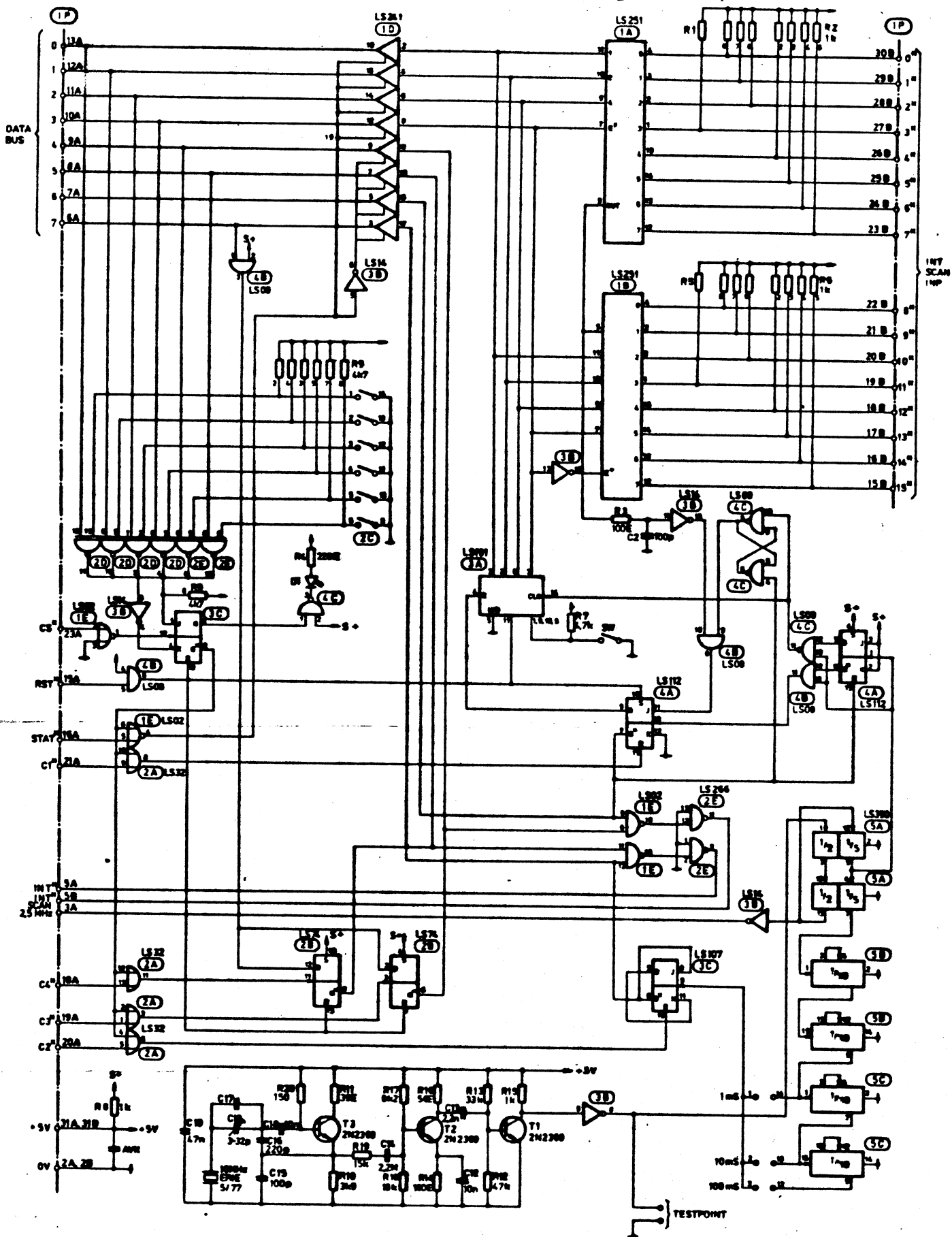


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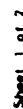






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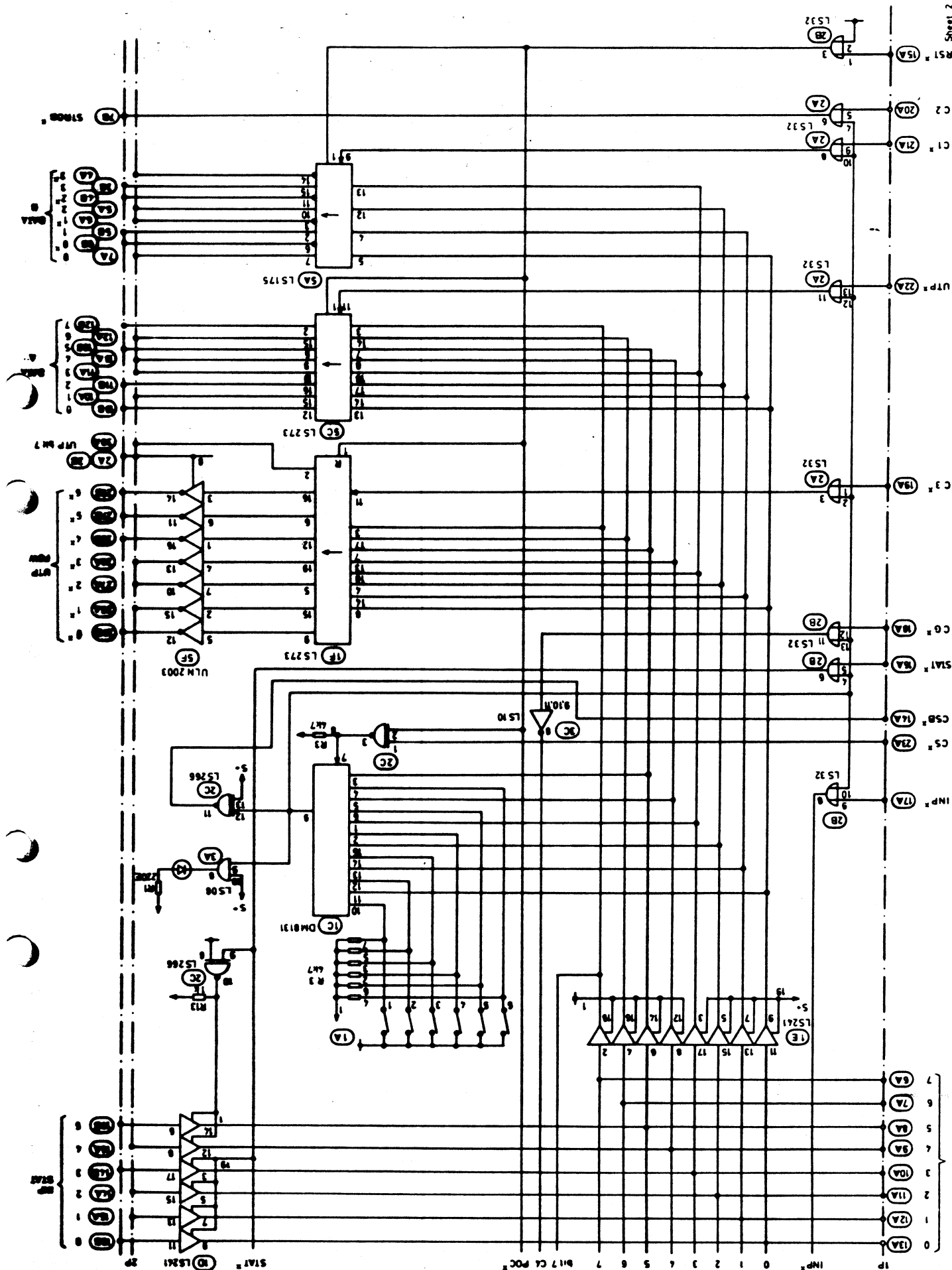


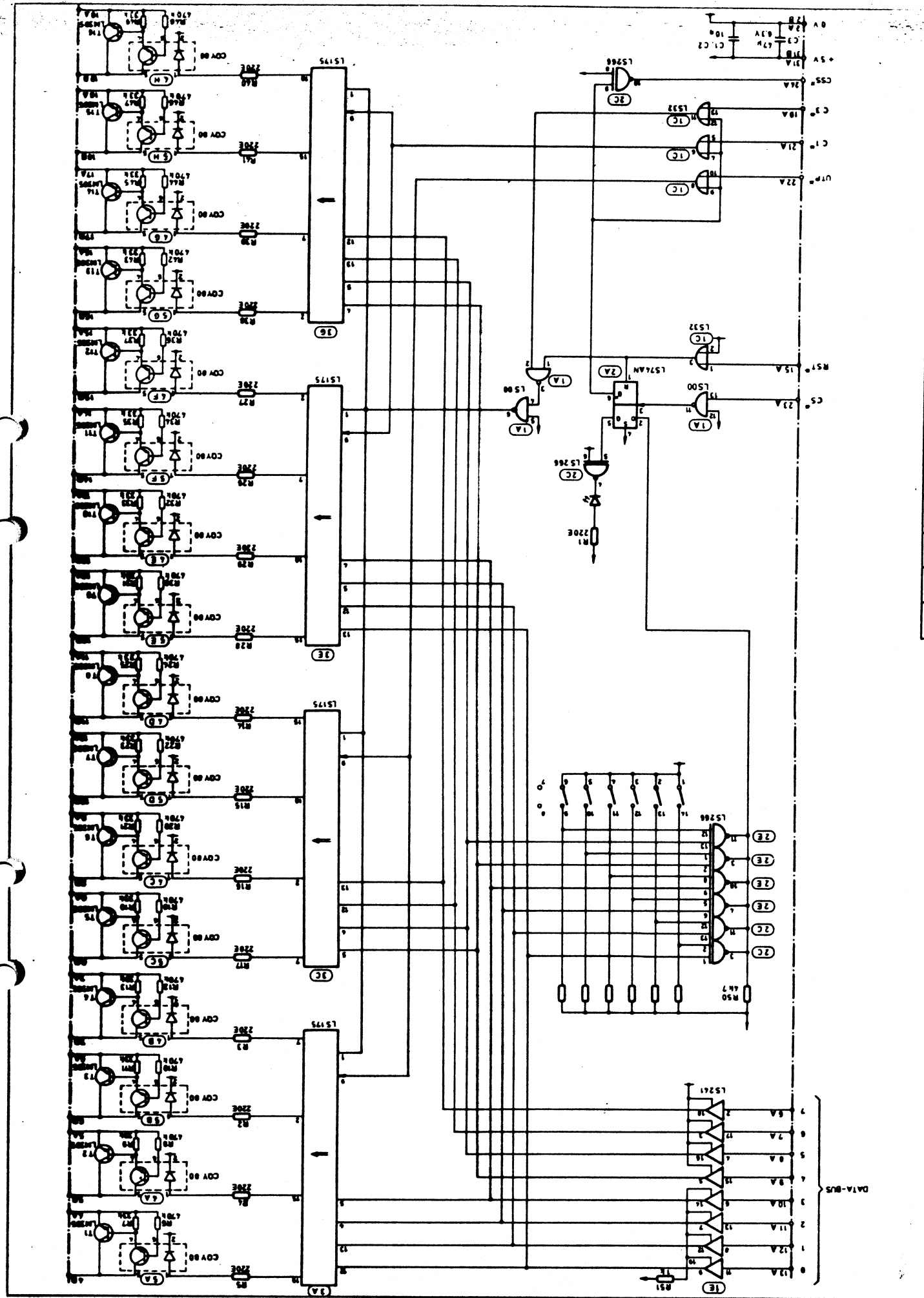


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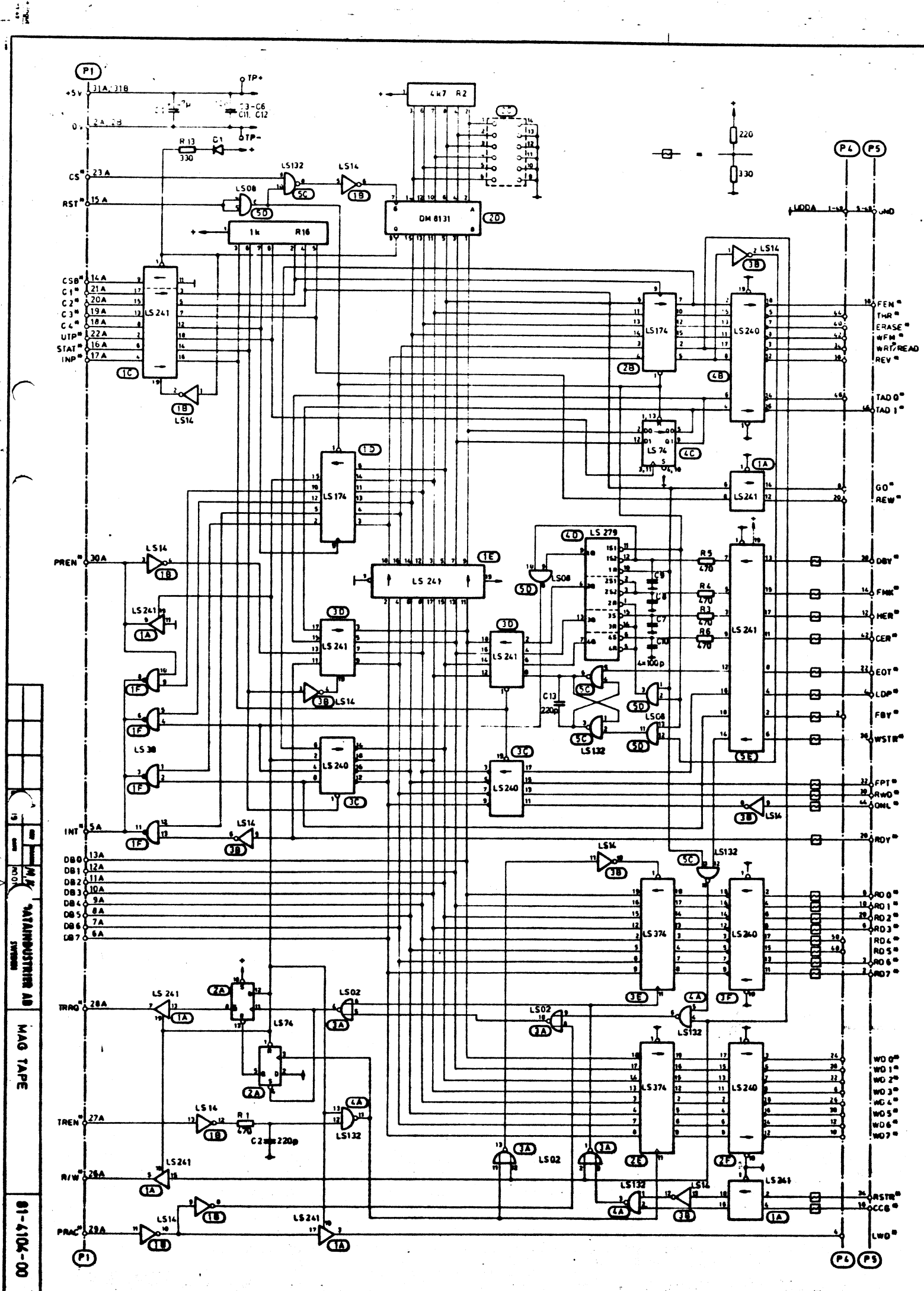
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TÄBY SWEDEN

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031102	00720	00521	DATUM



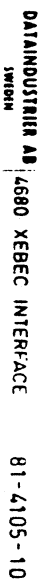


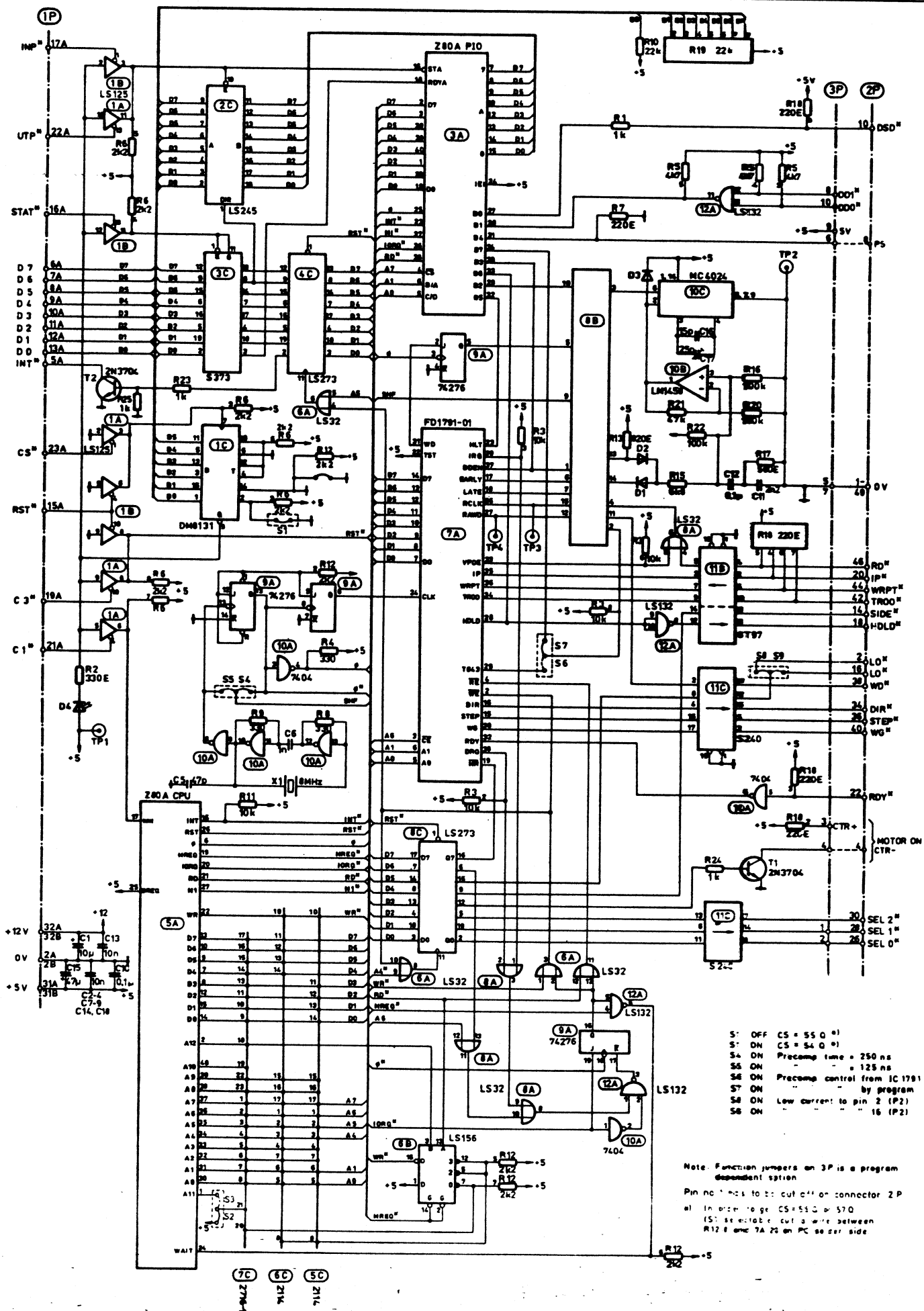




19
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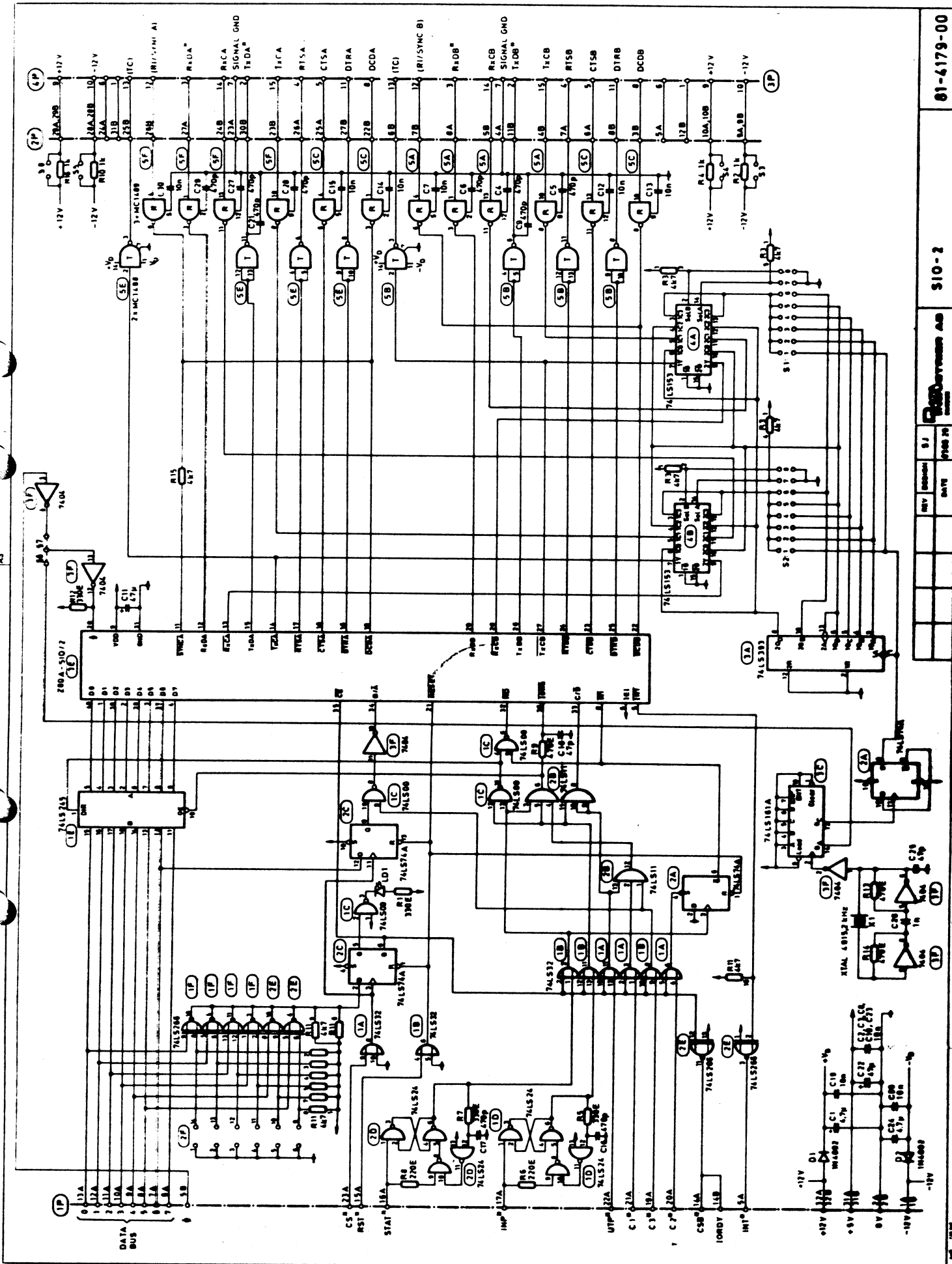
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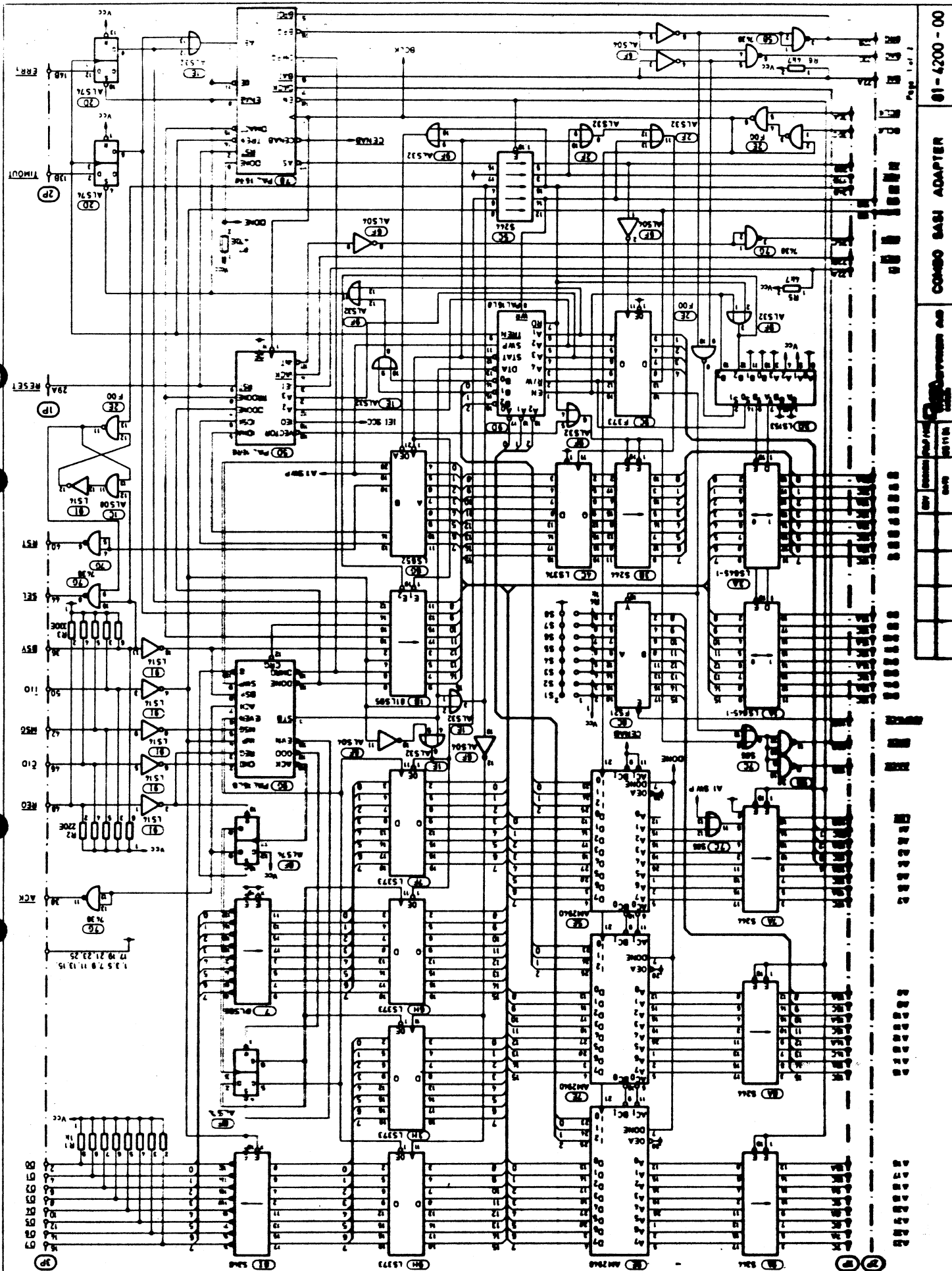




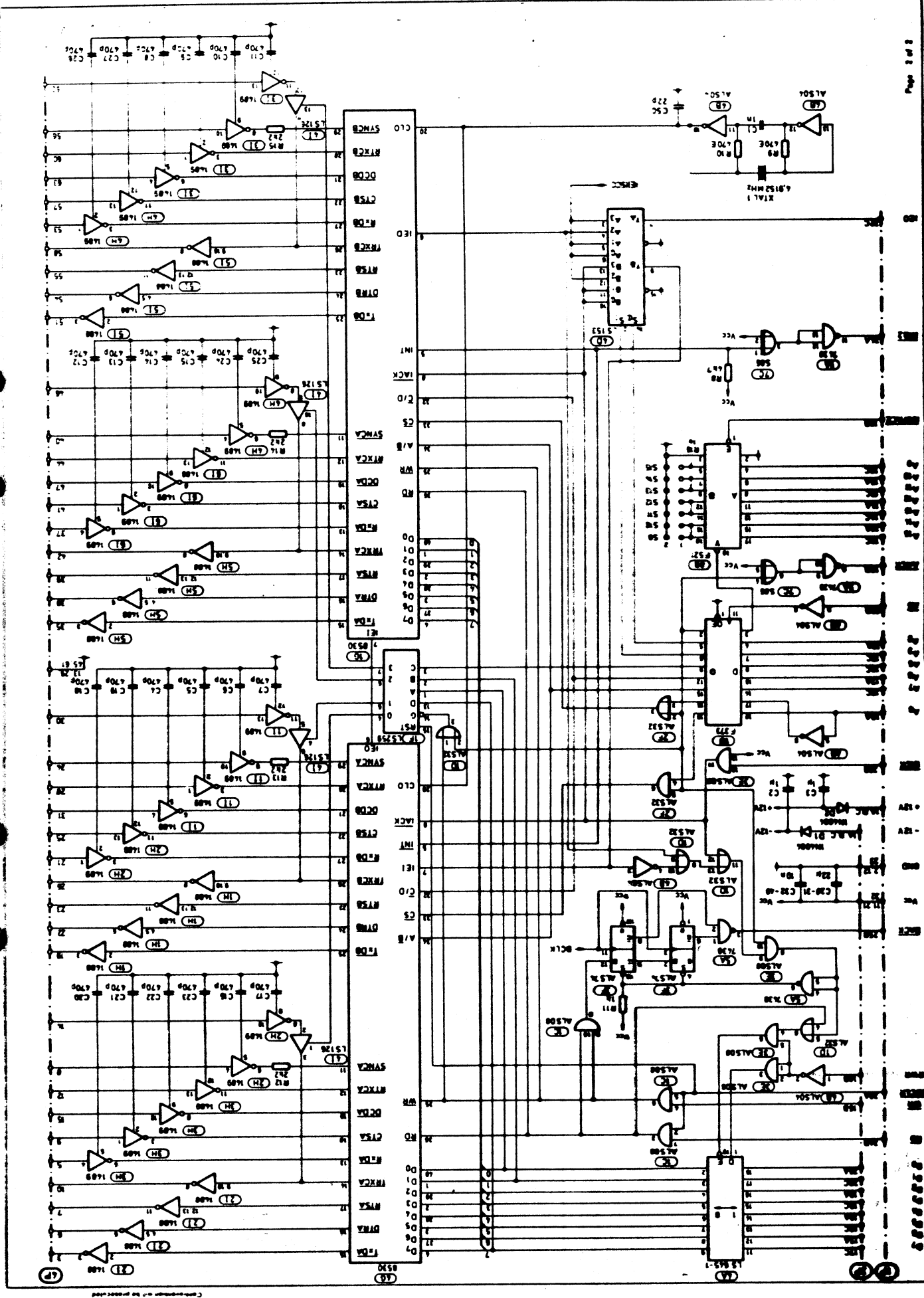


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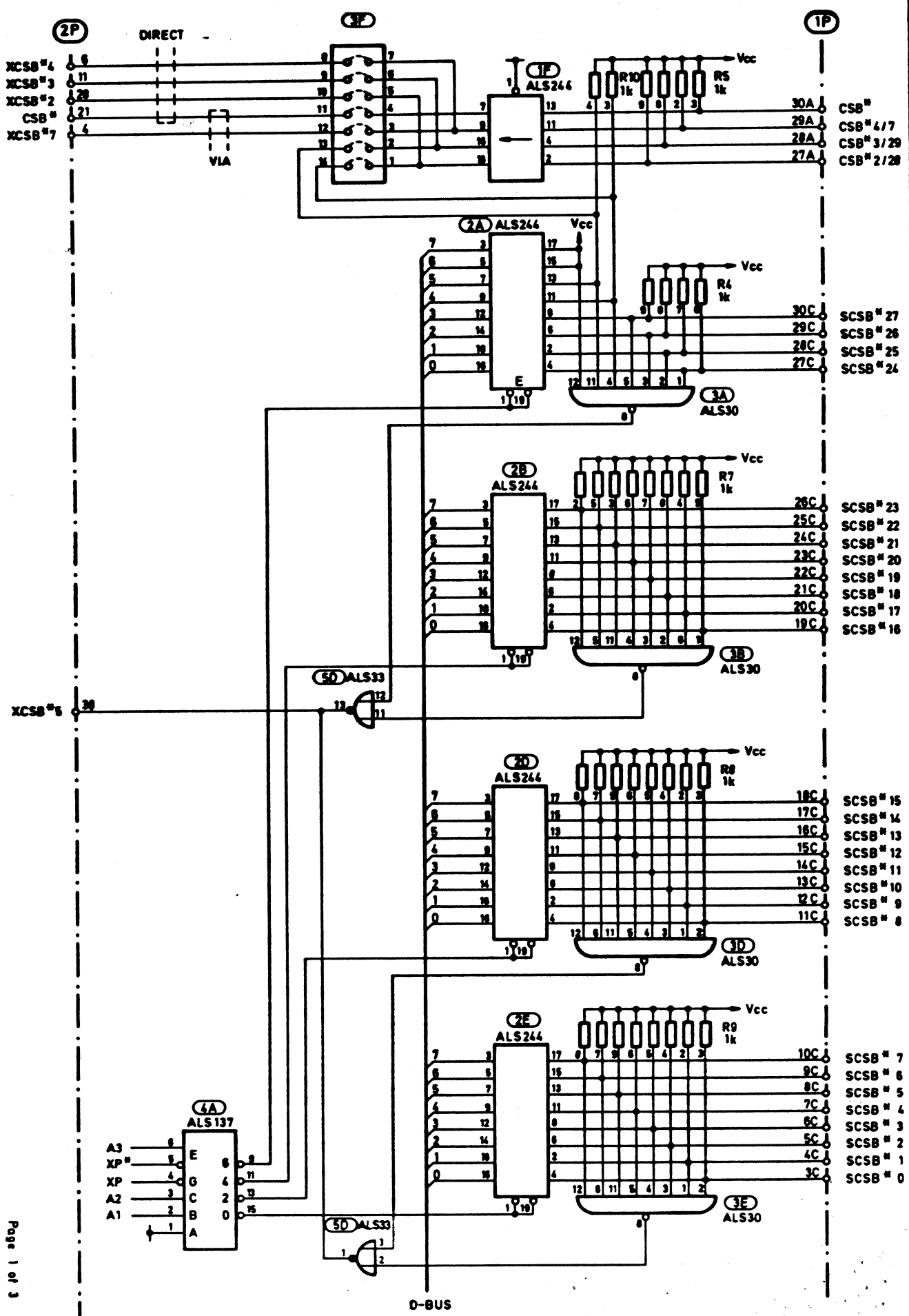


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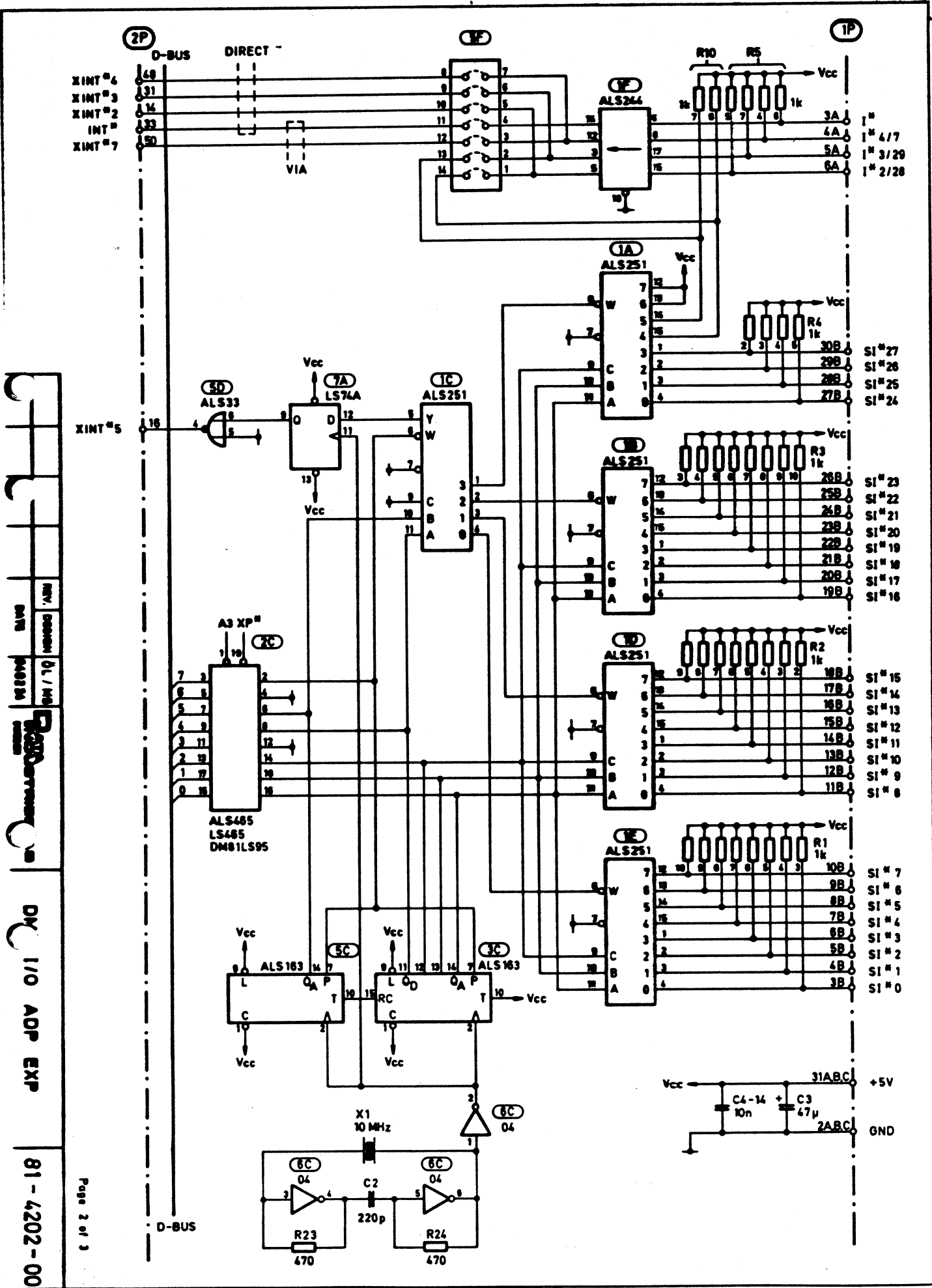
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REV. DESIGN Dk / MS DATE 04/02/24 81-4202-00

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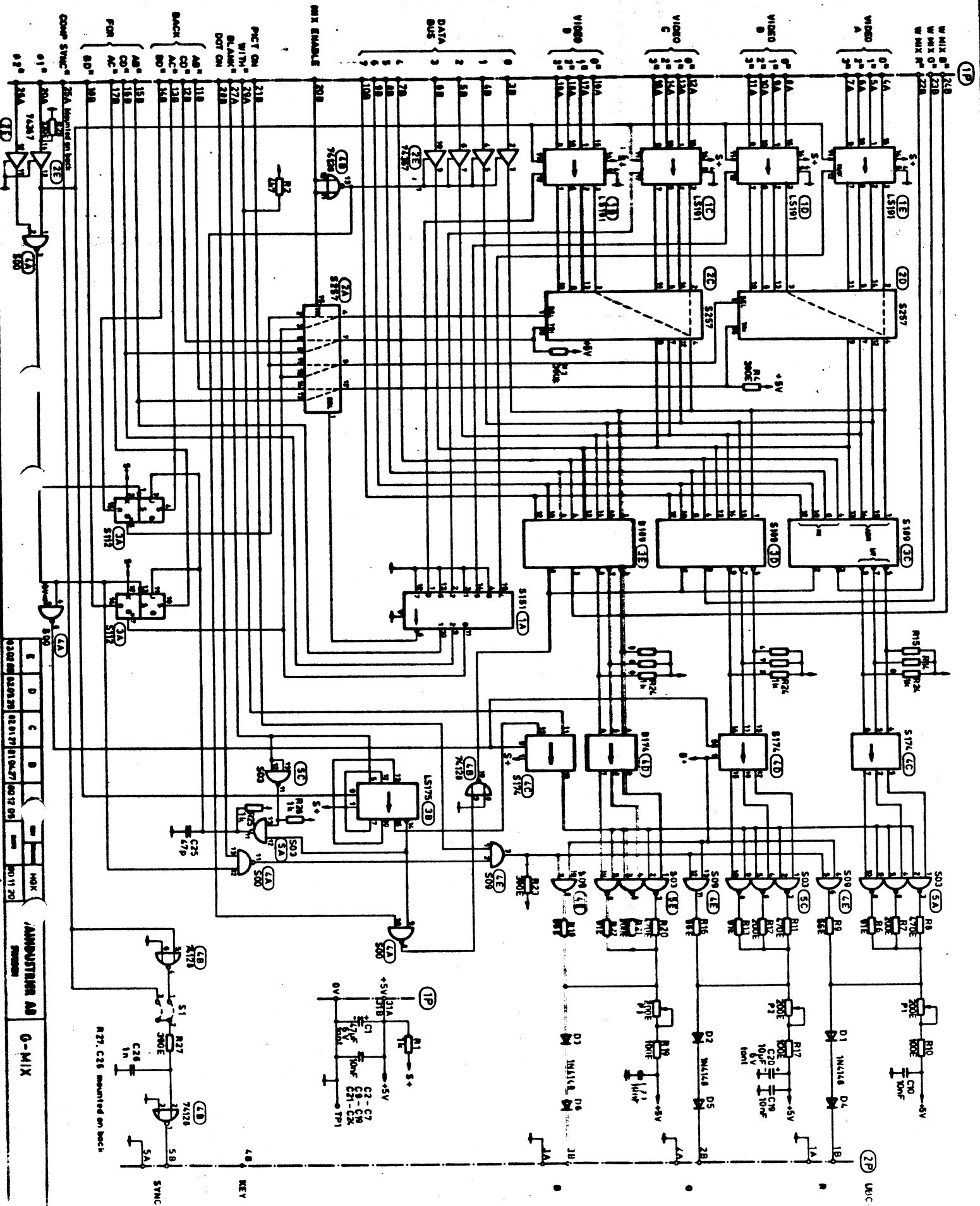
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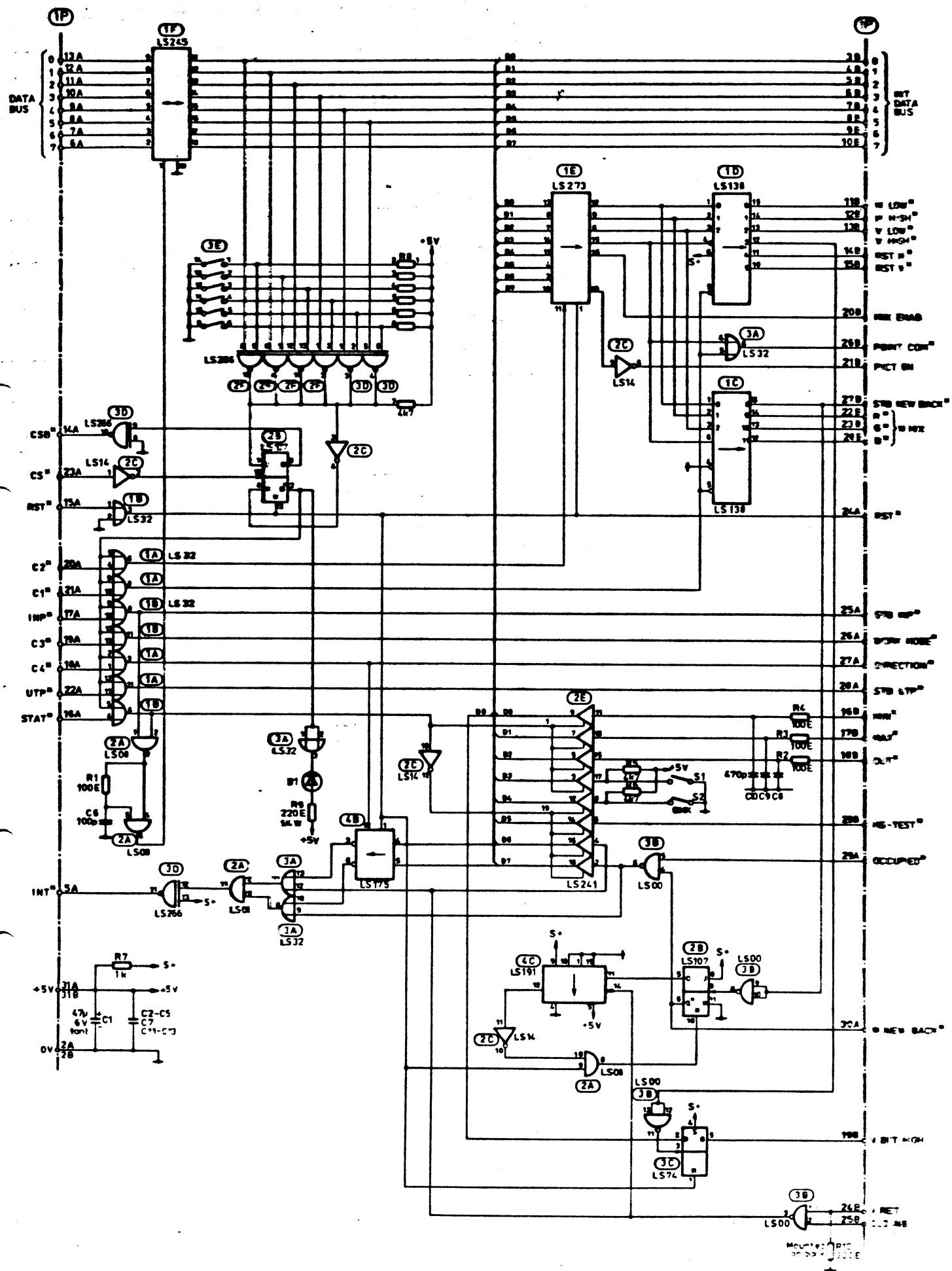


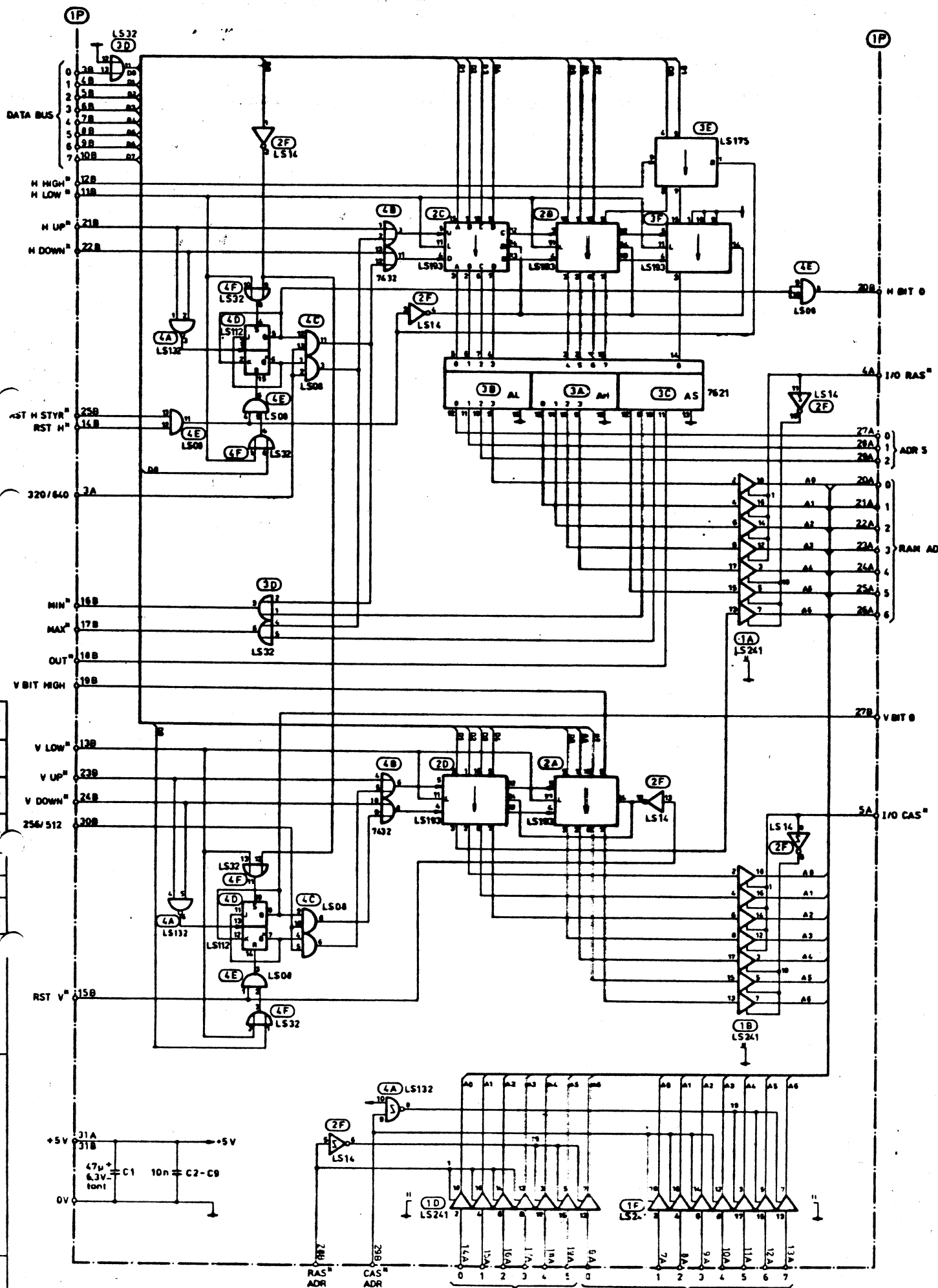


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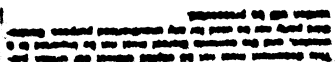
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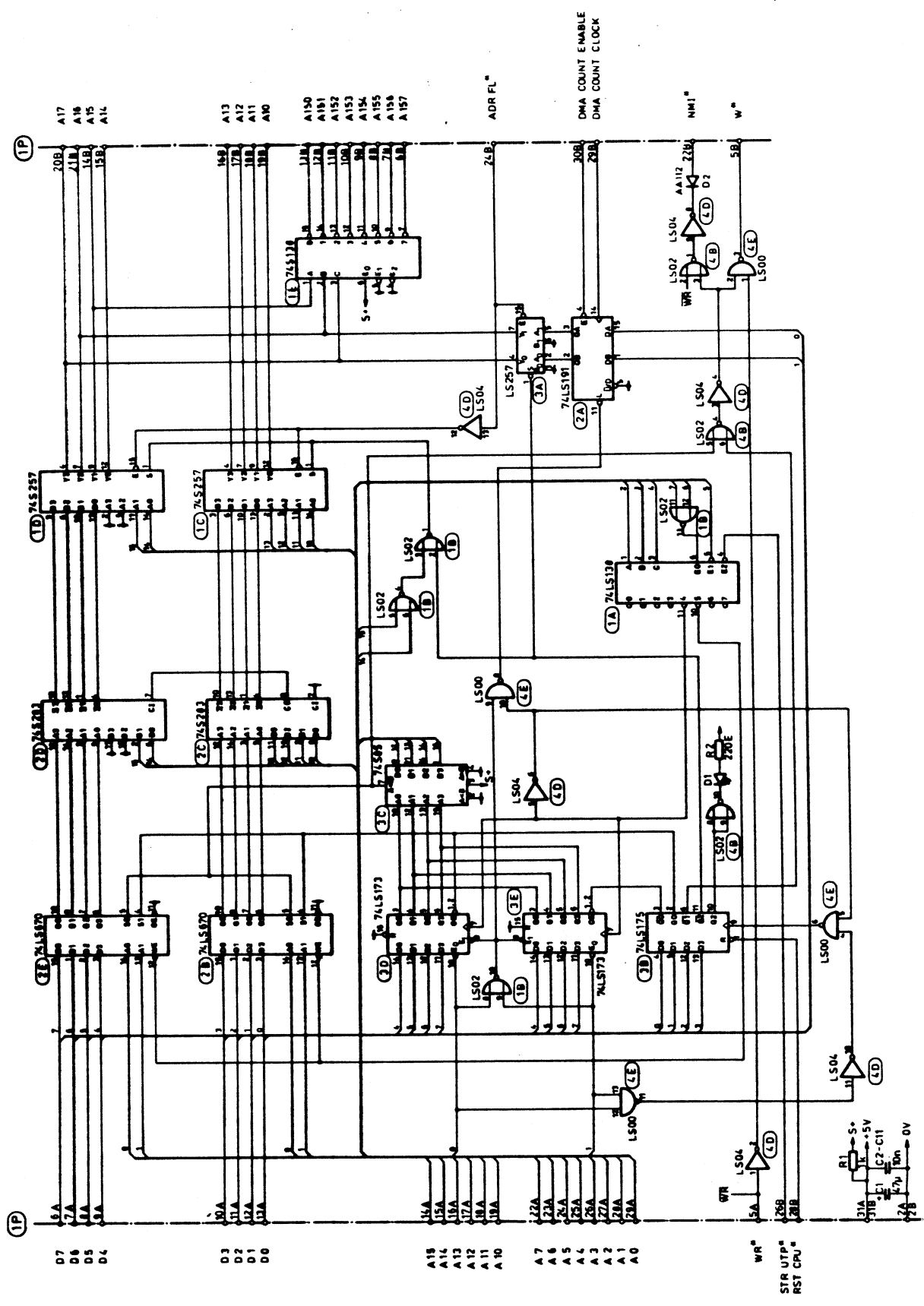




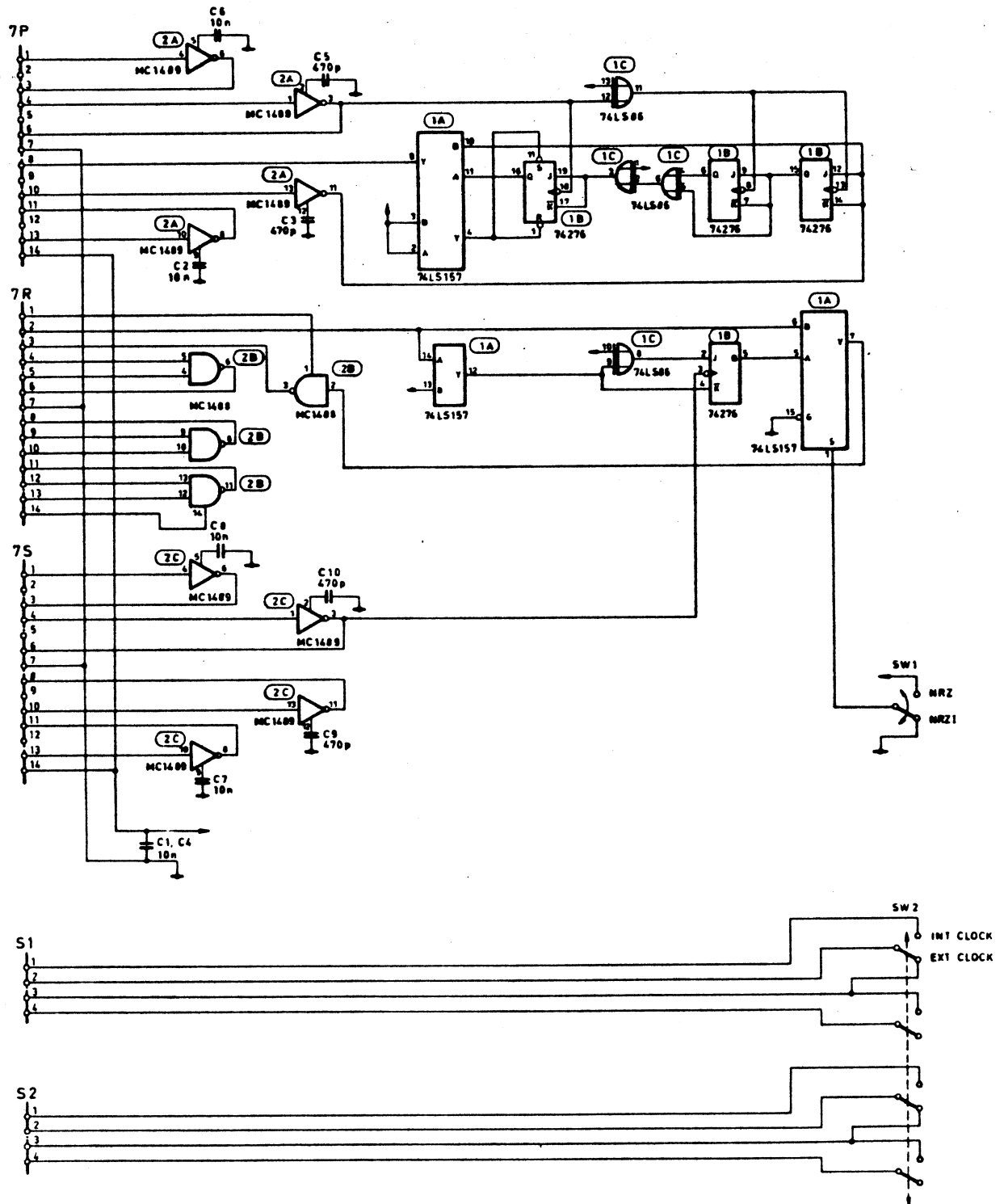


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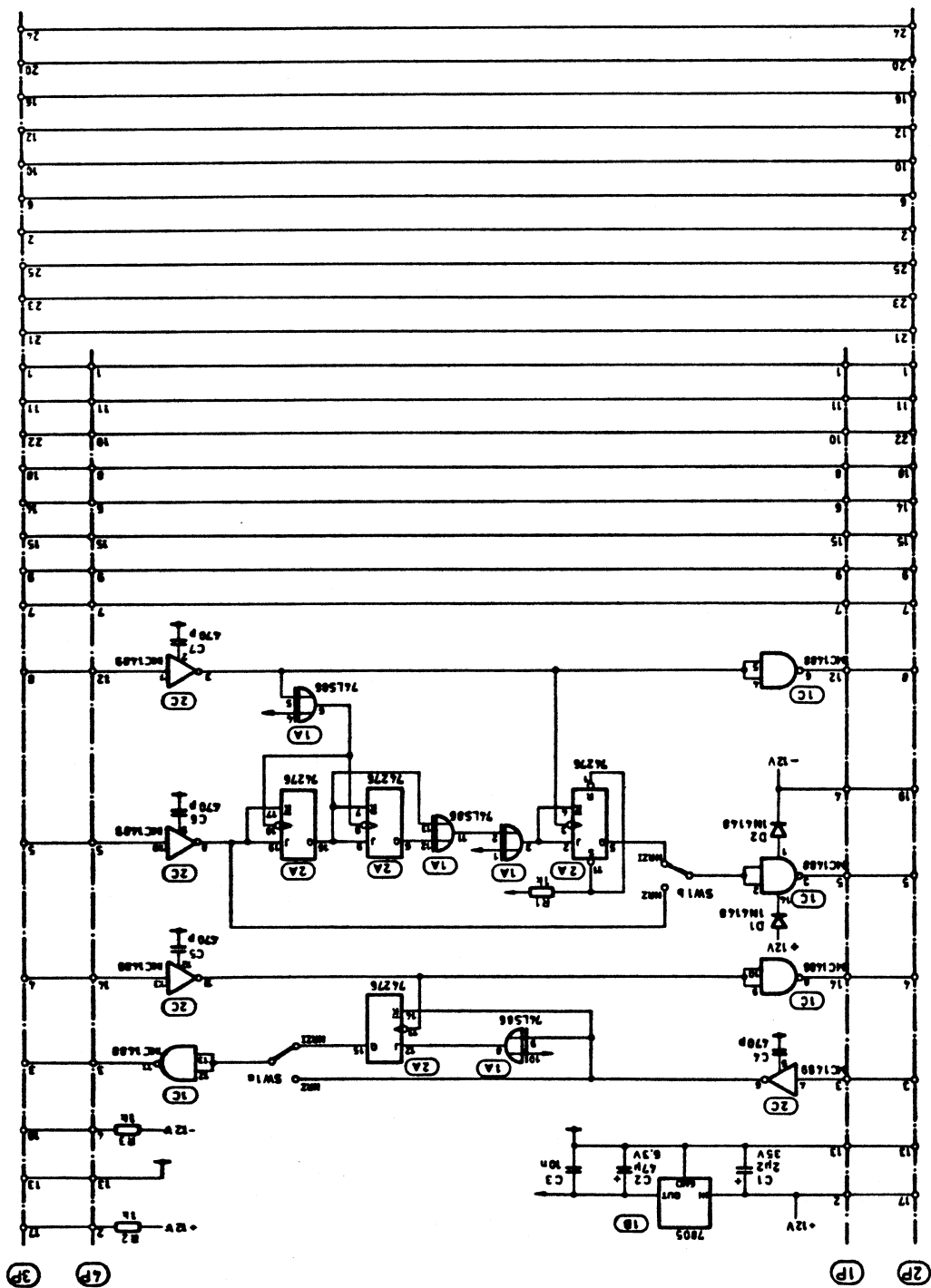


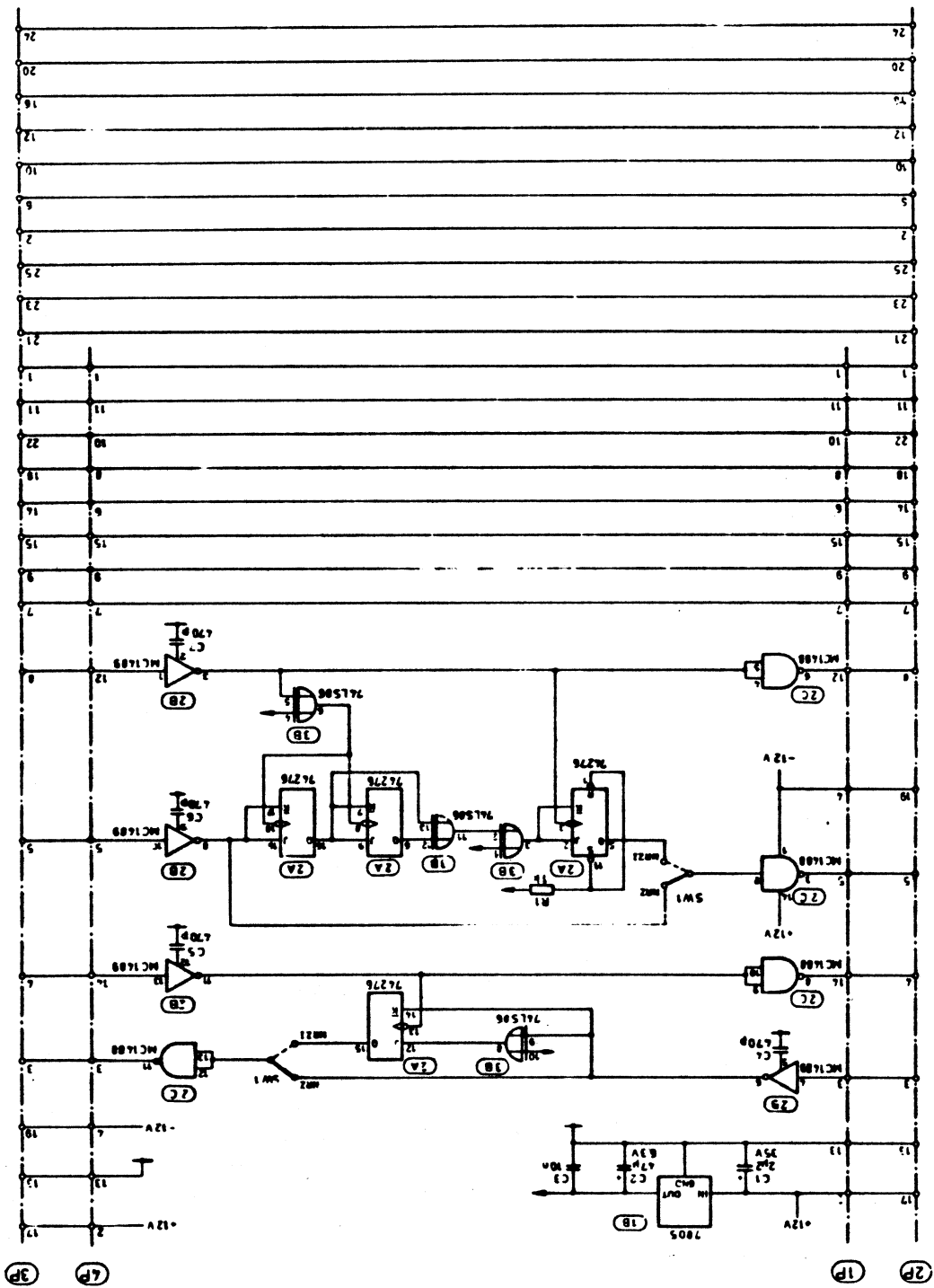


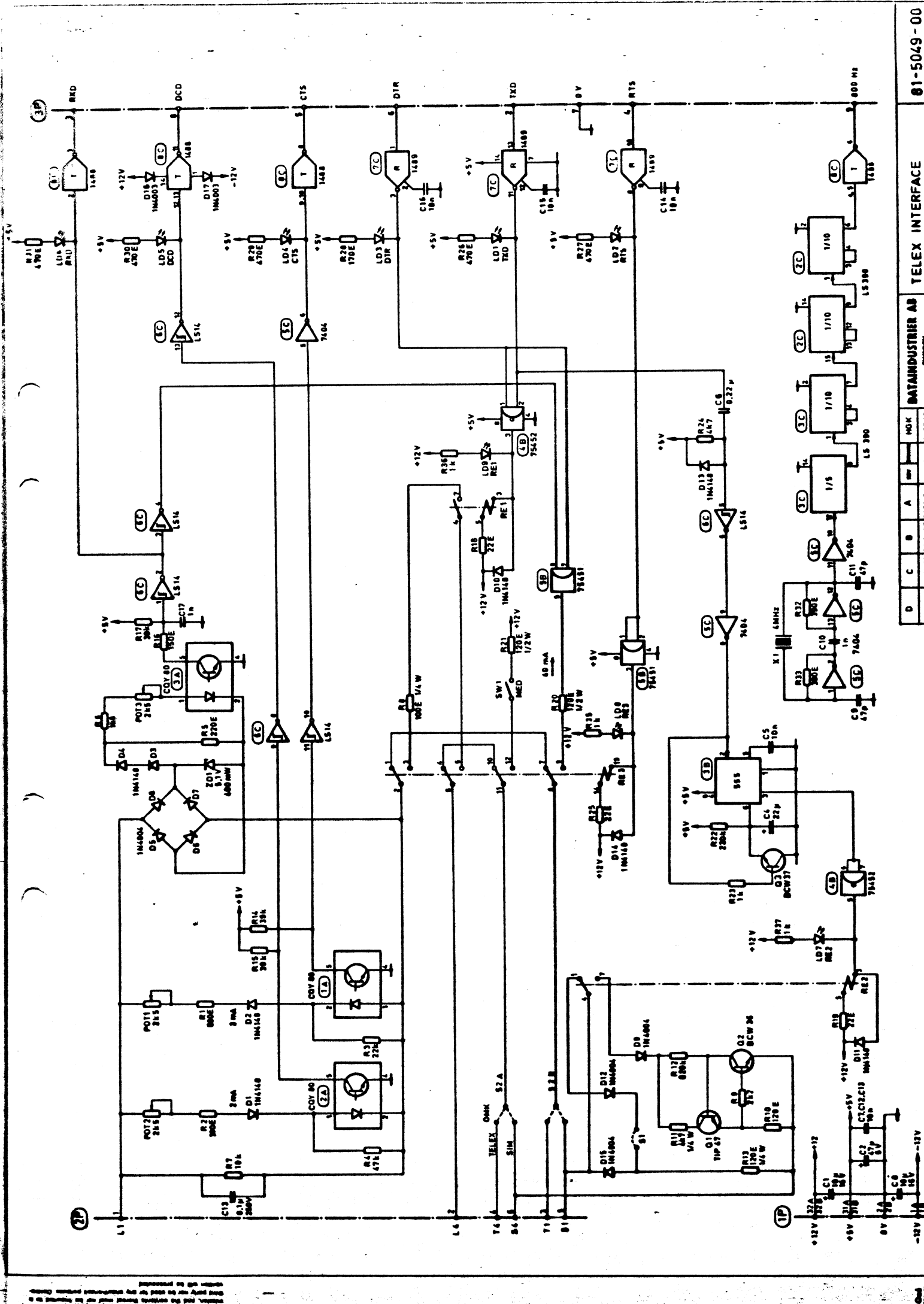
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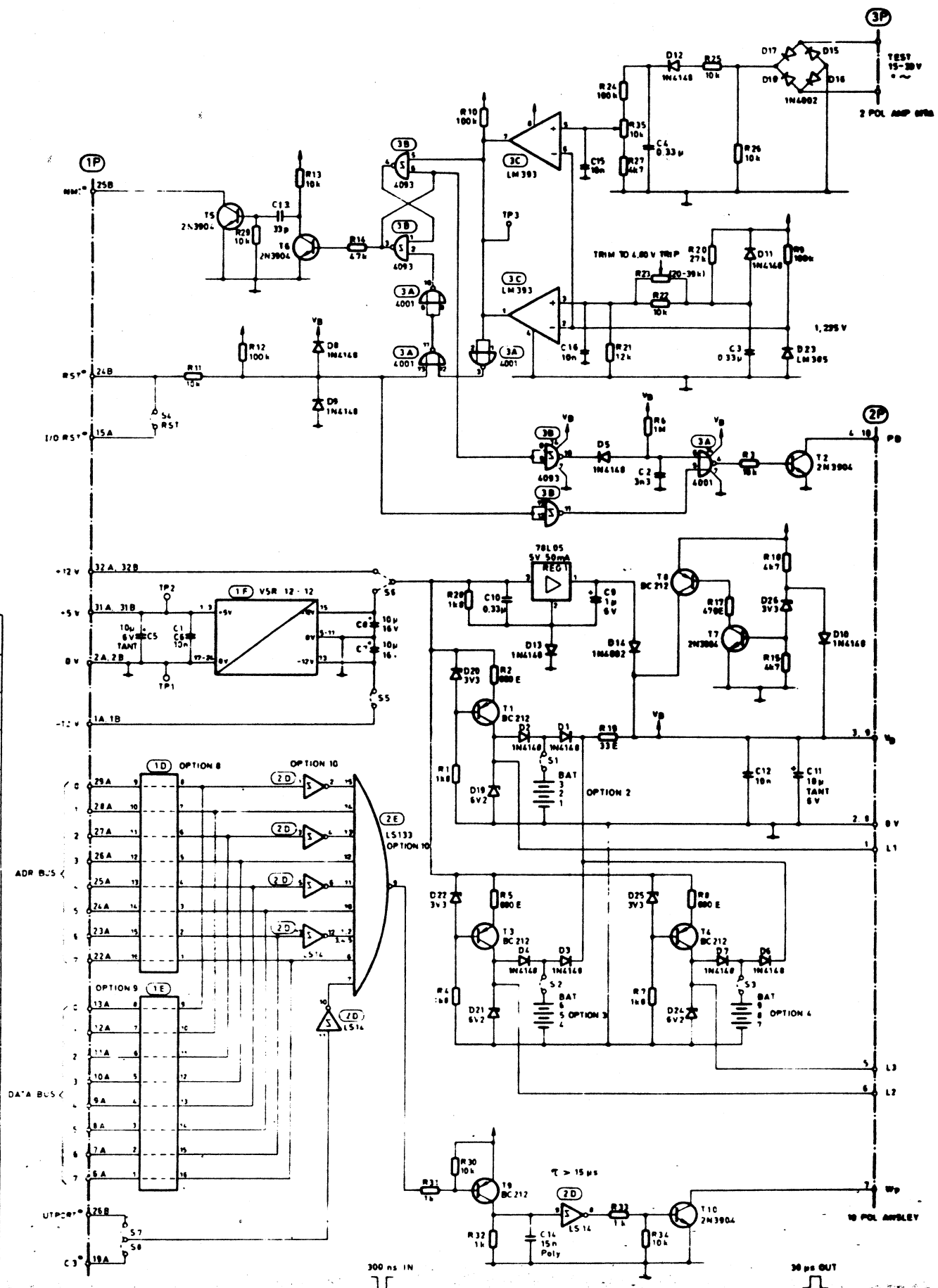
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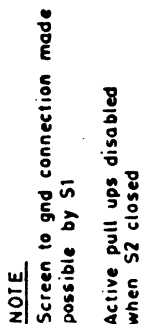




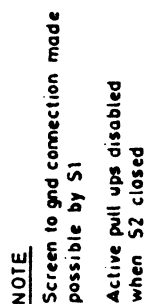


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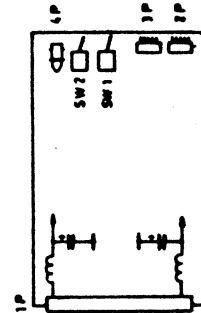
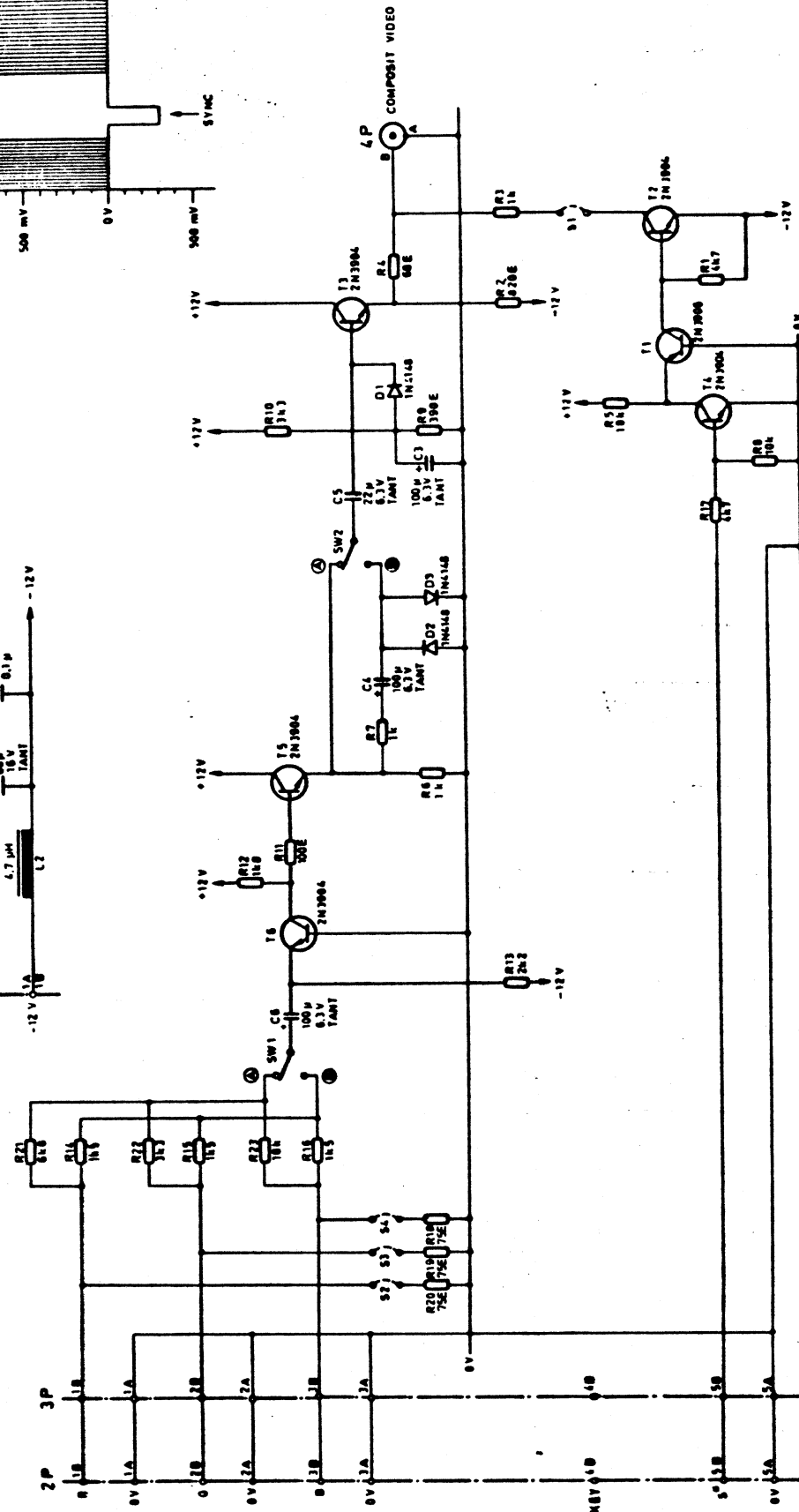
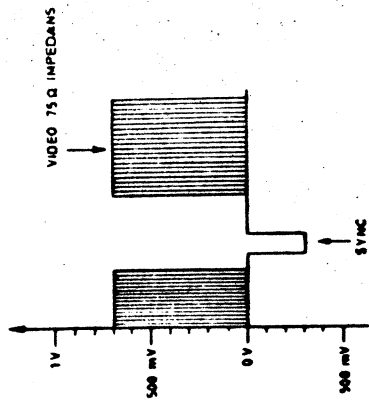
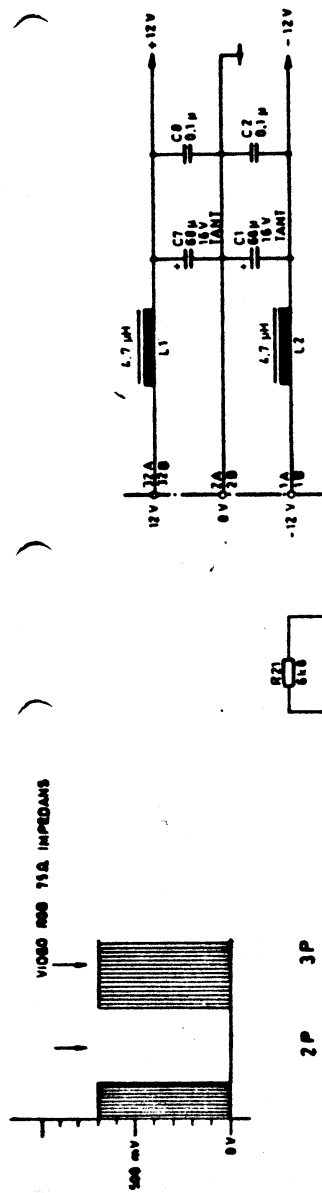




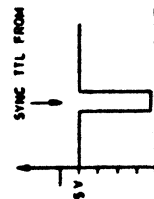
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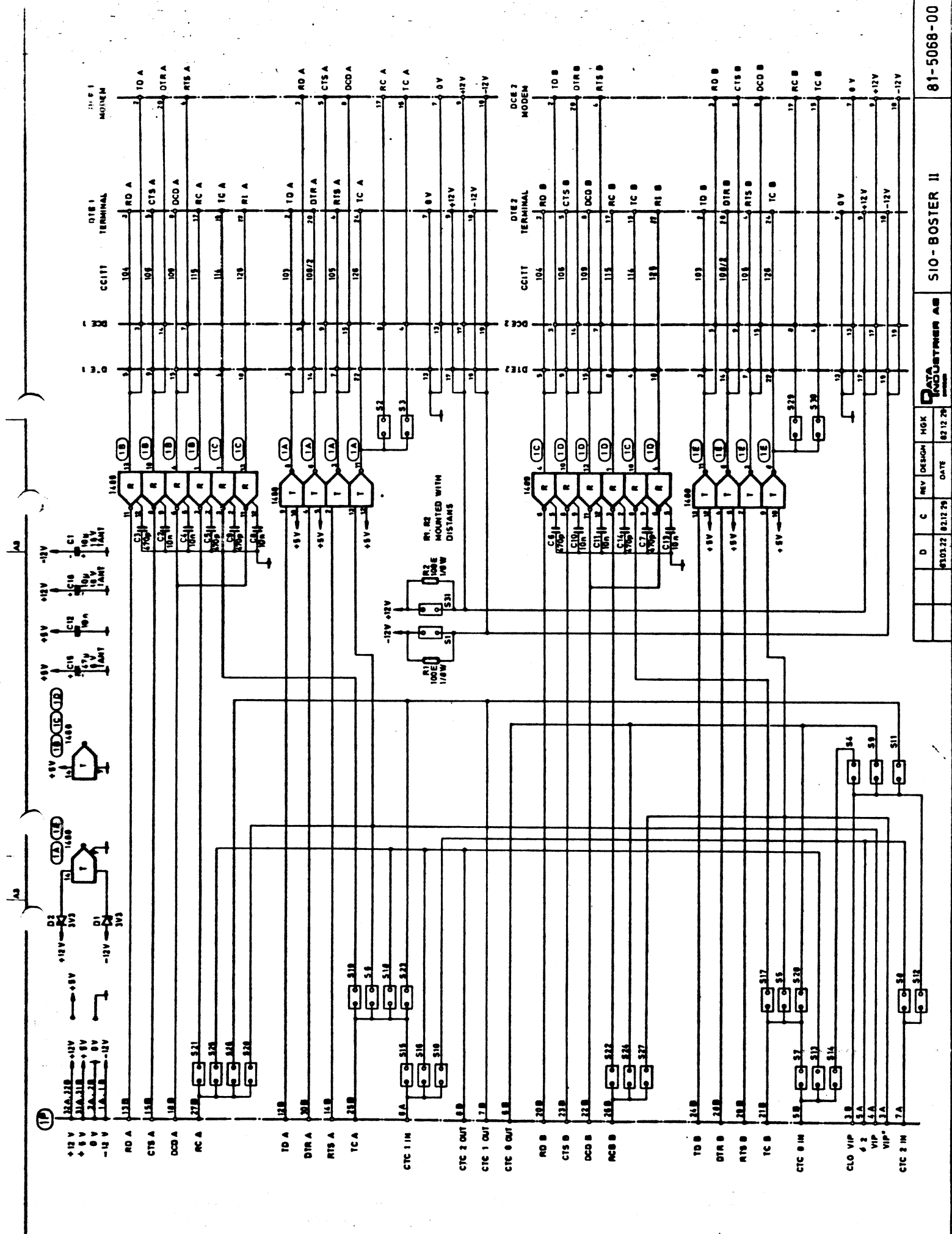


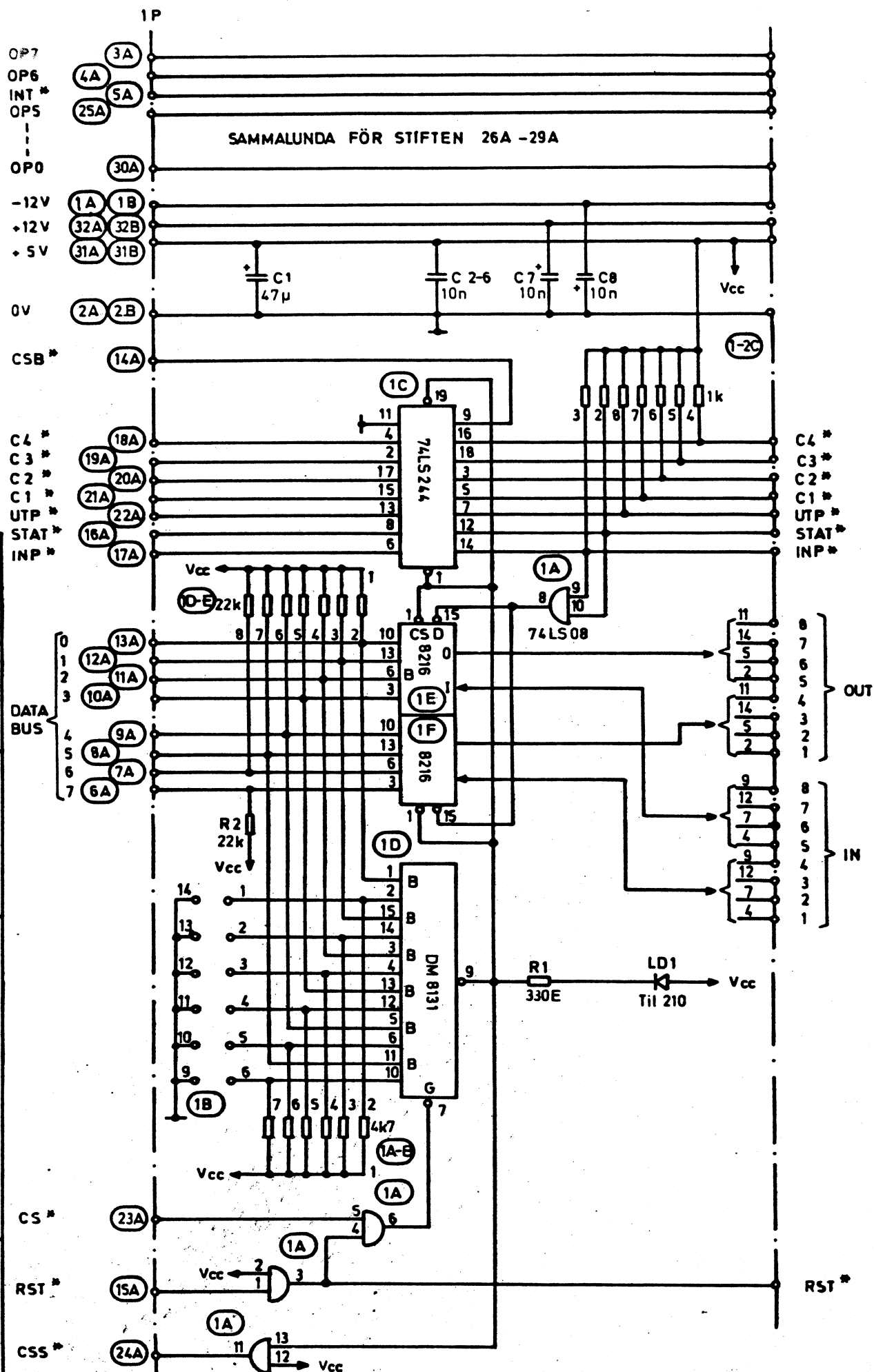
SYNC TTL FROM SMT1120 50A DRIVER

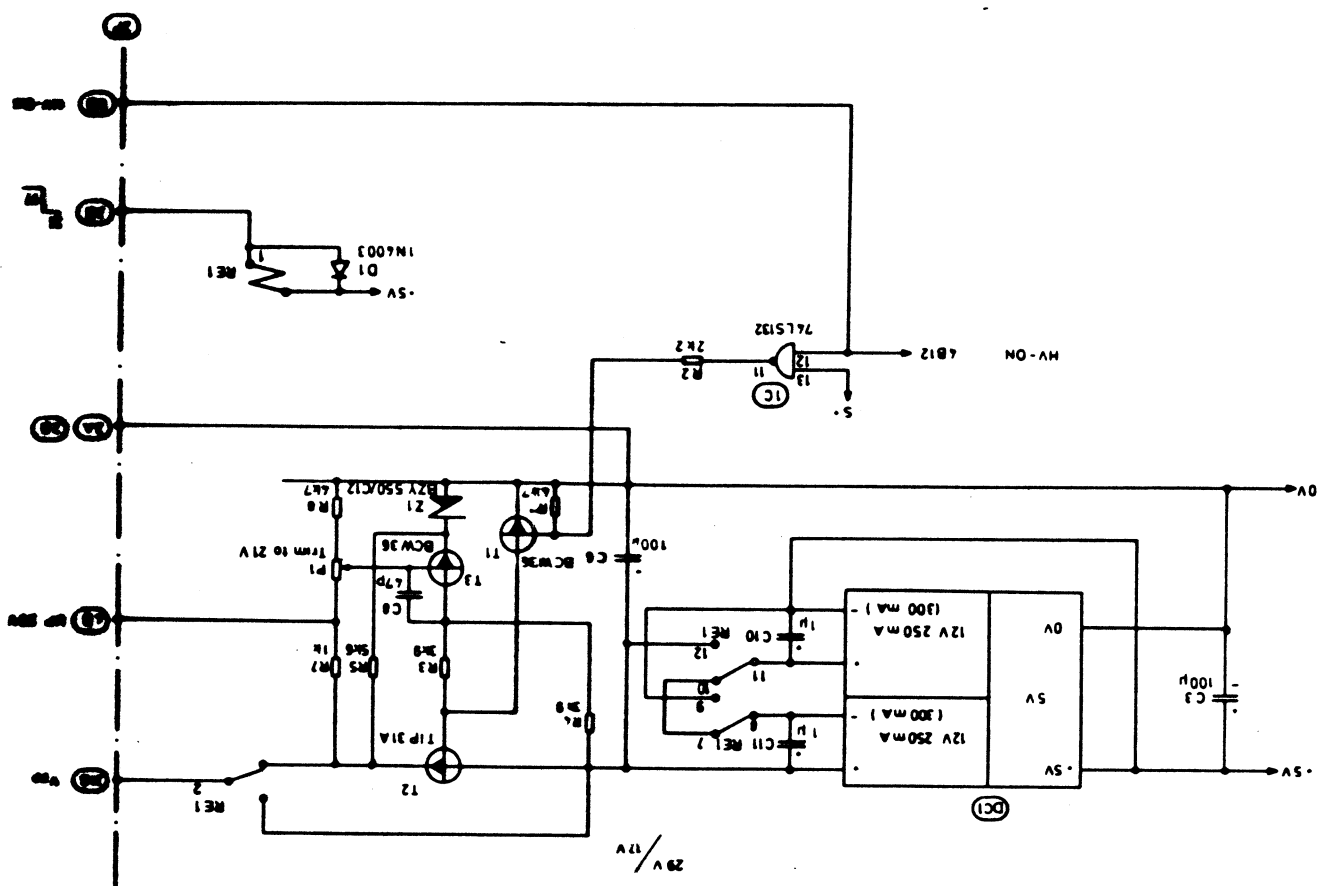


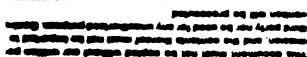
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SW2 A: B:

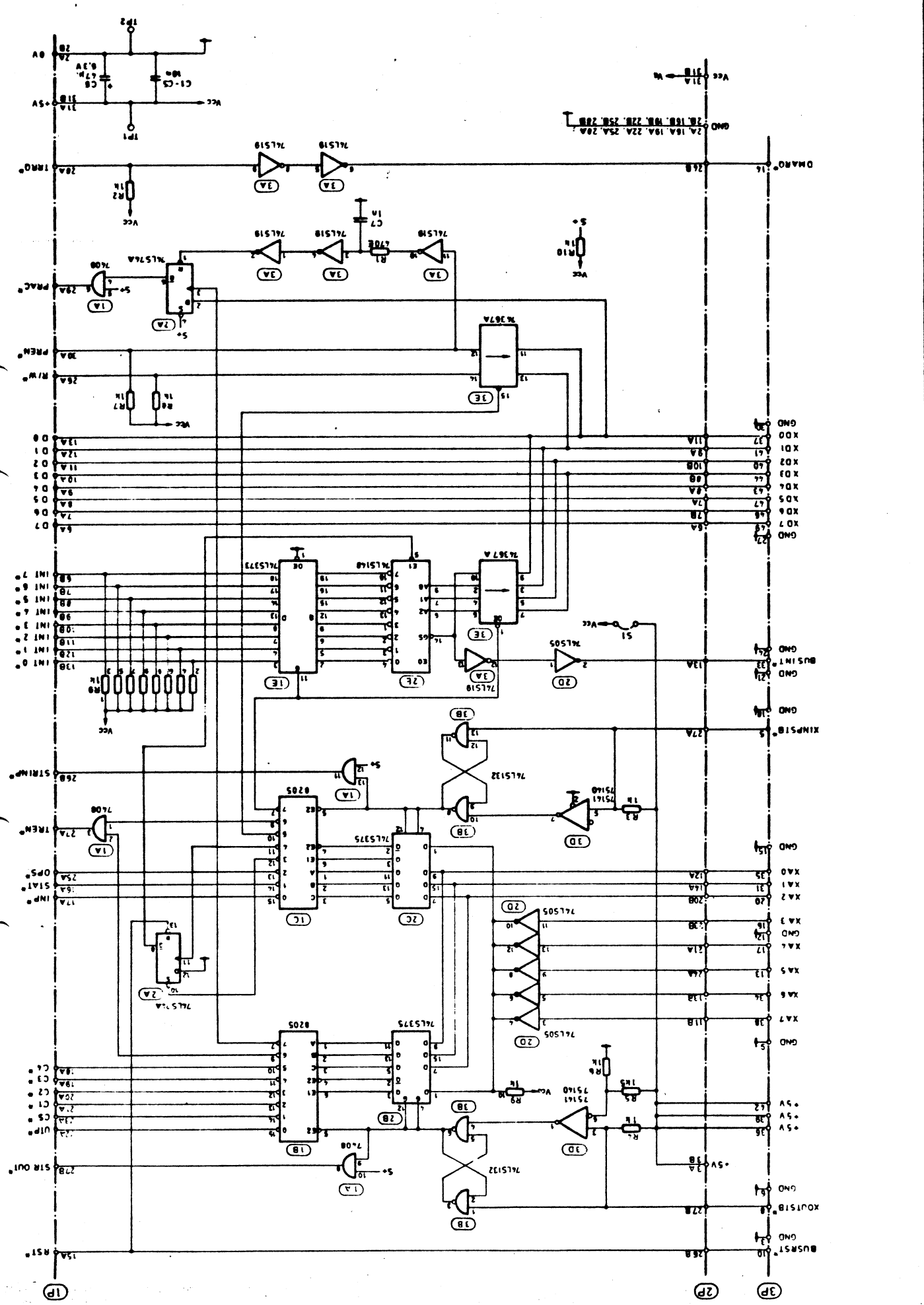
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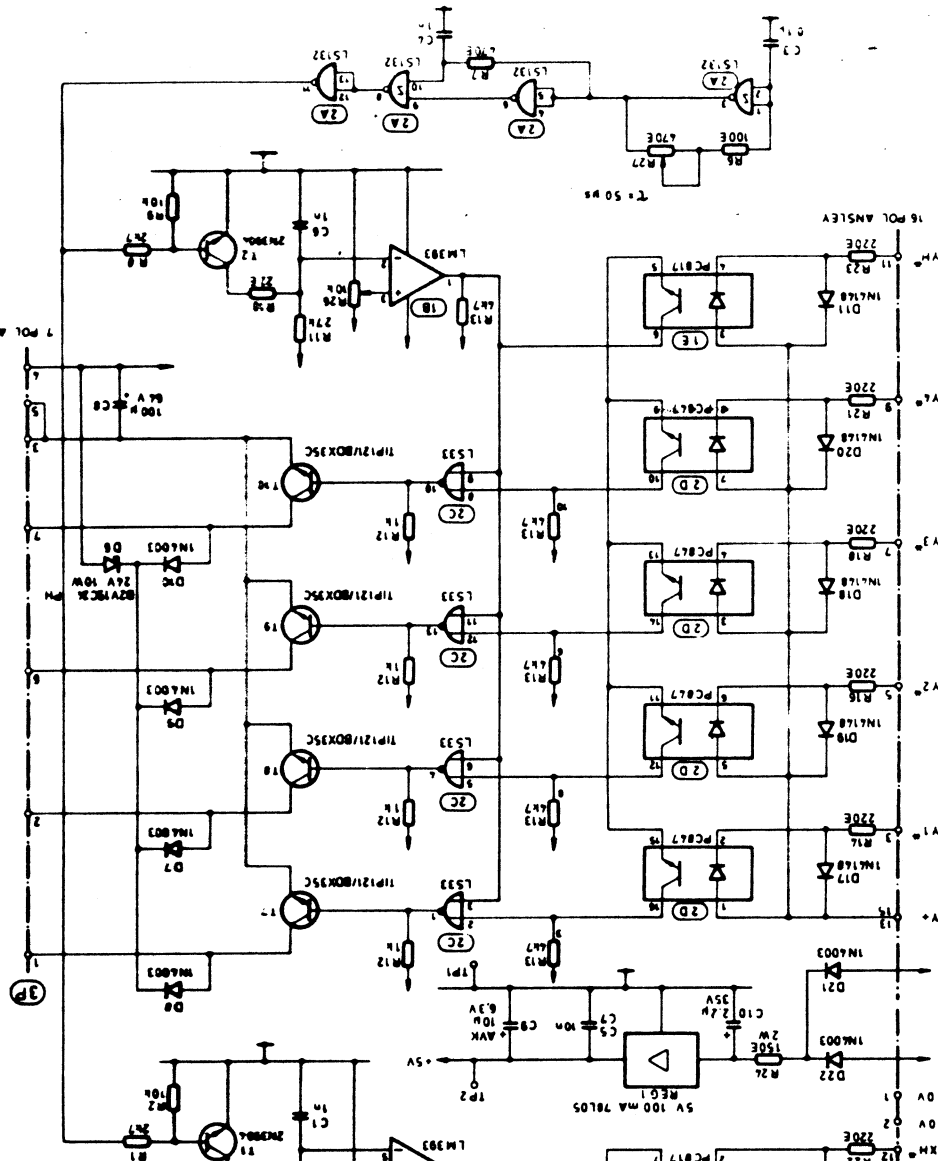
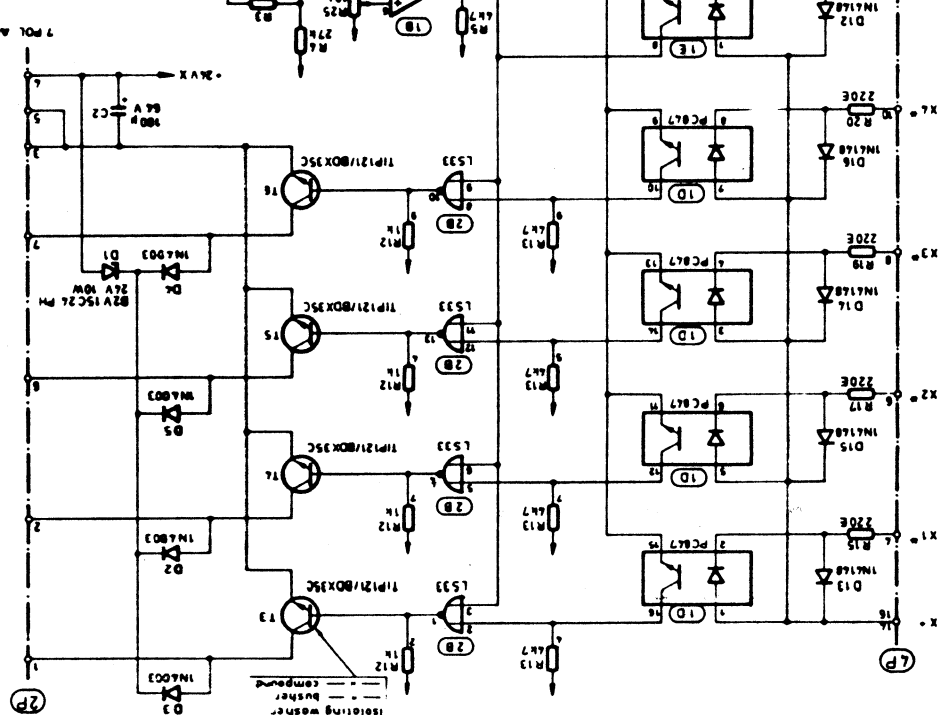
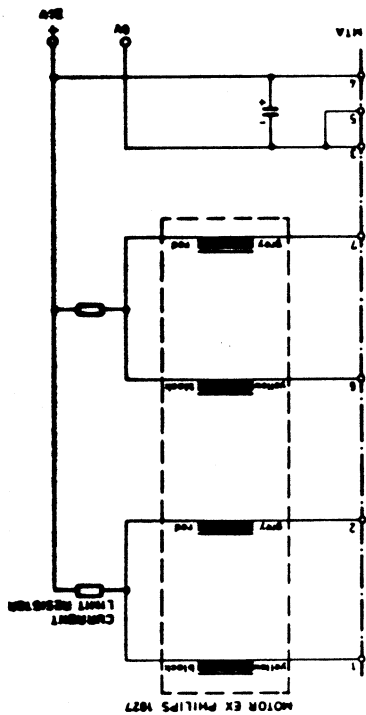


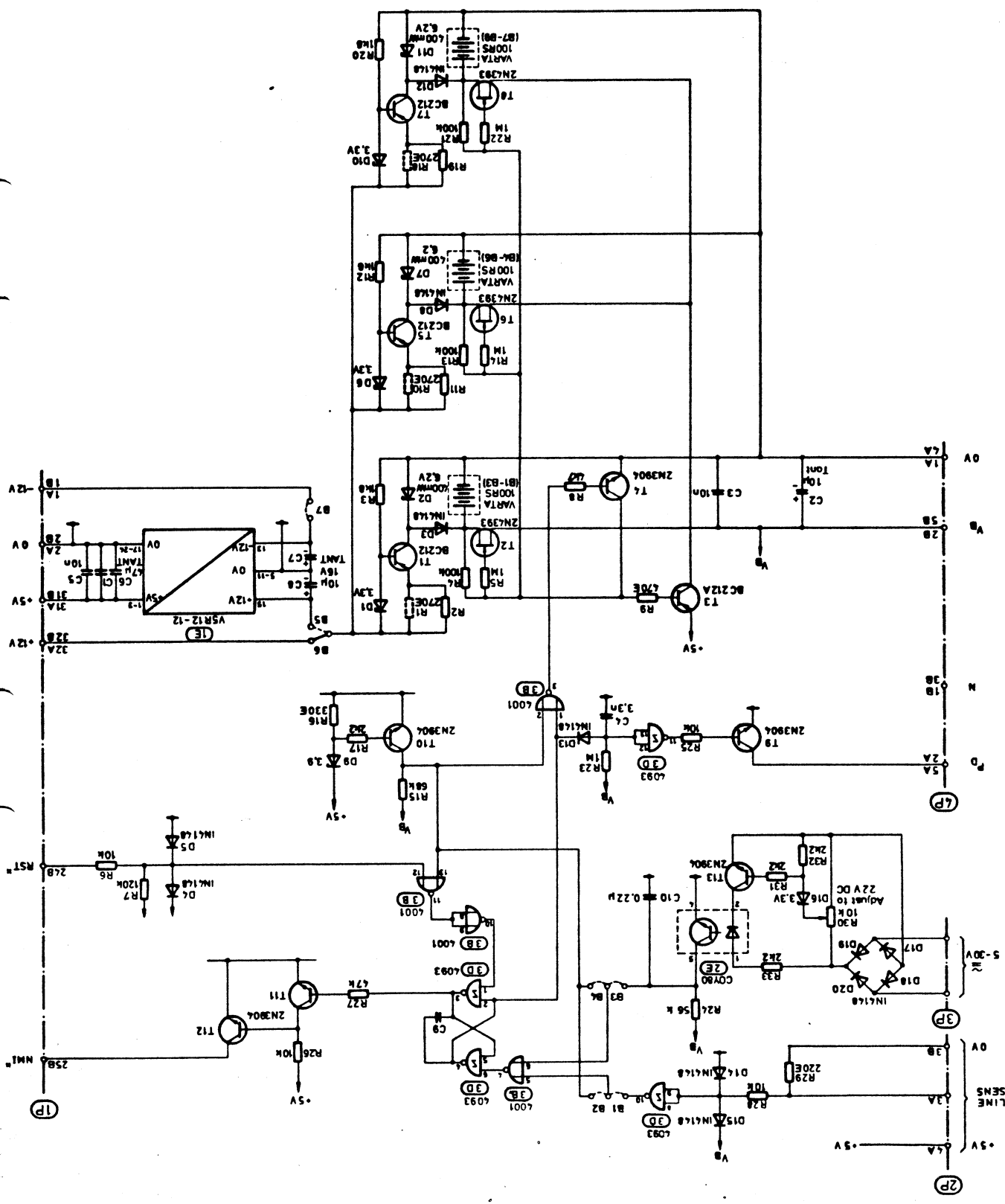




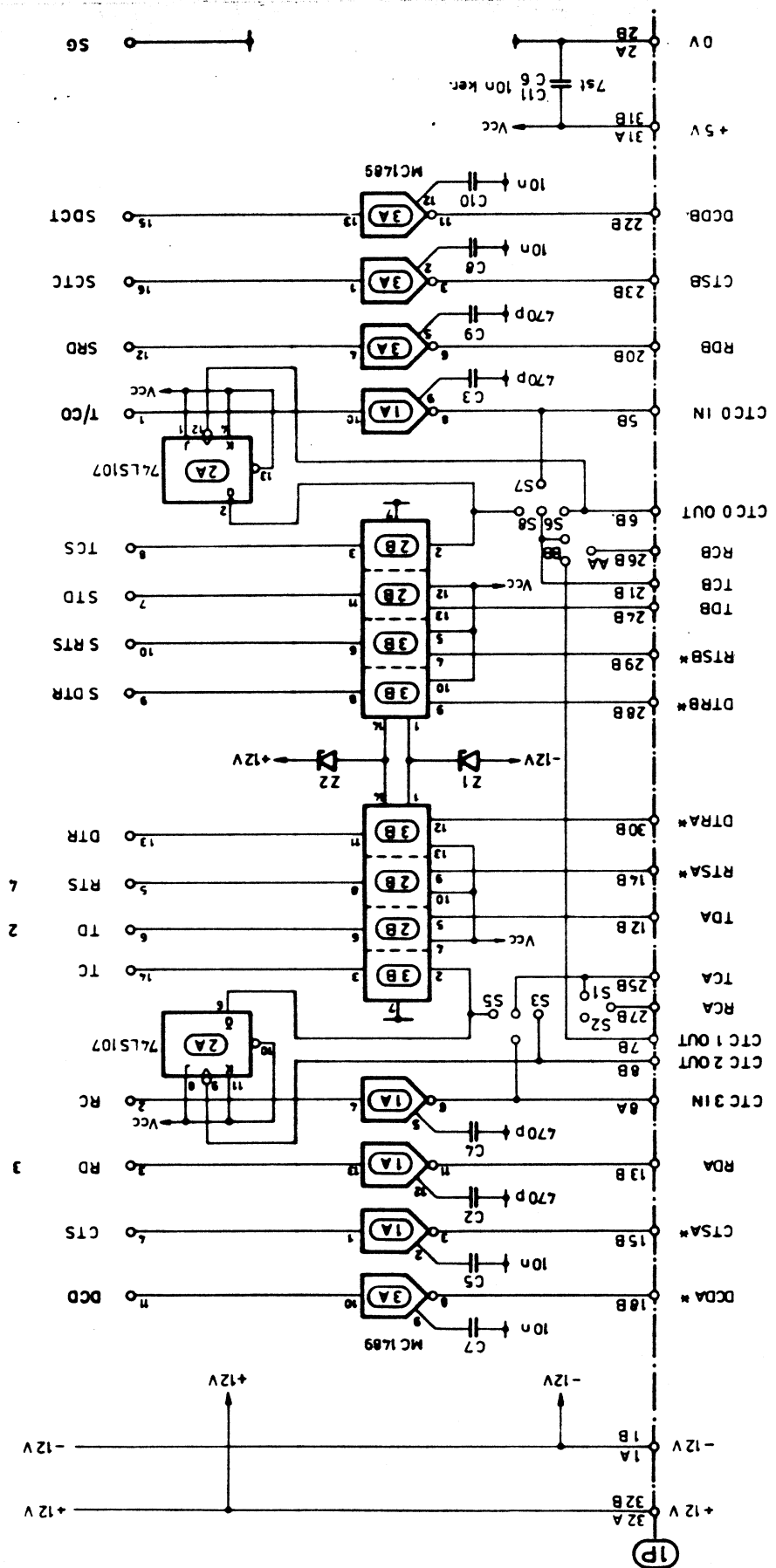


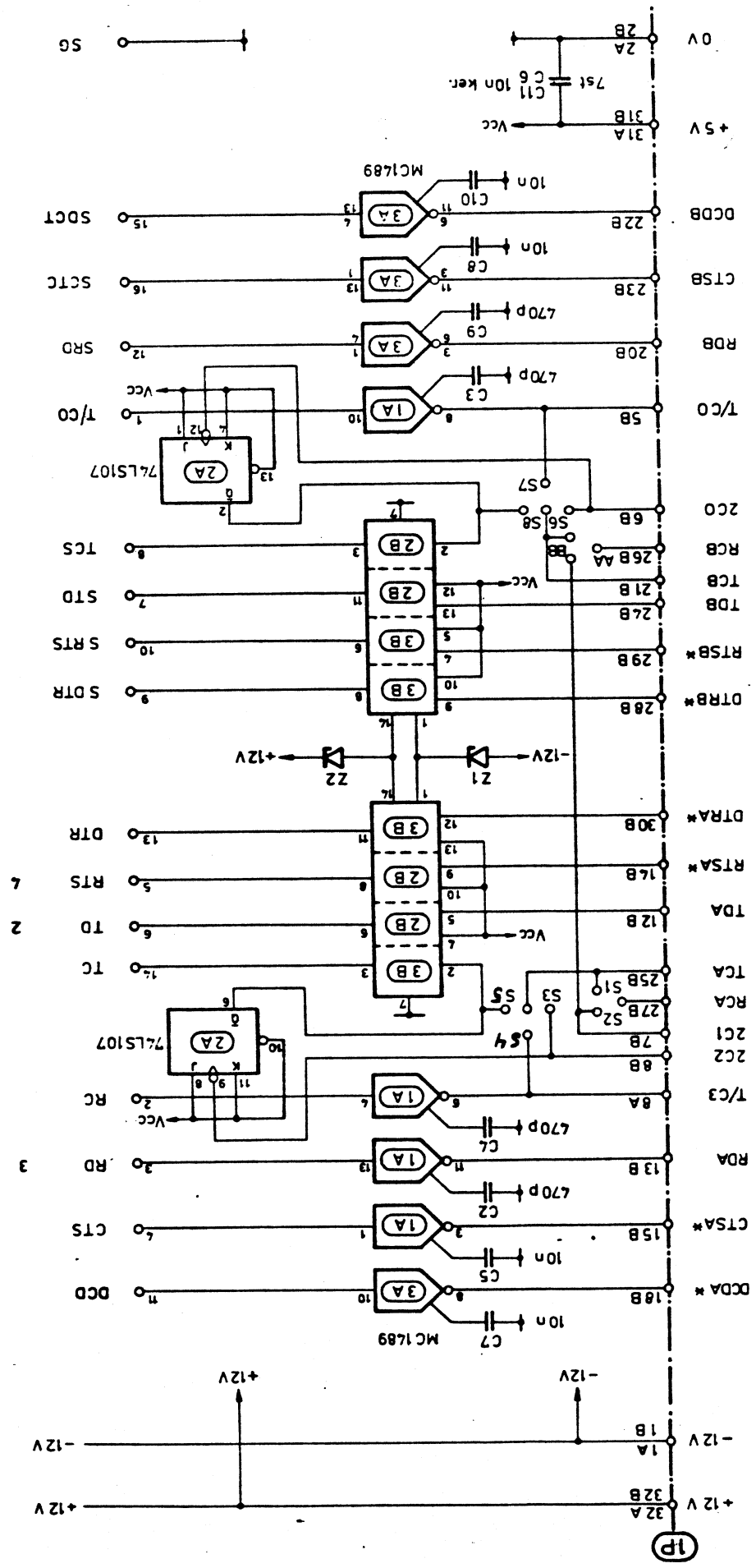
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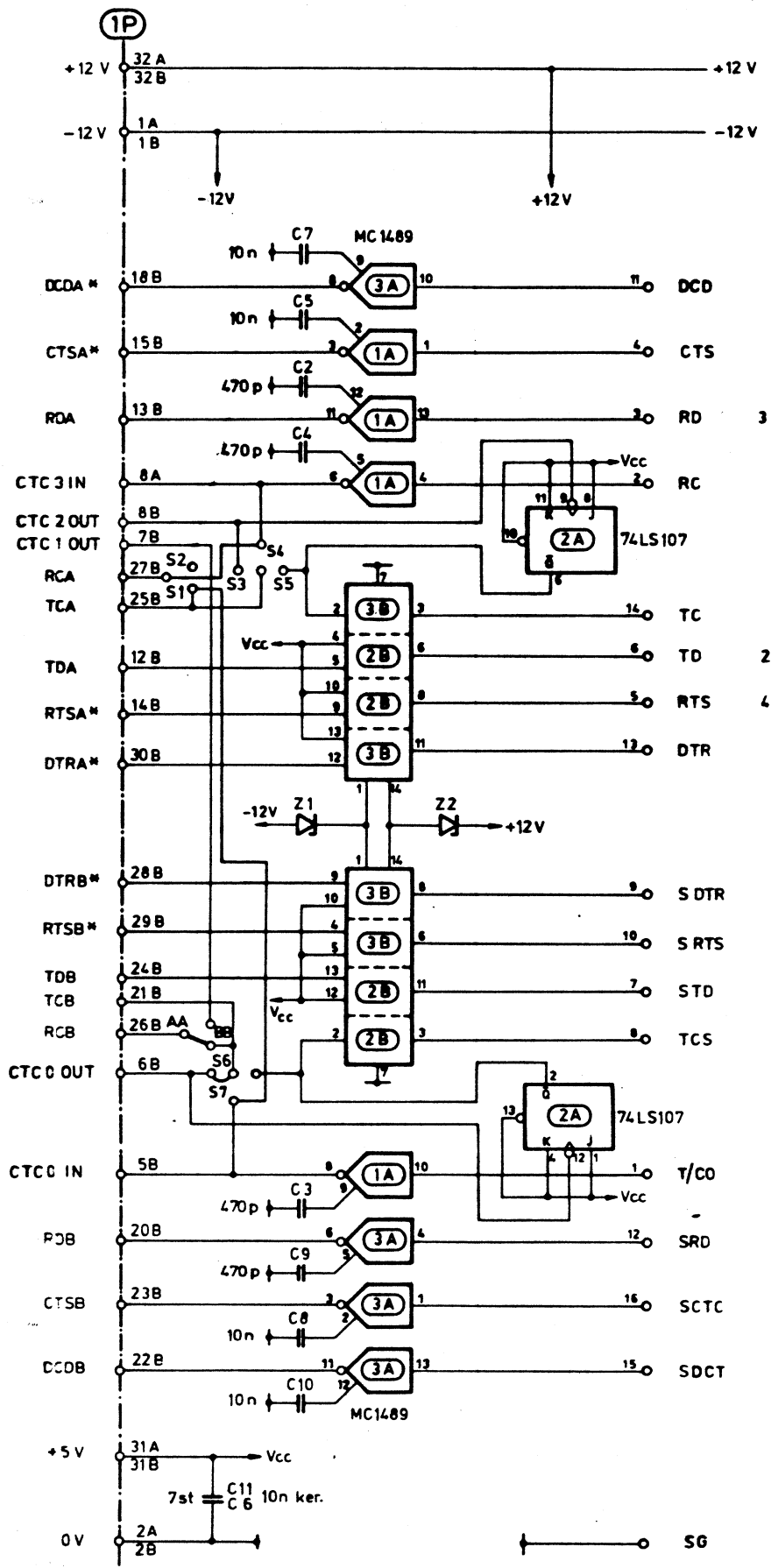




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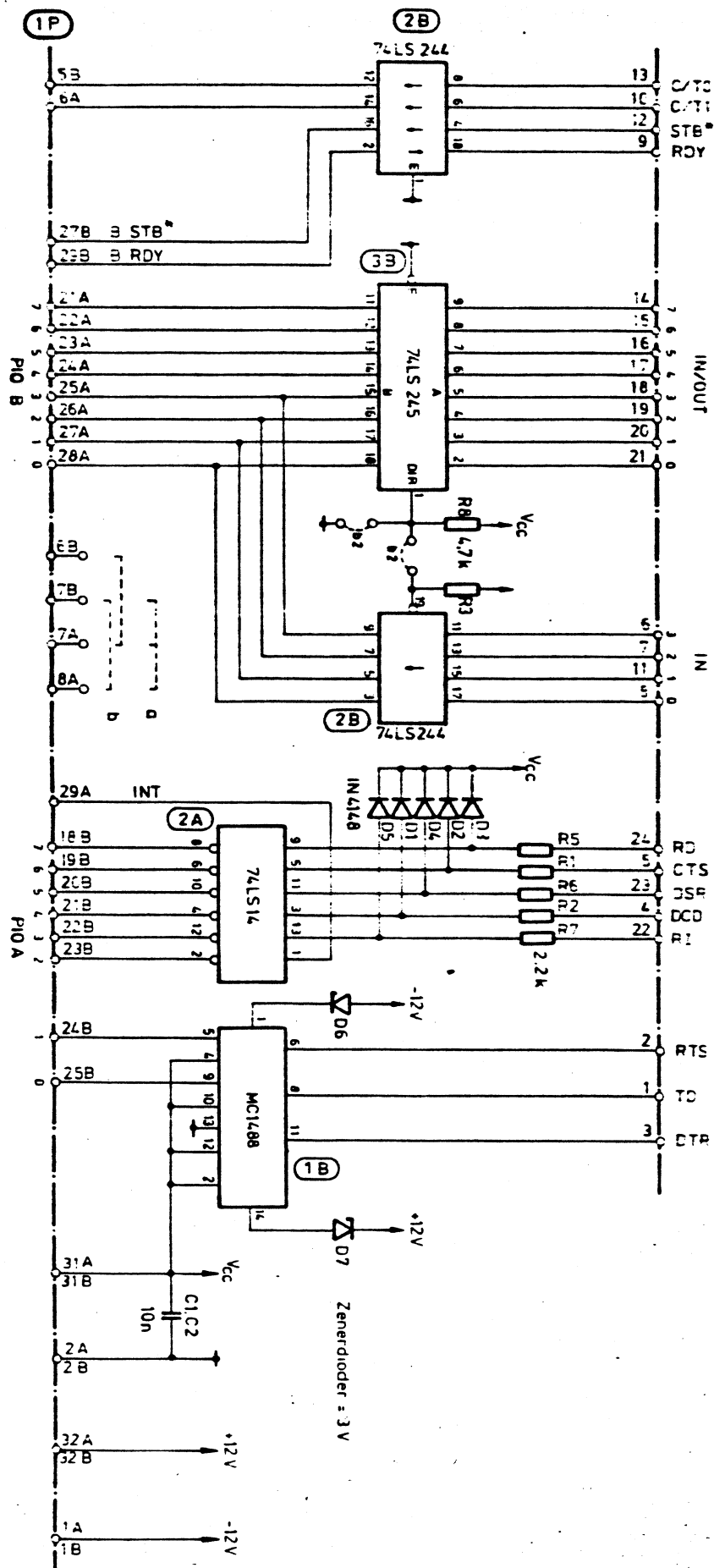


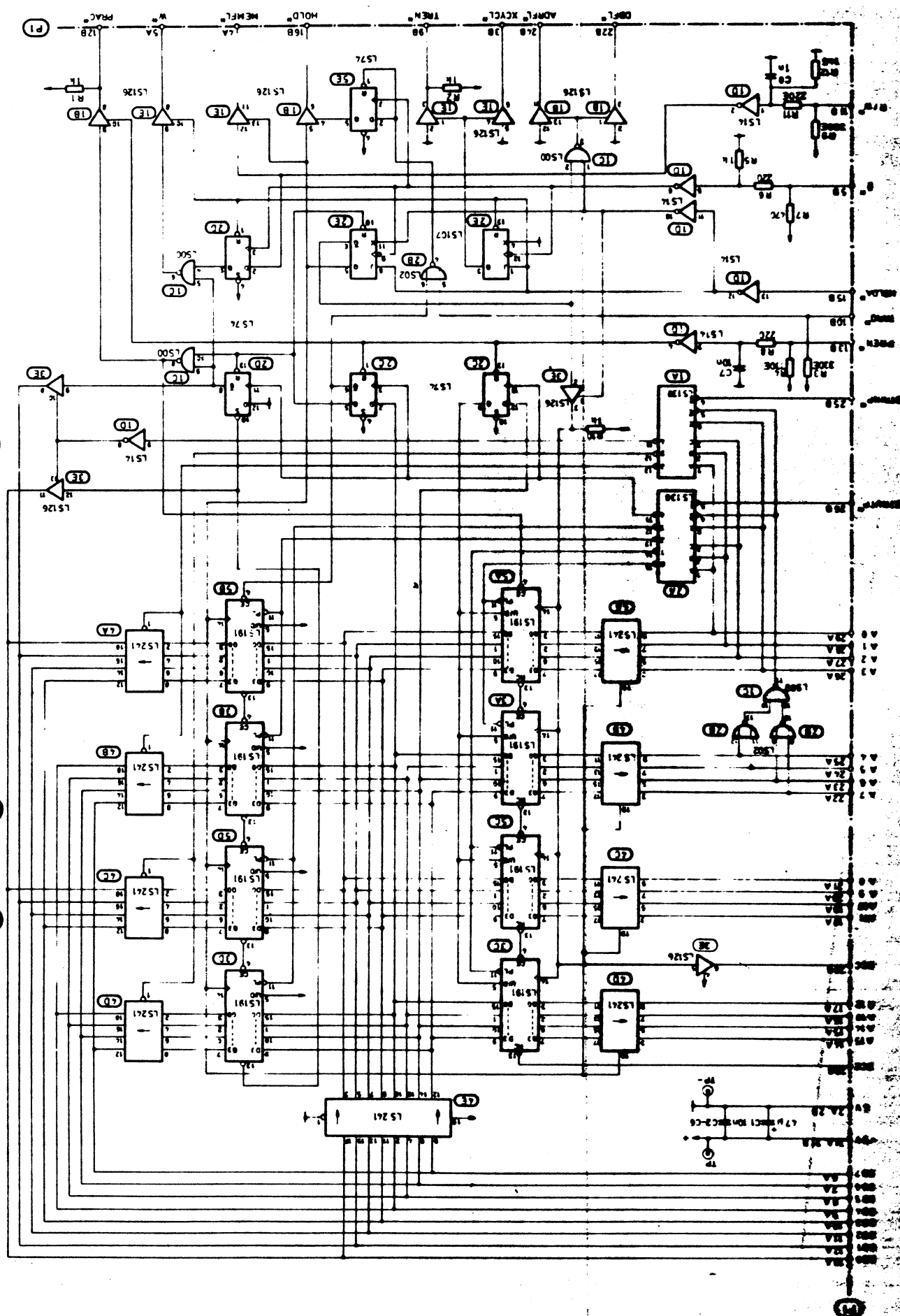
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DATA/INDUSTRIAL
UNIVAC

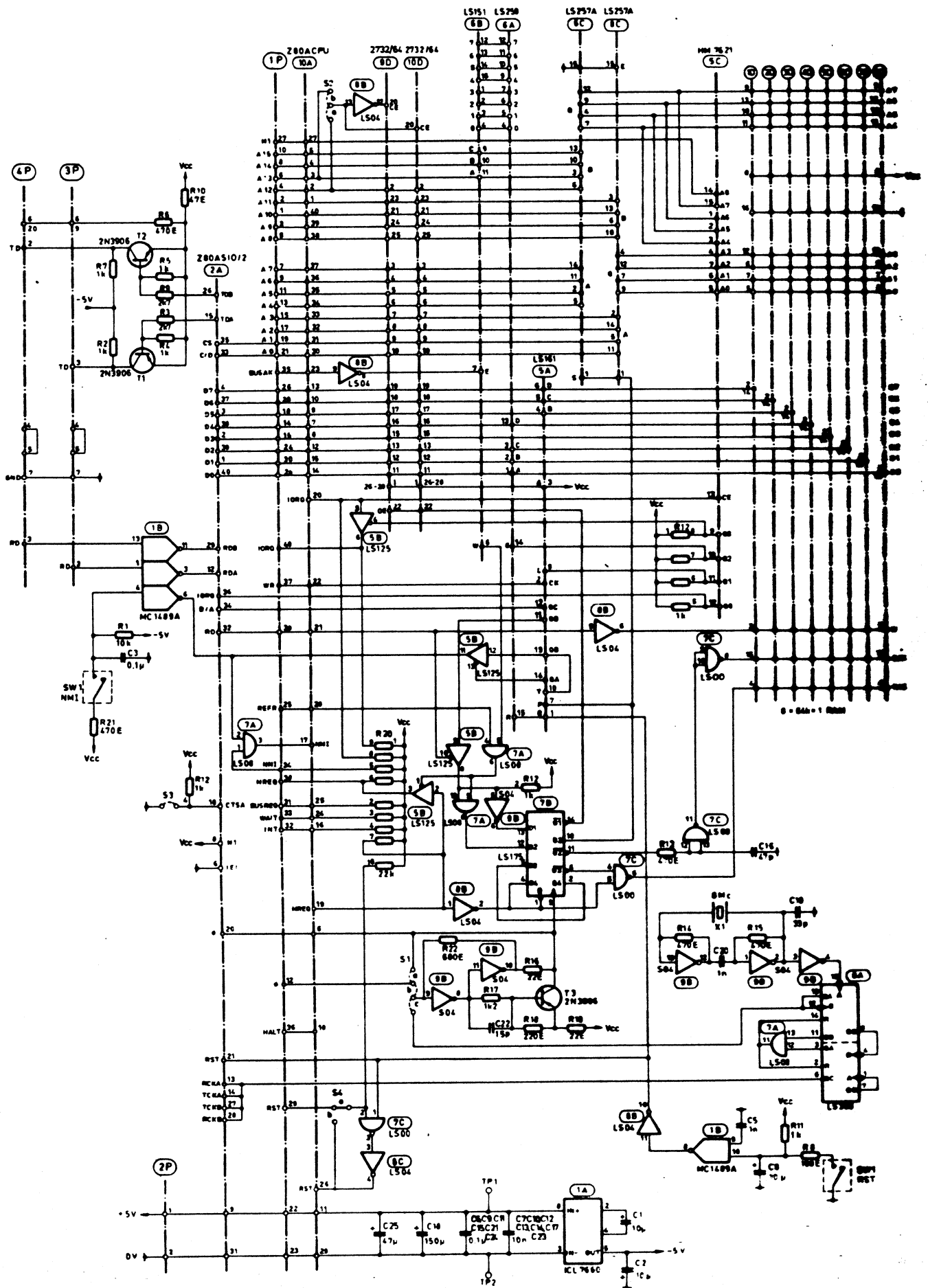
SIO-ADAPTER DATA COLOR

81-5098-10

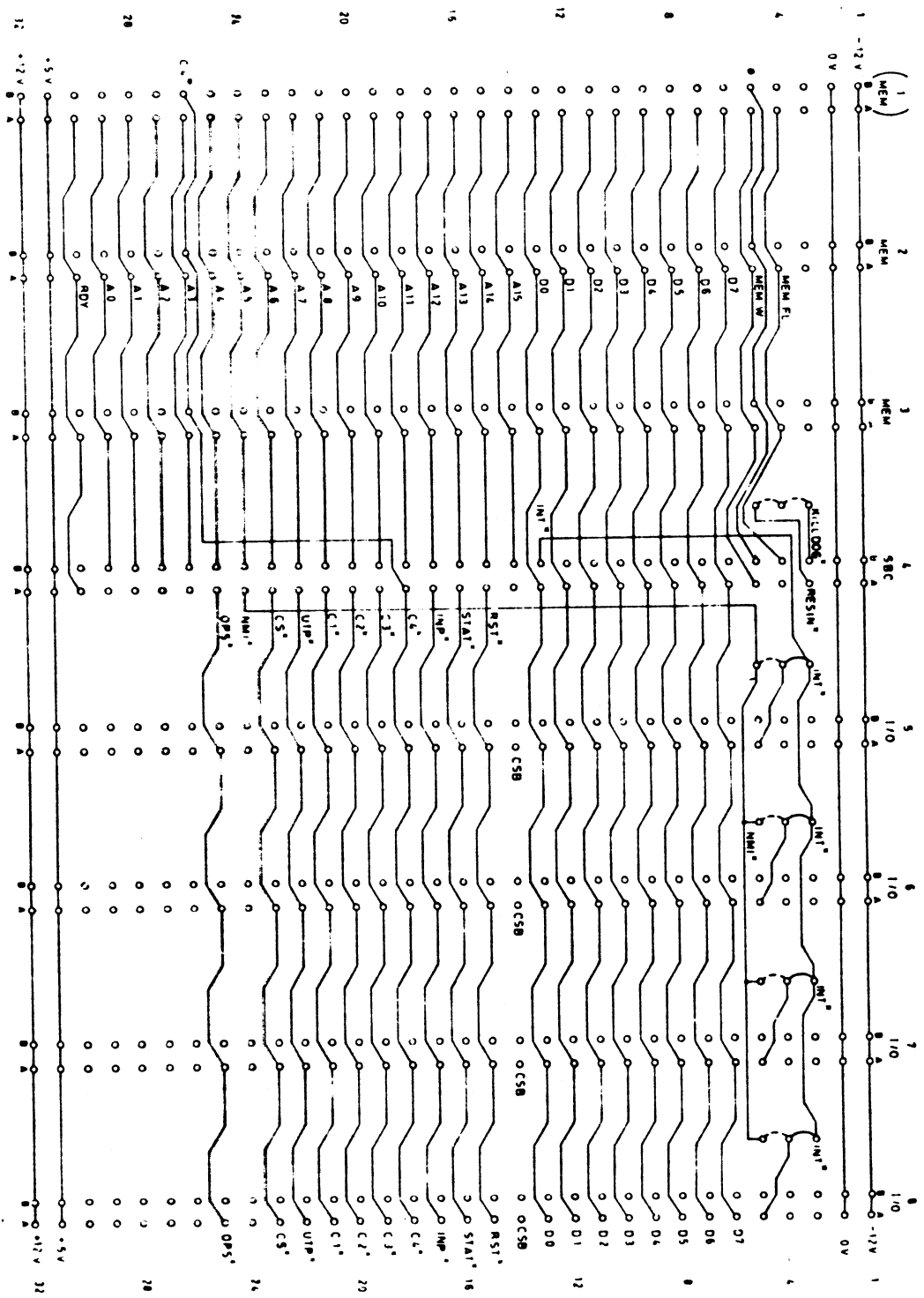




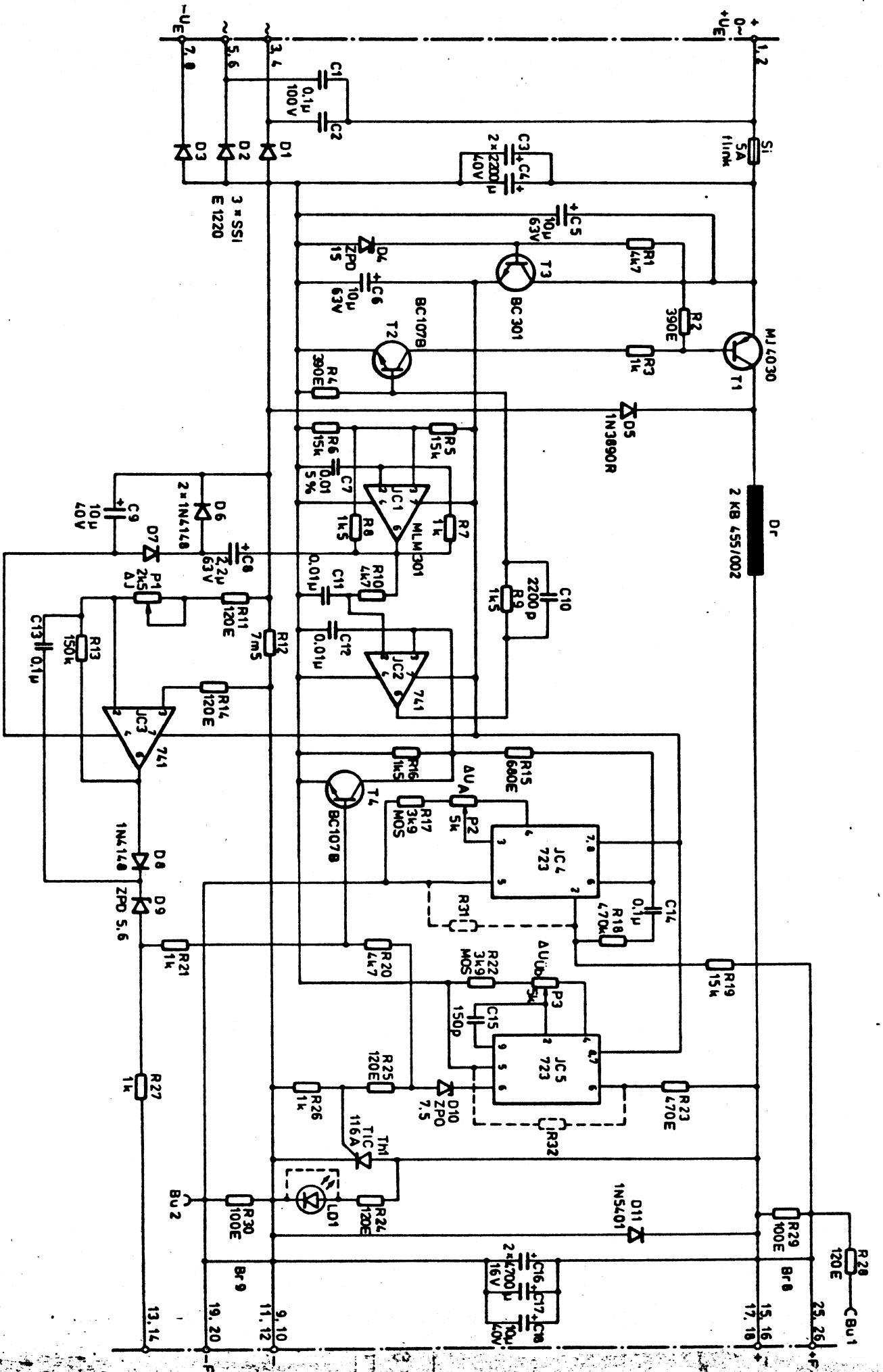
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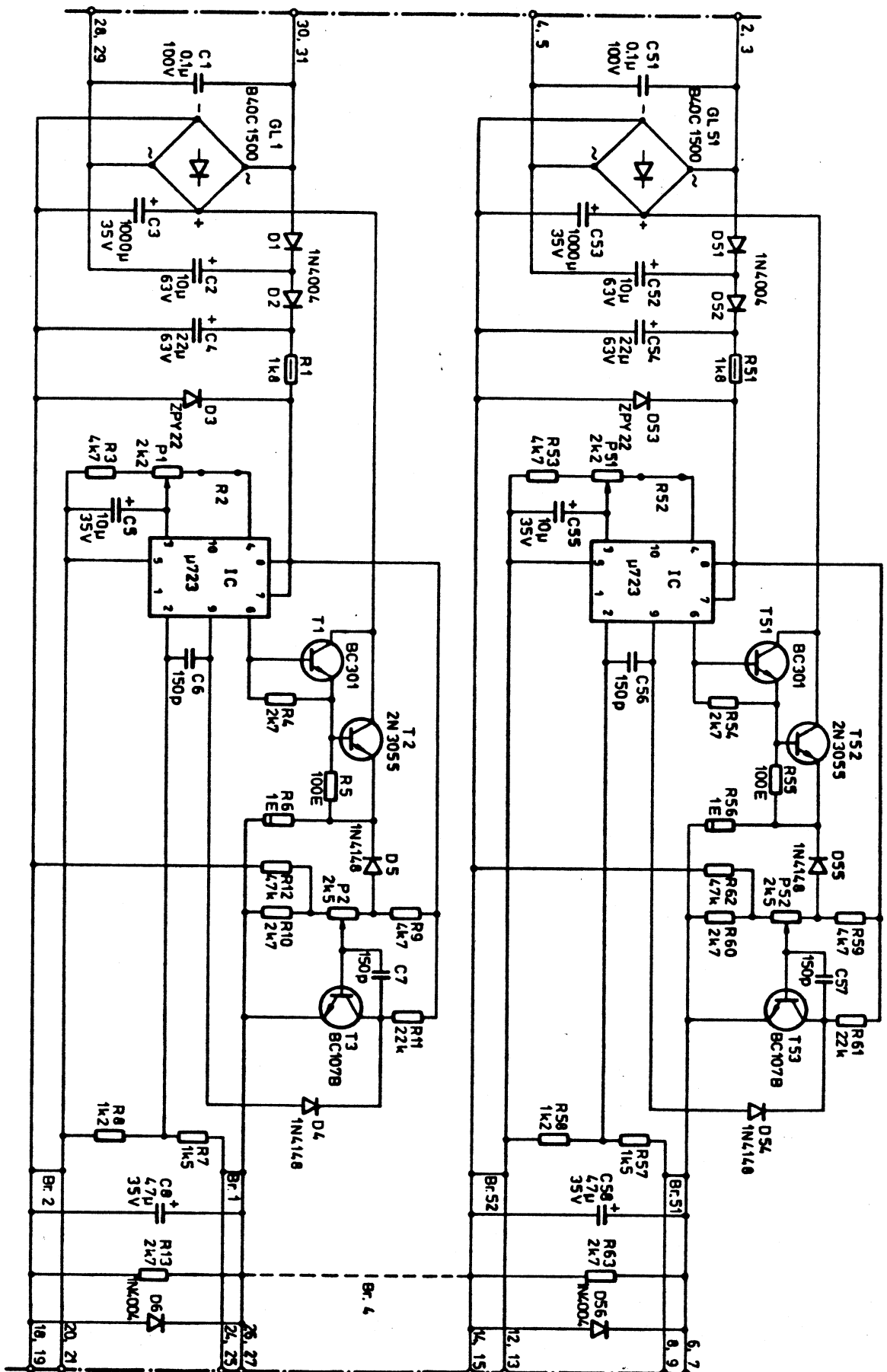
D C 0 A 10 22
 01-5115-00
 DATA INDUSTRIES AB
 MINI-ICE Z80



BACK VIEW



DATE	01.05.19	DATE	01.04.24	DATE	01.04.24
DESIGNER	A	DESIGNER	A	DESIGNER	A
CHECKED		CHECKED		CHECKED	
APPROVED		APPROVED		APPROVED	
DATAK STRIER AB POWER UNIT SCHROFF TYPE SN 601 4-6V 10A					
81-7010-00					



nicht bezeichnete Widerstände LCA 0309
LCA 0411
LCA 0719

A	610519	610425

DATAINDUSTRIE AB

POWER UNIT - SCHROFF
Type SN 11N 2x12V/1A

81-7011-00

