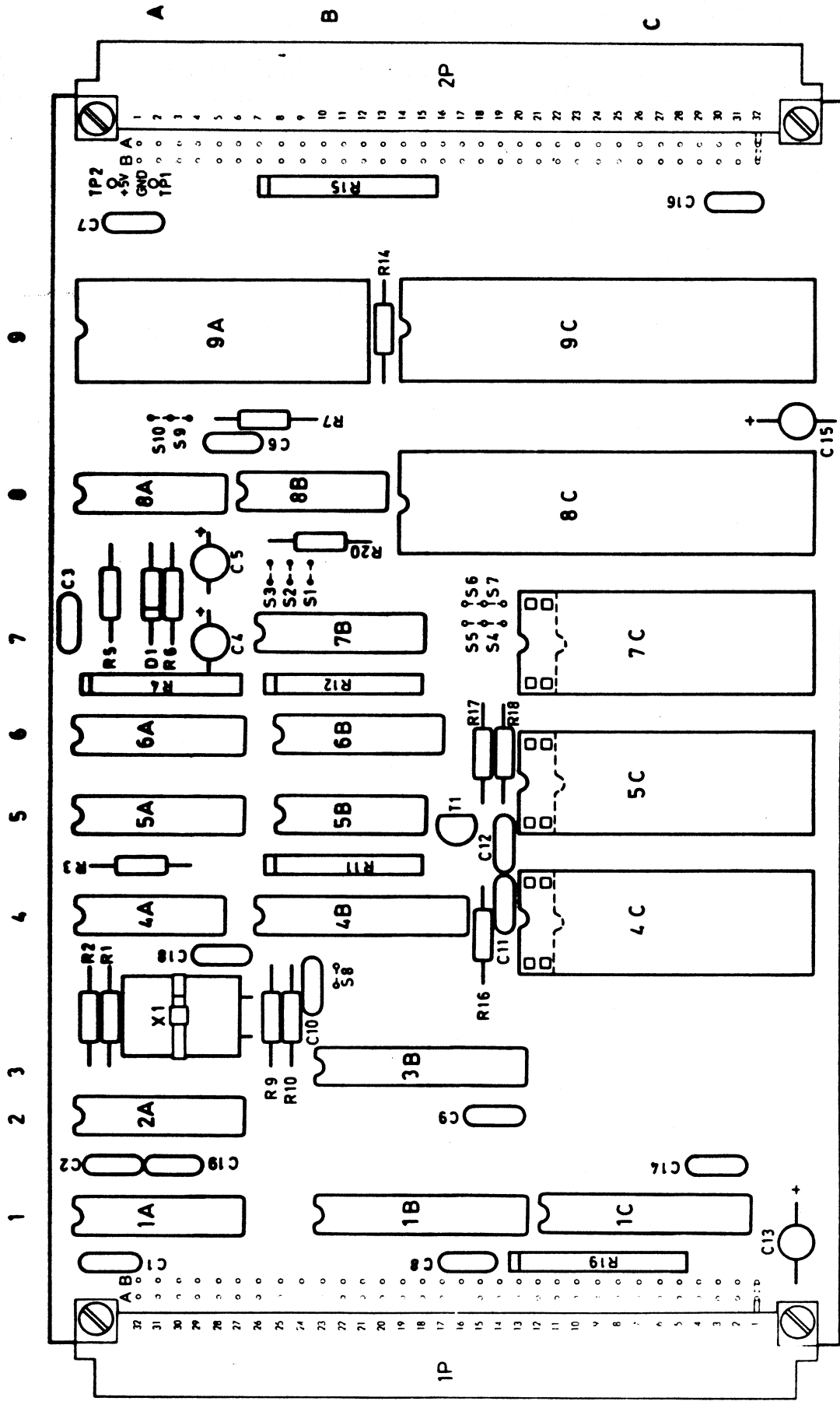


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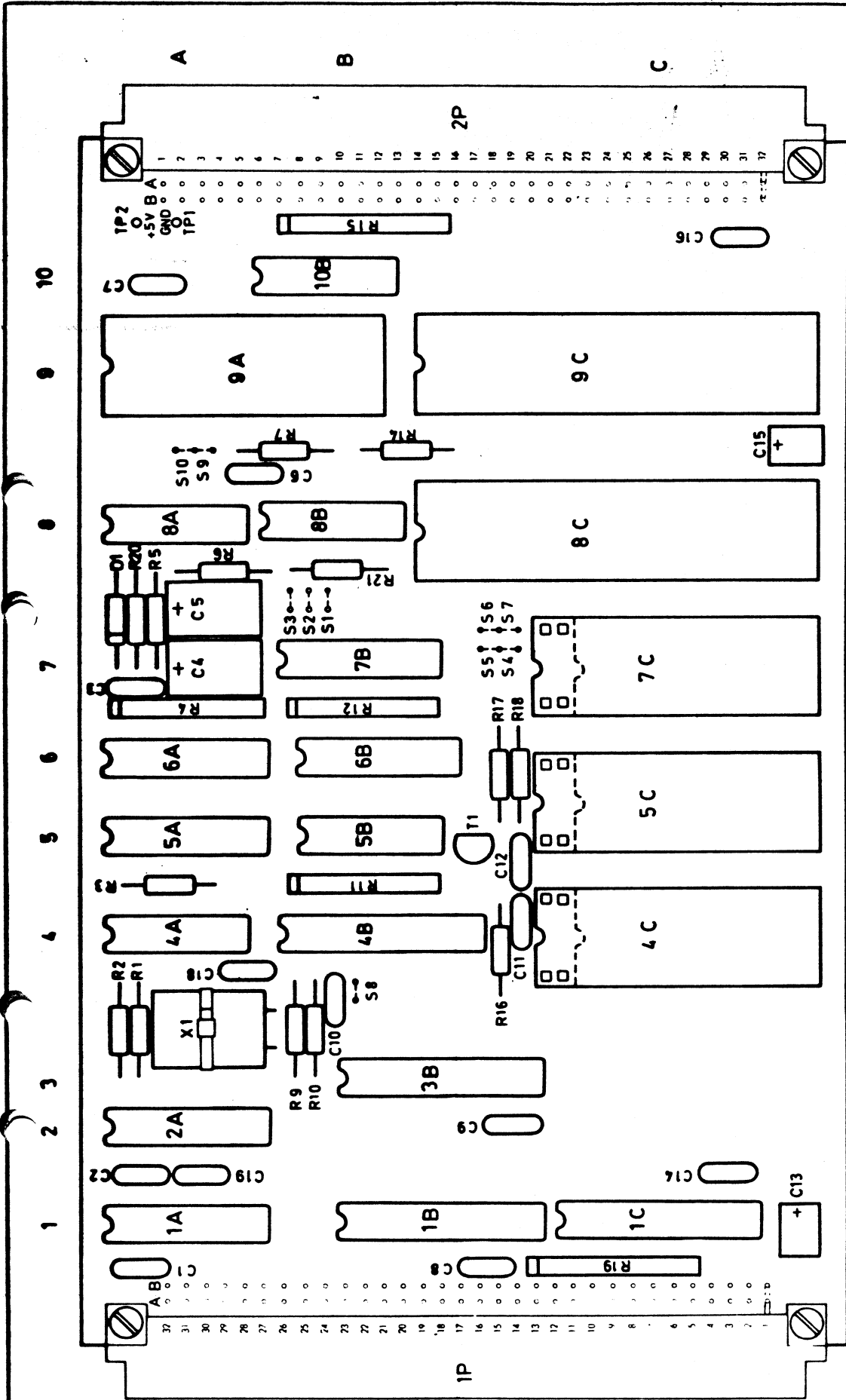
S8 c → no W-dog
 S8 o → W-dog
 S9 c, S10 o = bus interrupts disabled
 S9 o, S10 c = — enabled
 W 2764 i address 24k-32k

o = open
 c = closed

PROM type	RAM pos	Sirap	1	2	3	4	5	6	7
2732	8k RAM	ccc	cc	cc	cc	cc	cc	cc	cc
2732	2k RAM	ccc	cc	cc	cc	cc	cc	cc	cc
2764	2k RAM	ccc	cc	cc	cc	cc	cc	cc	cc
0	2k RAM	ccc	cc	cc	cc	cc	cc	cc	cc

PROM type	RAM pos	Sirap	1	2	3	4	5	6	7
2764	8k RAM	ccc	cc	cc	cc	cc	cc	cc	cc
2732	2k RAM	ccc	cc	cc	cc	cc	cc	cc	cc
2764	2k RAM	ccc	cc	cc	cc	cc	cc	cc	cc
0	2k RAM	ccc	cc	cc	cc	cc	cc	cc	cc

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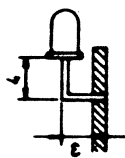


PROM type	RAM pos	Strap	1	2	3	4	5	6	7
2732	8k RAM	ccc	oc	oc	oc	oc	oc	oc	oc
2732	2k RAM	ccc	oc	oc	oc	oc	oc	oc	oc
2764	2k RAM	ccc	oc	oc	oc	oc	oc	oc	oc
0	2k RAM	ccc	oc	oc	oc	oc	oc	oc	oc

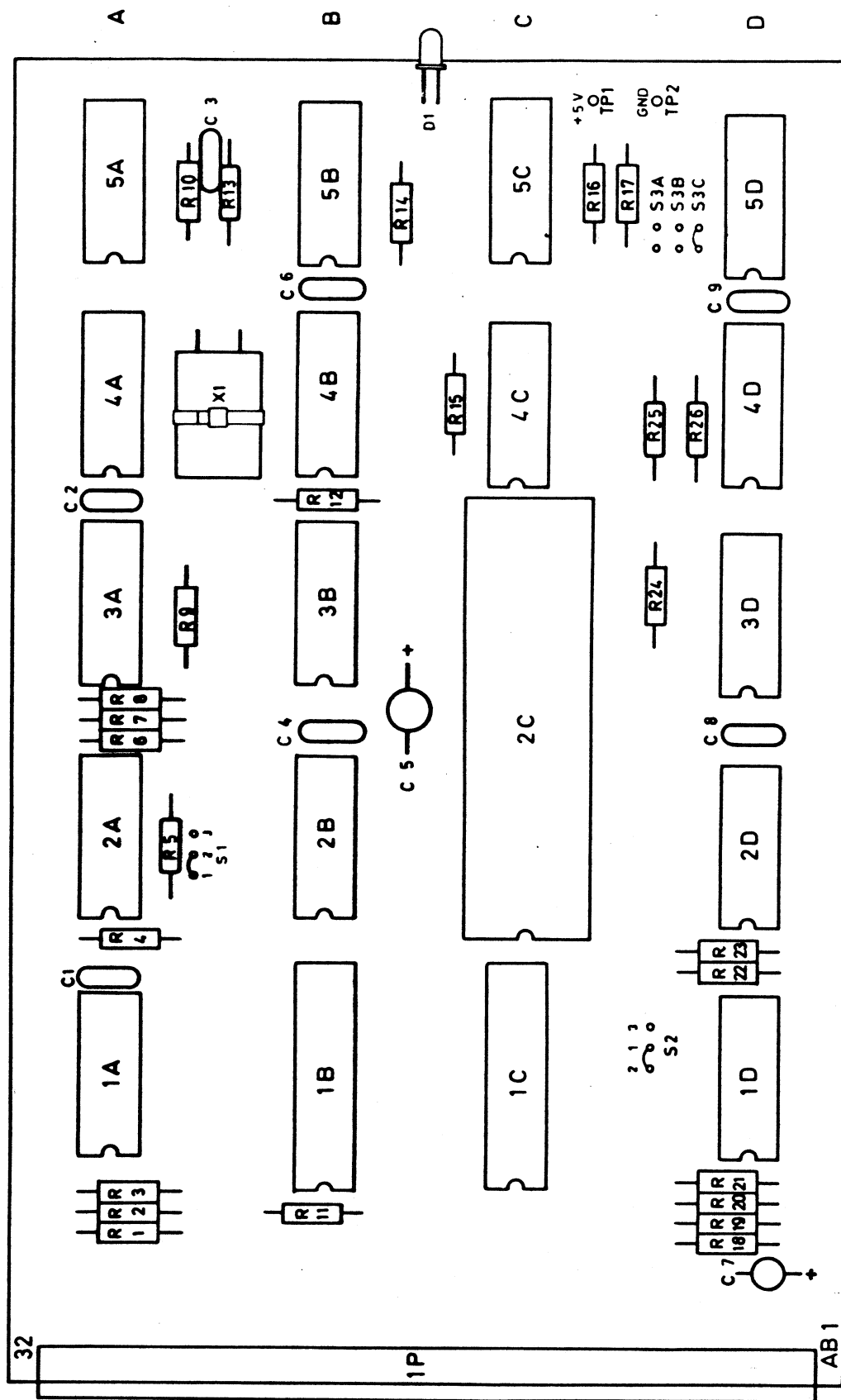
S8 c → no W-dog
 S8 o → W-dog
 S9 c S10 o = bus interrupts disabled
 S9 o S10 c = * — enabled
 * 2764 i address 24k - 32k

Oce-Ingen 24.0.07

[illegible]



1 2 3 4 5



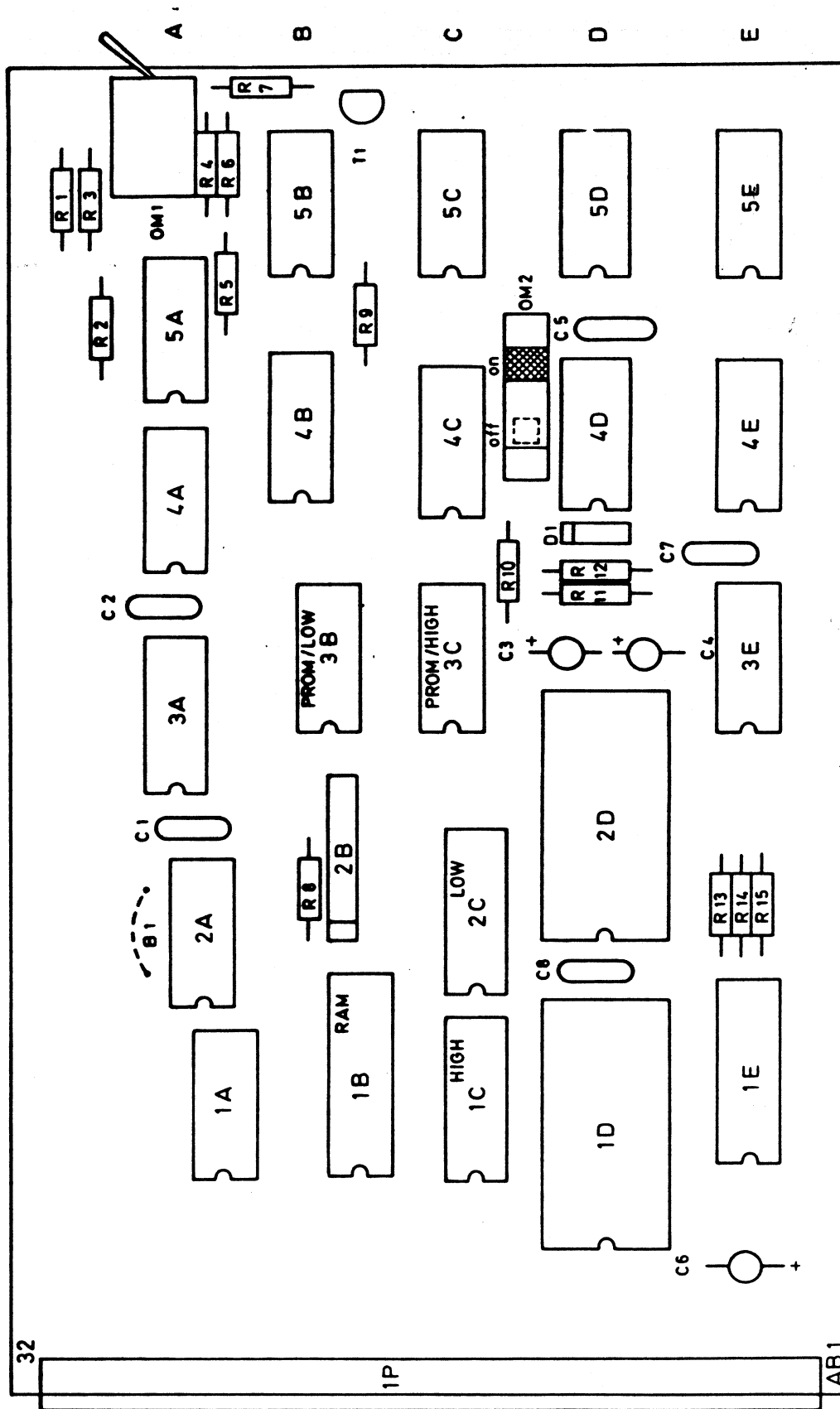
S1 Jumper 1 - 2 wait on M1 MREQ
 S1 Jumper 2 - 3 wait on all MREQ
 S2 Jumper 2 - 1 to pin 10 A
 S2 Jumper 1 - 3 to pin 10 A
 S3 A clock out X1/16
 S3 B clock out X1/8
 S3 C clock out X1/10

Default S1 2-1
 S2 2-1
 S3 C

D	C	B	A	REV. NR	OL	DATAINDUSTRIER AB
8306 09	8303 02	8205 17	81/08 05			SWEDEN
						TÄBY
						Z80 CPU

82-1043-00

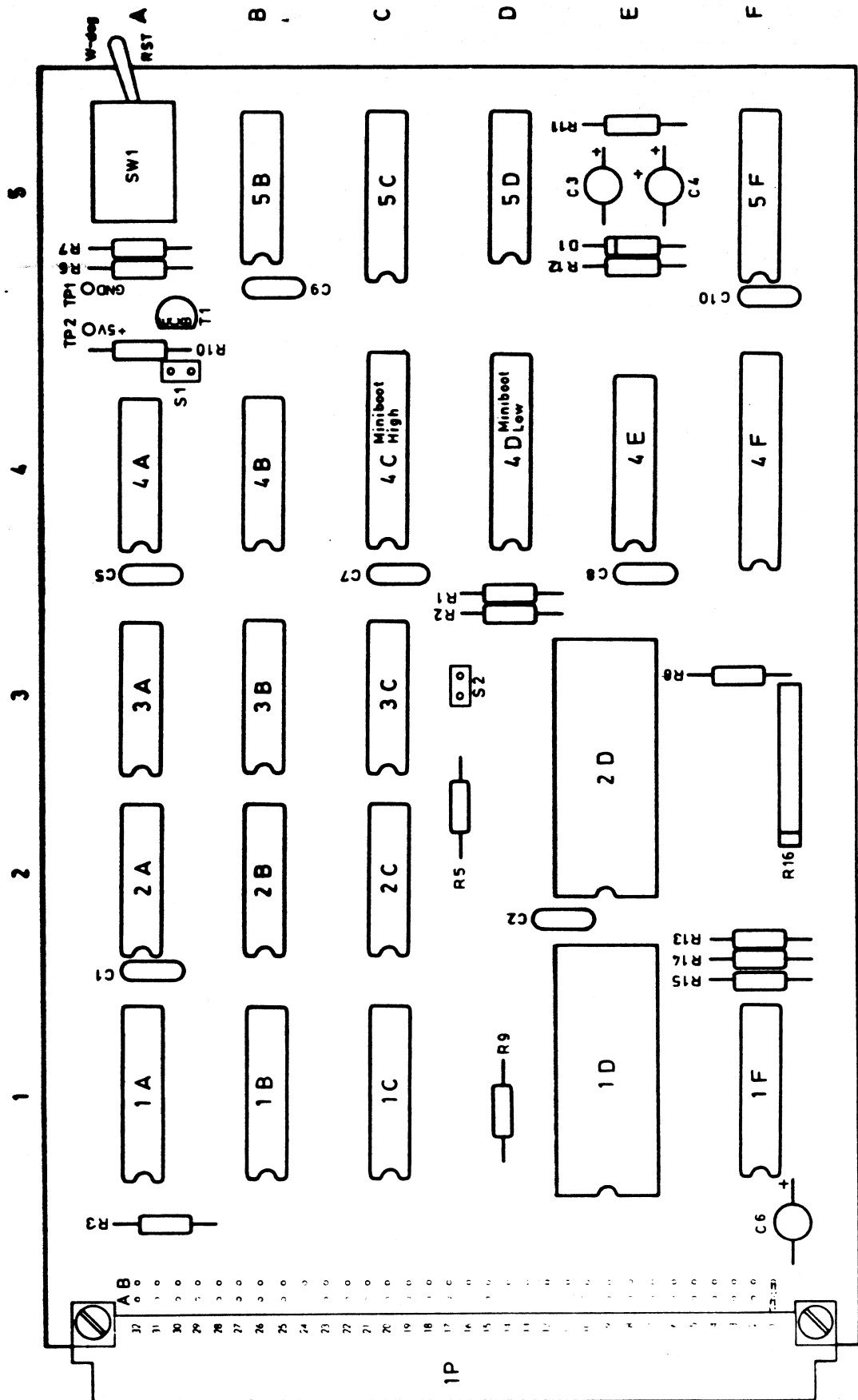
1 2 3 4 5



NOTE
 OPTIONS ARE PLACED AS FOLLOWS:
 BOOTSTRAP PROM ON LOC. 3B AND 3C
 TRACE-RAM DRIVER ON LOC. 2C AND 1C
 RAM ON LOC. 1B
 PROM SIZE IS SELECTED BY JUMPER B1
 INSTALLED = 1/2 K
 NOT " = 1 K

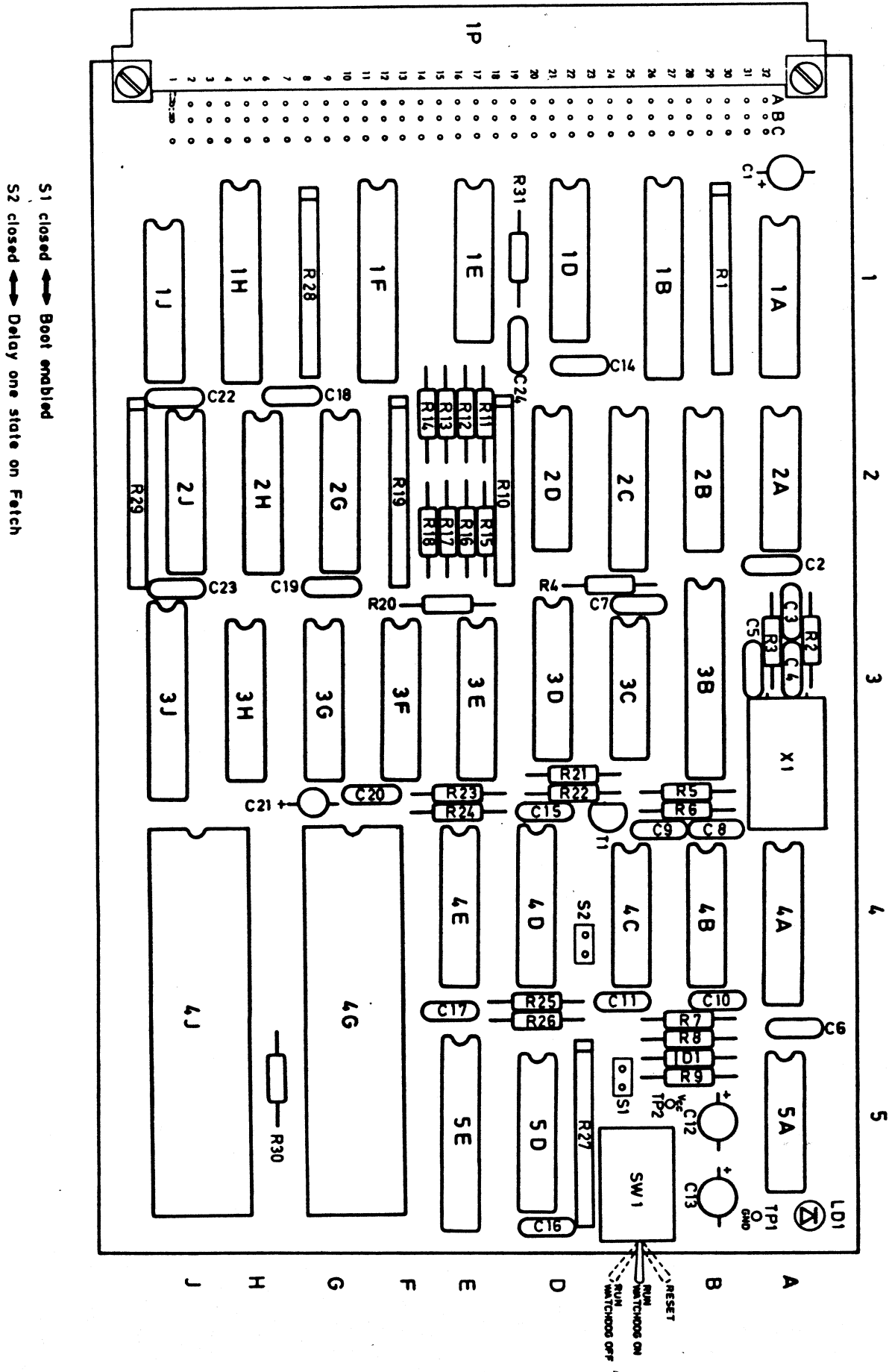
DATA INDUSTRIER AB		Z 80 CONTROL UNIT		82-1044-00
D	C	B	REV. NR	
81.04.05	80.12.02	79.03	DATUM	
TÄBY		SWEDEN		

1. The product shall not be repaired without the approval of the manufacturer and the customer. 2. The product shall not be repaired without the approval of the manufacturer and the customer. 3. The product shall not be repaired without the approval of the manufacturer and the customer.



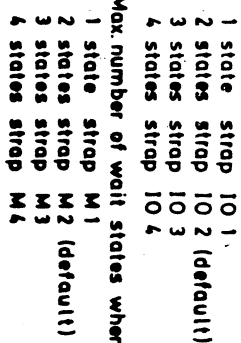
S1 Open: no miniboot at power on
S1 Shorted: miniboot at power on
S2 Open: 1 Kb miniboot
S2 Shorted: 512 b miniboot

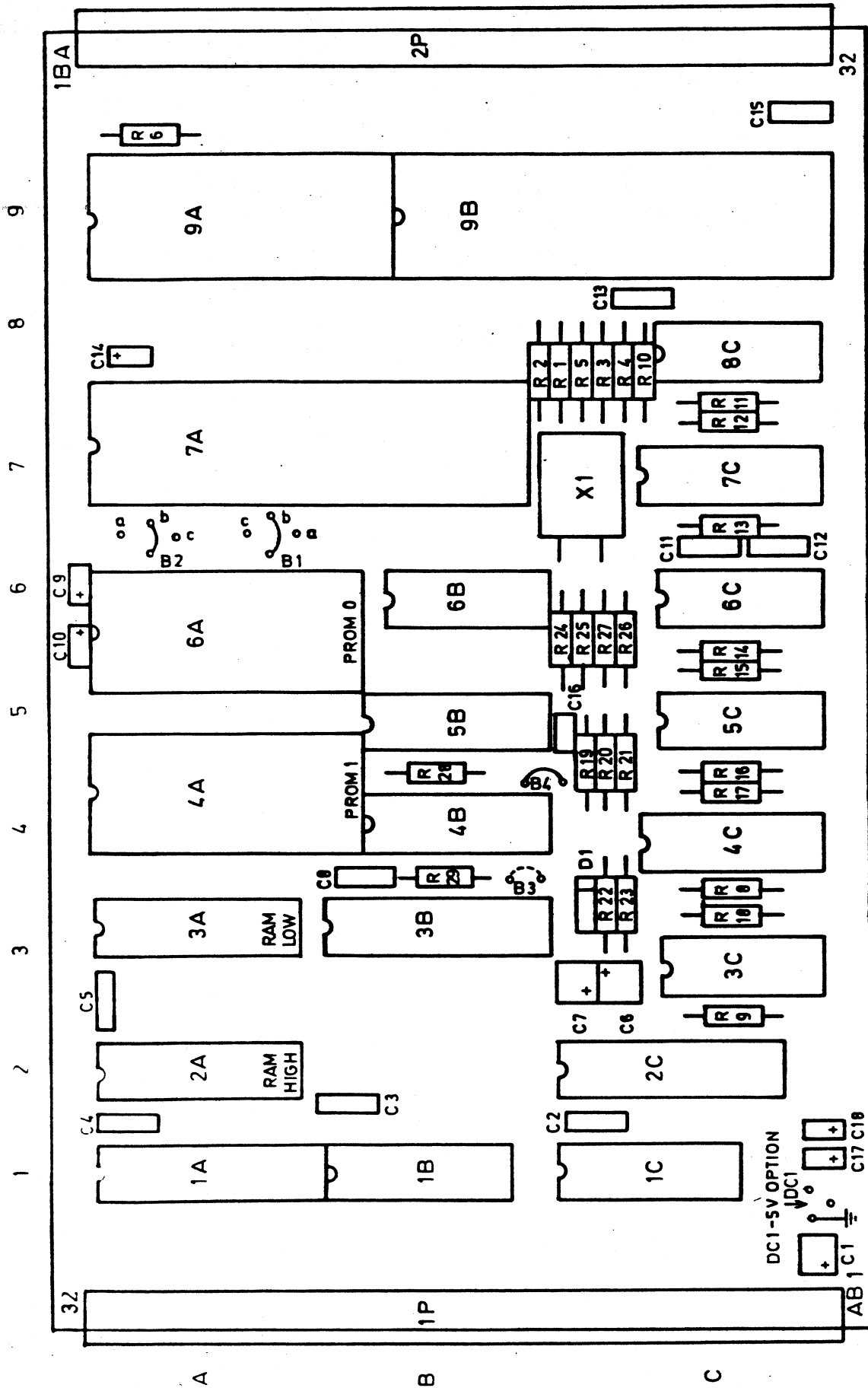
DATAINDUSTRIER AB		Z 80 CONTROL UNIT		82 - 1044 - 01	
SWEDEN					
REV	RIT	DATUM	61 06 03		



B	A	REV	INT	DL	DATA	SW	DATE	TIME
01.04.04	02.05.20	01.04.04	02.05.20	01.04.04	02.05.20	01.04.04	02.05.20	01.04.04

DATAINDUSTRIER AB
Z80 A MAPPED CPU
82-1045-00

[illegible]



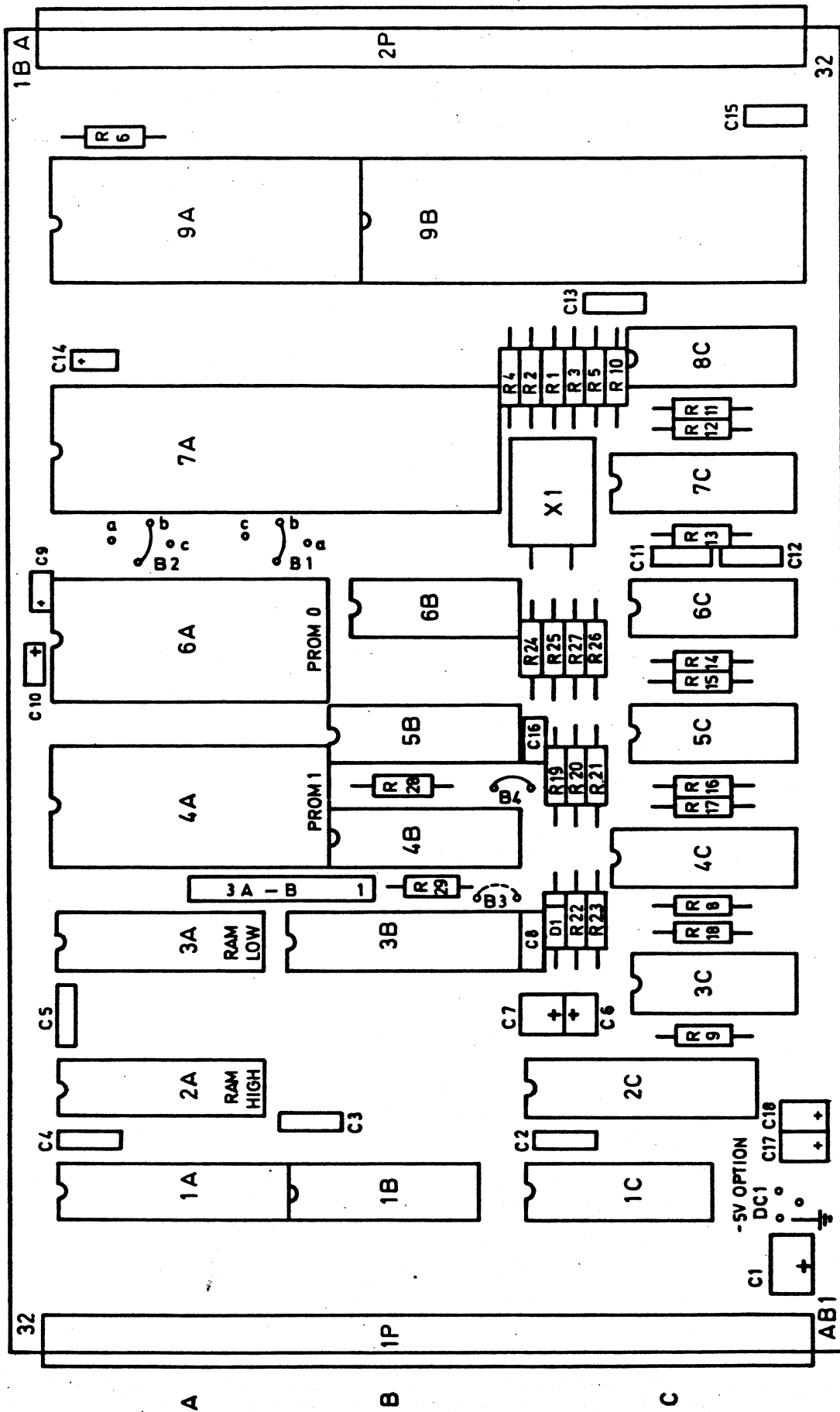
COMMENT: SELECT TYPE OF MEMORY BY JUMPERS B1 - B4.
 THE JUMPER AND JUMPER ALTERNATIVES NOT MENTIONED ARE ASSUMED OPEN.

PROM 0 SIGNIFIES THE LOW ADDRESS INTERNAL PROM MEMORY.
 RAM LOW RESP. HIGH ARE THE LOW (3-0) AND HIGH (7-4) ORDER
 BITS OF THE INTERNAL 1k RAM.

- 2756 ; INSTALL B1/a, B2/b, B3 AND B4.
- 2706 ; INSTALL B1/c, B2/c, B3 AND B4.
- 2716 ; INSTALL B1/b, B2/b AND B4.
- 2732 ; INSTALL B1/b, B2/a AND B3.

NO INTERNAL MEMORY BOTH B3 AND B4 SHALL BE OPEN.

B	A	REV NR	DATAINDUSTRIER AB	SINGLE BOARD COMPUTER	82-1057-00
81.04.09	79.03	DATUM	TÄBY SWEDEN	- P10	



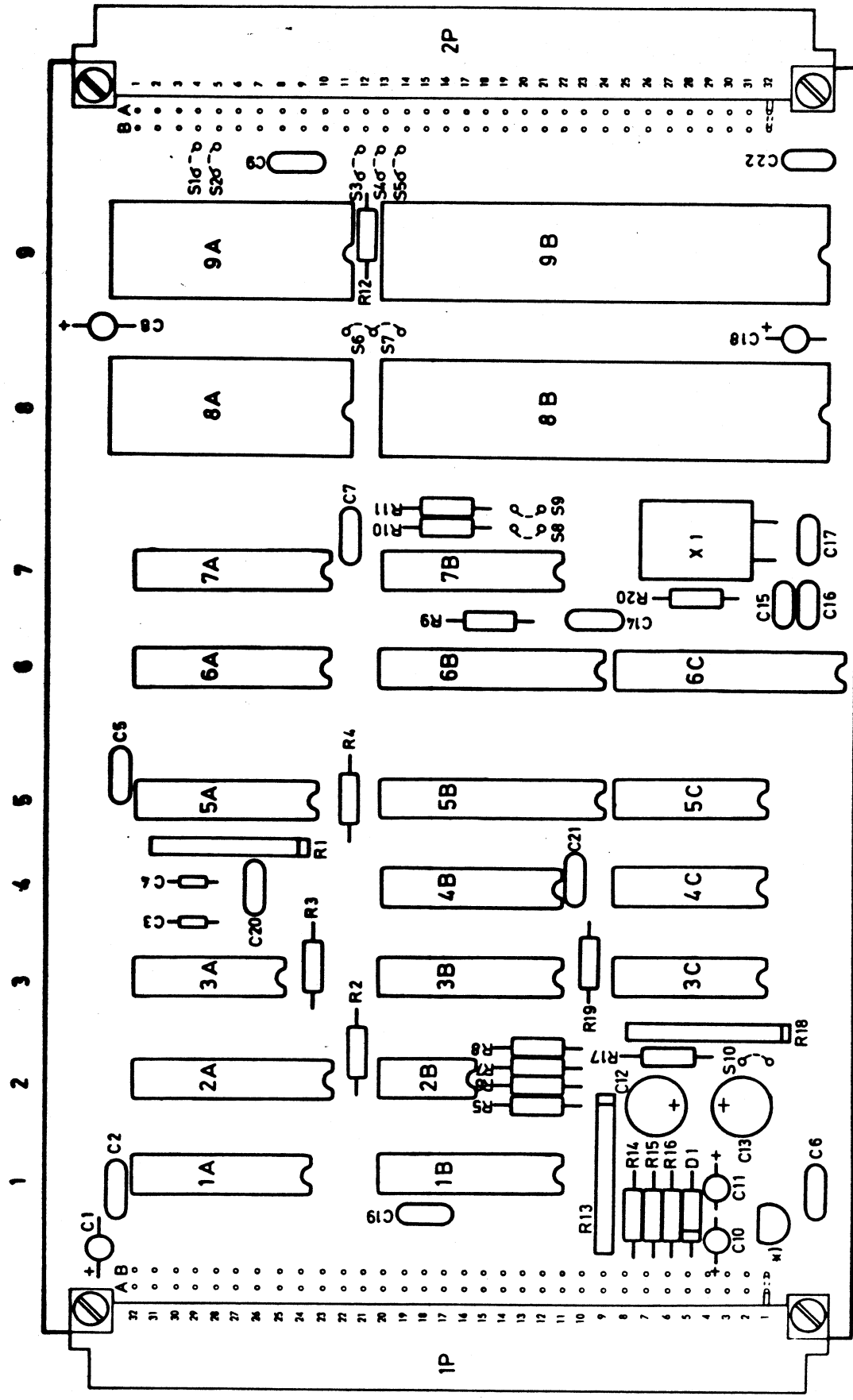
COMMENT: SELECT TYPE OF MEMORY BY JUMPERS B1 - B4.
 THE JUMPER AND JUMPER ALTERNATIVES NOT MENTIONED ARE ASSUMED OPEN.

PROM 0 SIGNIFIES THE LOW ADDRESS INTERNAL PROM MEMORY.
 RAM LOW RESP. HIGH ARE THE LOW (3-0) AND HIGH (7-4) ORDER
 BITS OF THE INTERNAL 1k RAM.

-2750; INSTALL B1/a, B2/b, B3 AND B4.
 -2706; INSTALL B1/c, B2/c, B3 AND B4.
 -2716; INSTALL B1/b, B2/b AND B4.
 -2732; INSTALL B1/b, B2/a AND B3.
 NO INTERNAL MEMORY BOTH B3 AND B4 SHALL BE OPEN.

B	A	REV NR	DATAINDUSTRIER AB	ONEBOARD CPU with SIO	82-1062-00
81-05-14	79.03	DATUM	TÄBY SWEDEN		

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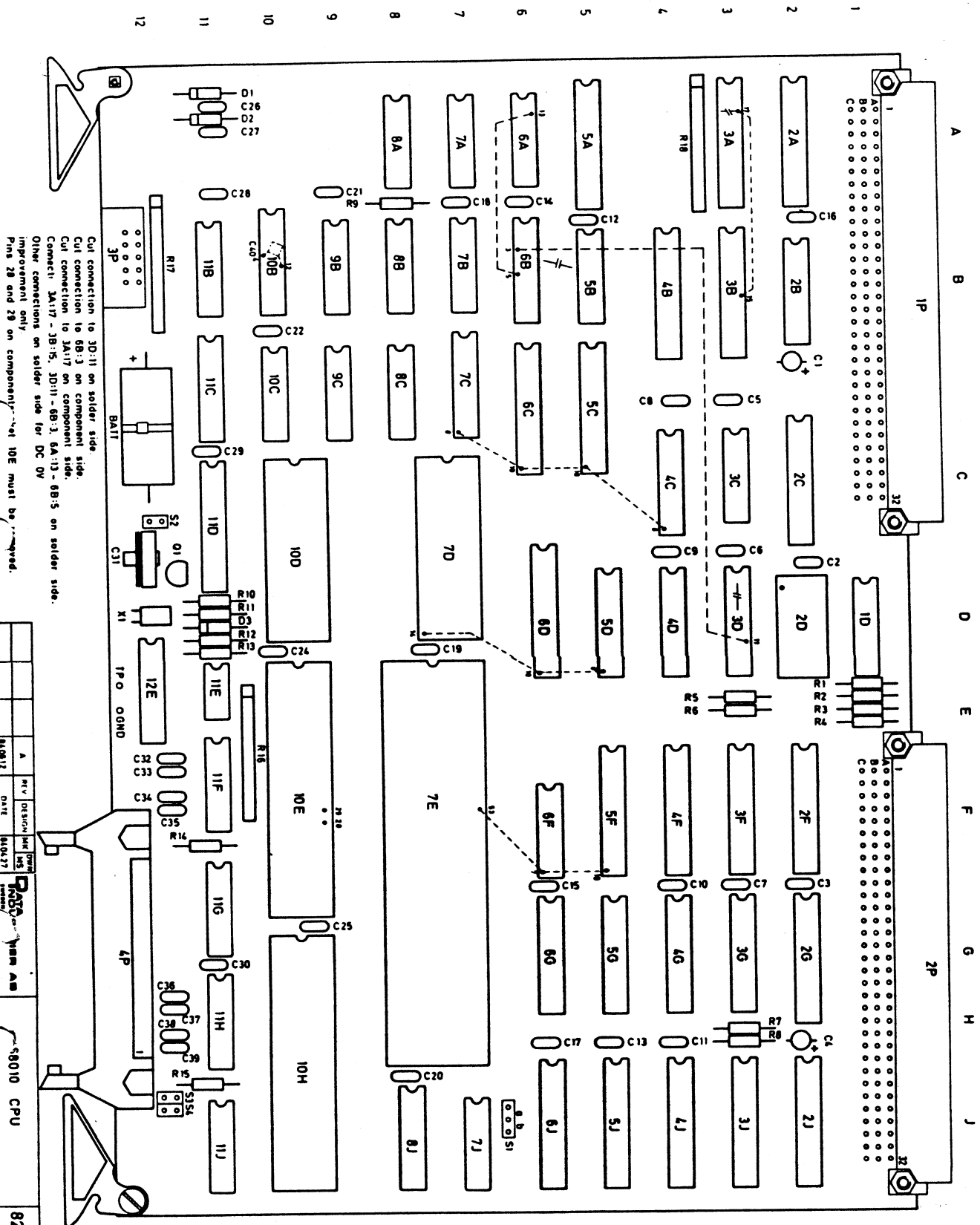
*) Option 79L05A

Strapping:

	S1	S2	S3	S4	S5	S6	S7	S8	S9
2708	C	O	O	O	C	C	O	C	C
2716	C	O	C	O	O	O	C	C	O
2532	O	C	C	O	O	O	C	C	O
2732	C	O	O	C	O	O	C	C	O
ext.prom	C	O	C	O	O	O	C	C	O
S10 open watchdog enable, S10 closed watchdog disabled	C	O	C	O	O	O	C	C	O

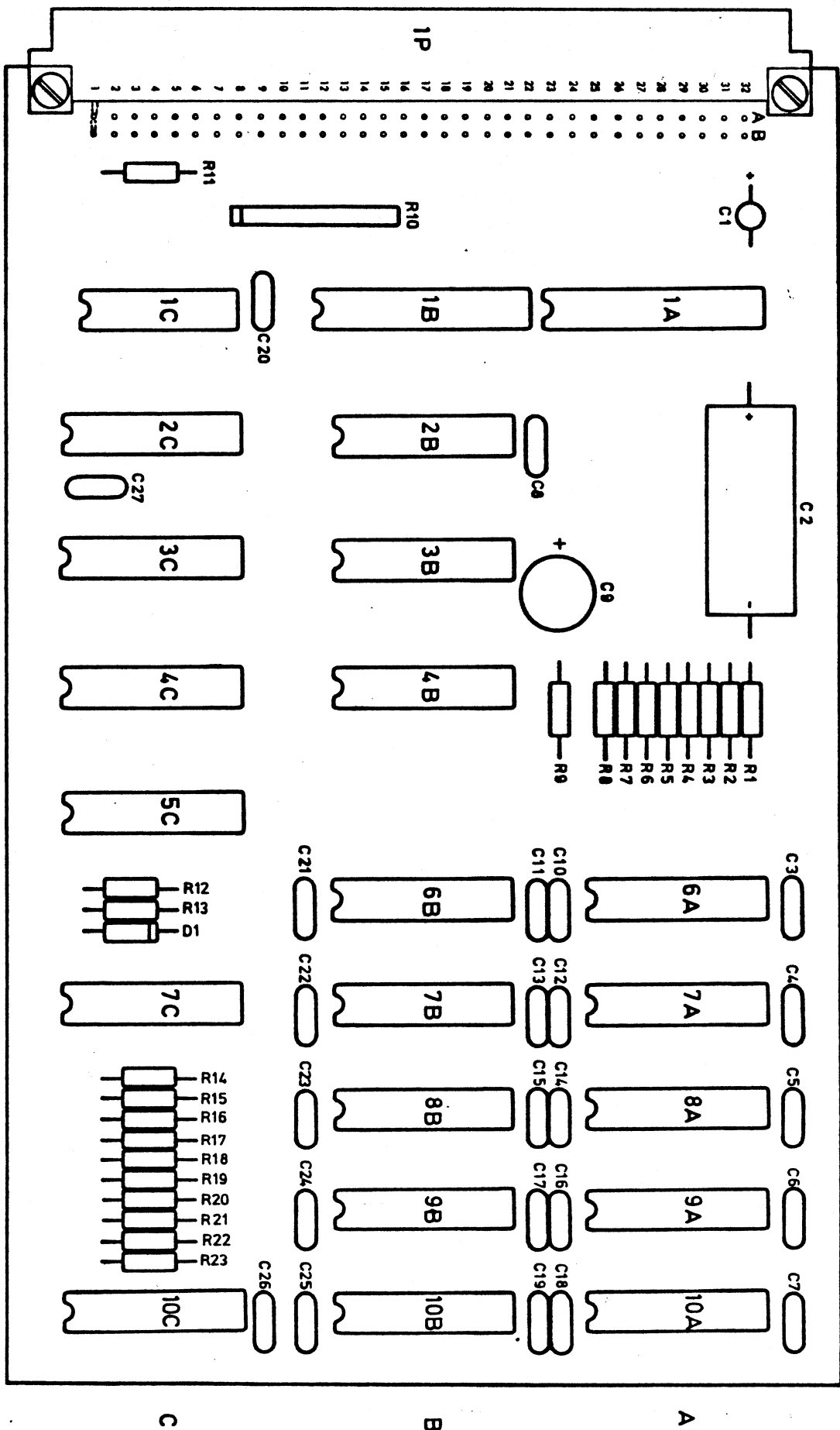
option 79L05A needed
default

c = closed
o = open



Cut connection to 3D-11 on solder side.
Cut connection to 6B-3 on component side.
Cut connection to 1A17 on component side.
Connect: 1A17 - 3B-5, 3D-11 - 6B-3, 6A-13 - 6B-5 on solder side.
Connect: connections on solder side for DC 0V
Other connections on solder side.
improvement only
Pins 28 and 29 on component side must be removed.

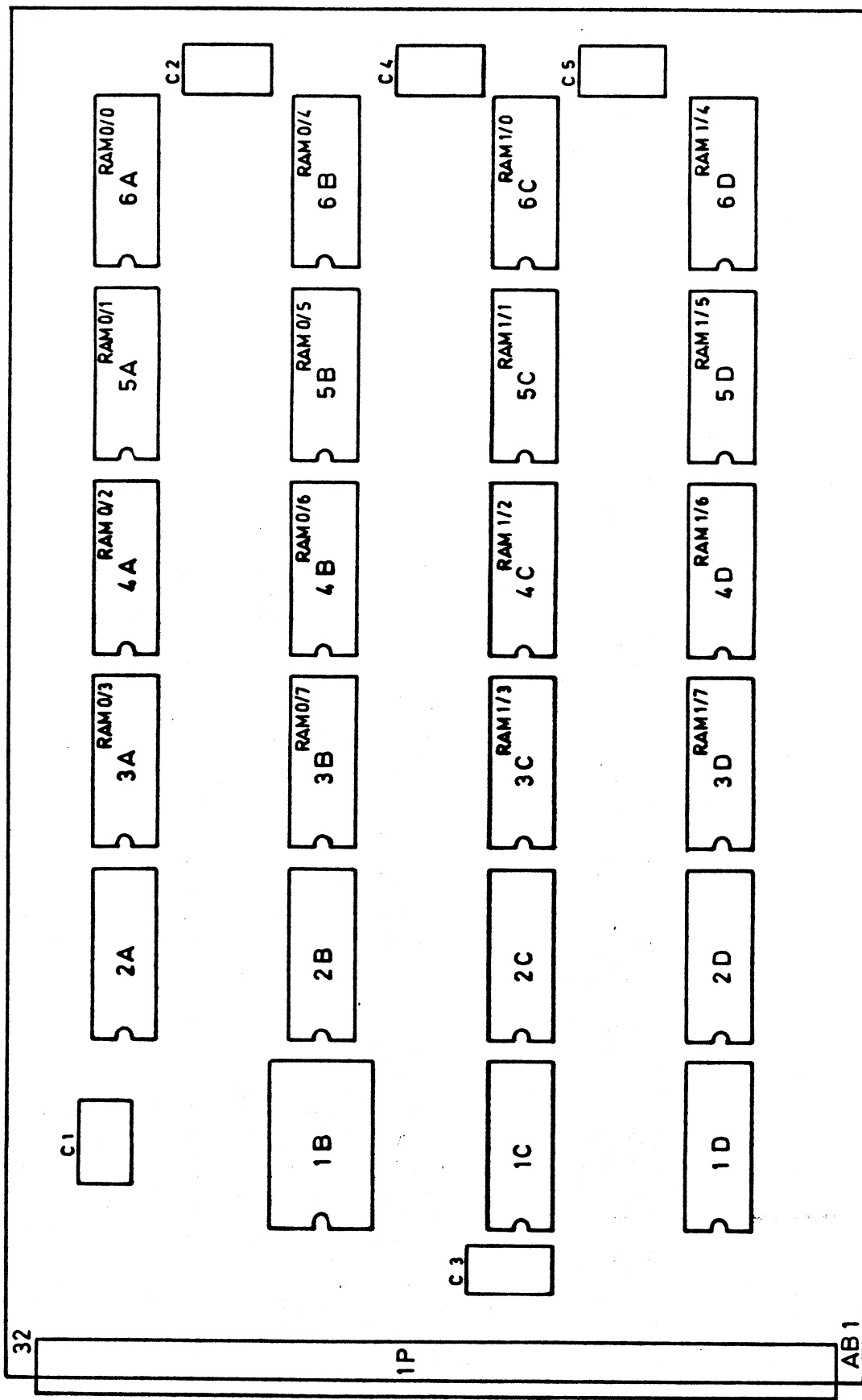
[illegible]



1 2 3 4 5 6 7 8 9 10

A B C

1 2 3 4 5 6



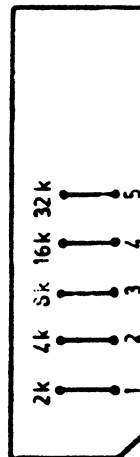
COMMENT : THE MODULE MEMORY IS PLACED AS FOLLOWS

0 - 1k ARRAY IN 6A - 3A AND 6B - 3B

1 - 2k " " 6C - 3C " 6D - 3D

6A AND 6C FOR BIT 0

JUMPER B1 INSTALLED - WAIT-STATES BY MEMORY



CODE PLUG FOR
BASE ADDRESS
LOC. 1B

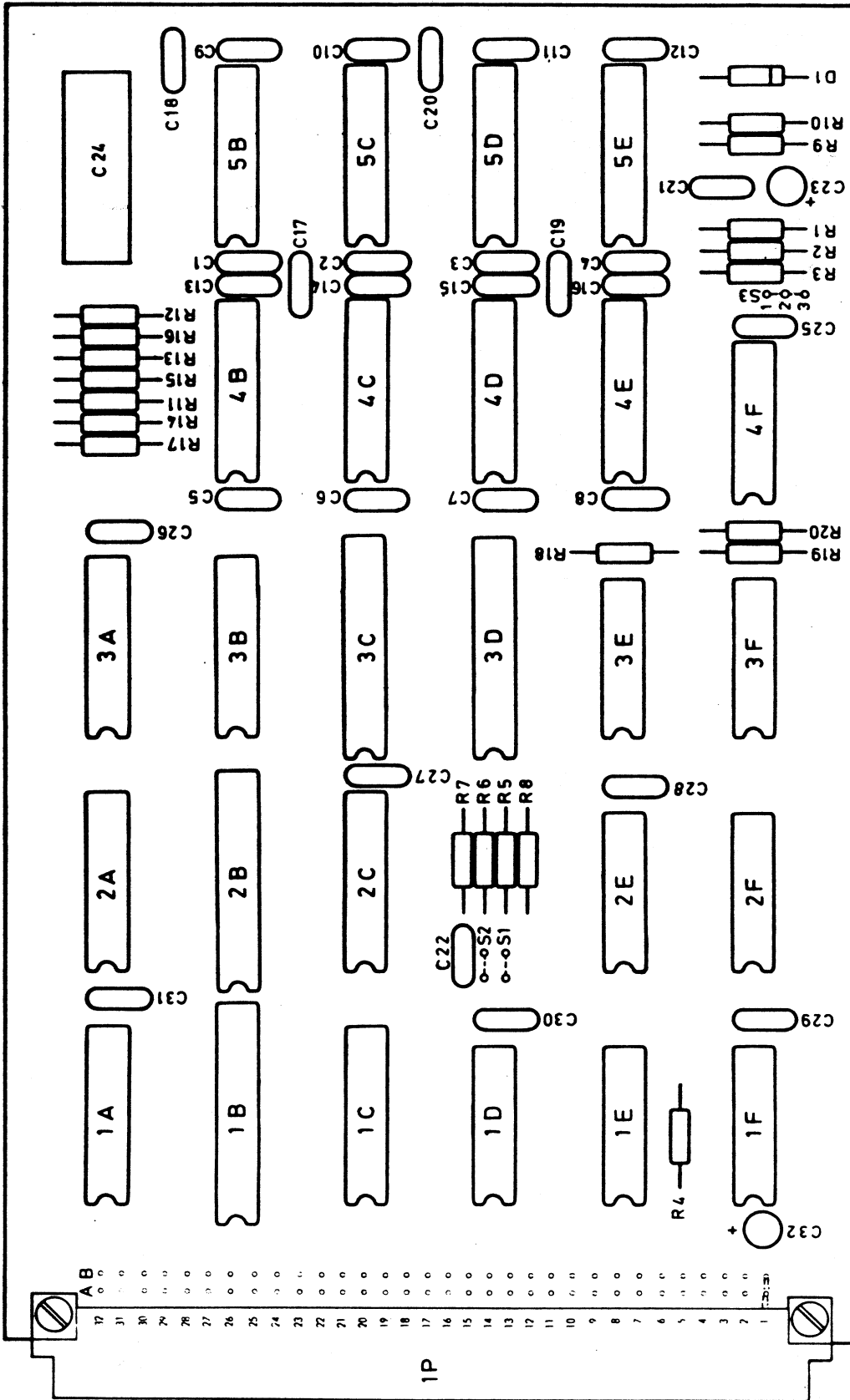
JUMPER B1 INSTALLED - WAIT-STATES BY MEMORY

B	REV. NR	DATAINDUSTRIER AB	TÄBY	SWEDEN
7903	DATUM			

2 K RAM

82-2002-00

1 2 3 4 5



Not: S3 Jumper 1-2 for Data Board 1043 CPU
S3 Jumper 2-3 for ABC 80 and Data Board 1062, 1057 CPU

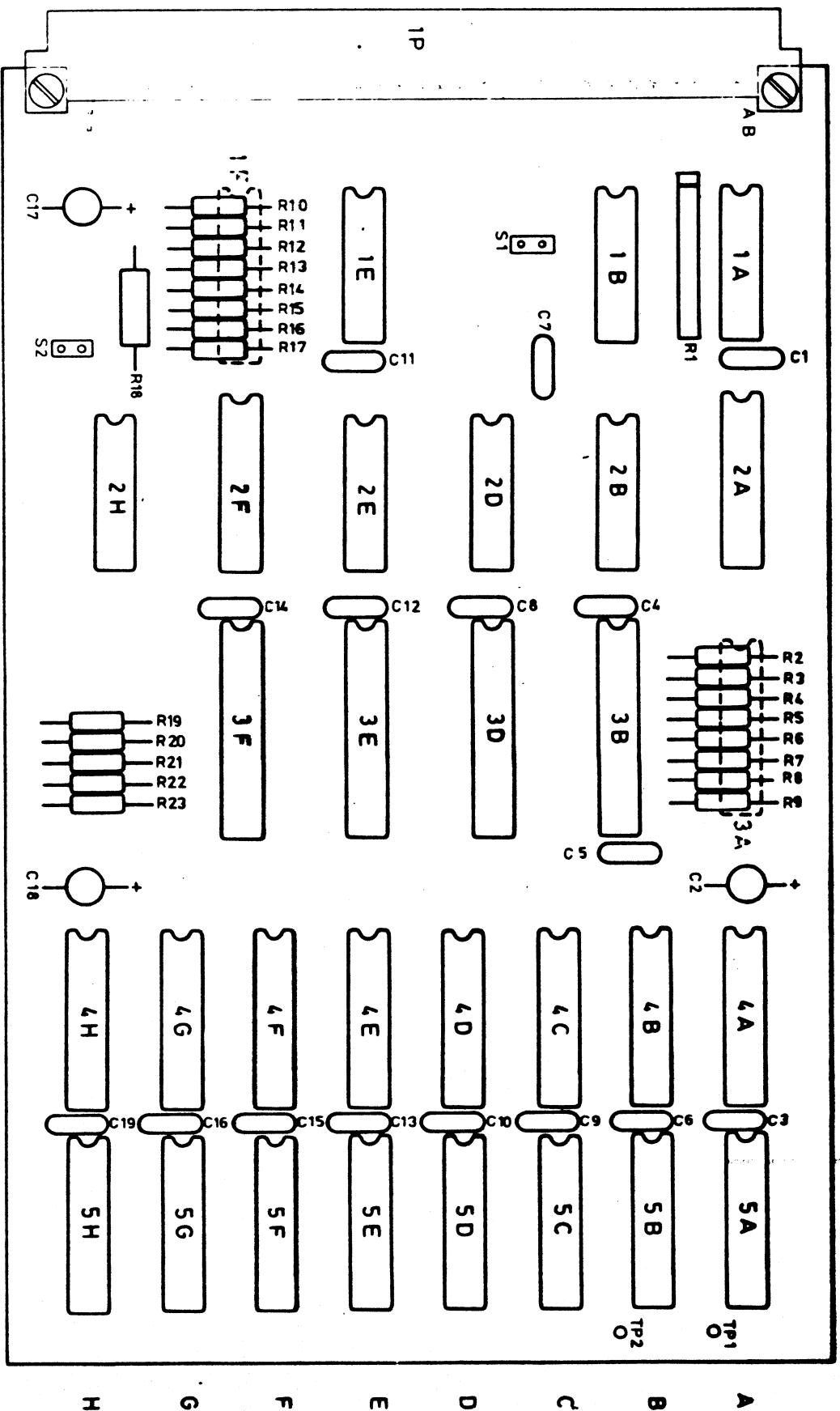
S2	S1	Base Address
0	0	oct not 000 HEX NOT
0	1	100:000 " " 4000 " "
1	0	200:000 " " 8000 " "
1	1	300:000 " " C000 " "

0 = Jumper
1 = No jumper

DATAINDUSTRIER AB	SWEDEN	REV	INT	DATUM	80 07 29
		A		81-02-25	81-01-15

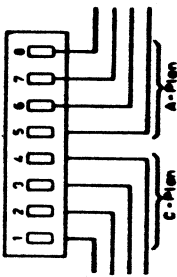
16 k REF - RAM

82-2003-00



Address space: S1 ON: 0 - 127
 S1 OFF: 128K - 255K
 S2 ON: Wait on instruction fetch (OPTION)
 S2 OFF: No wait
 Pos. 2 B: 256 refresh only (ex. TMX 416A.)

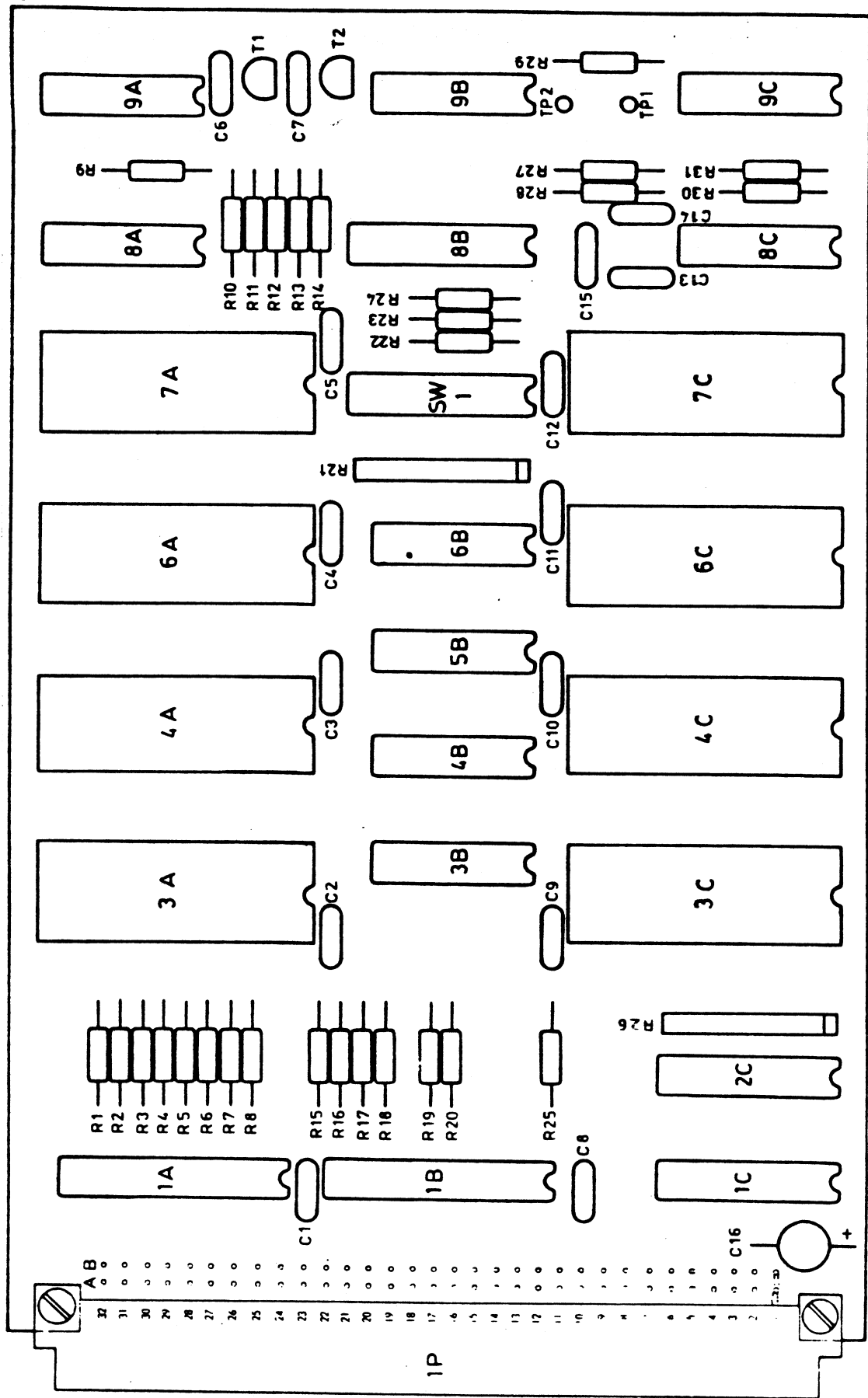
(39) (48)

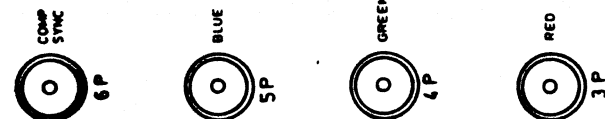


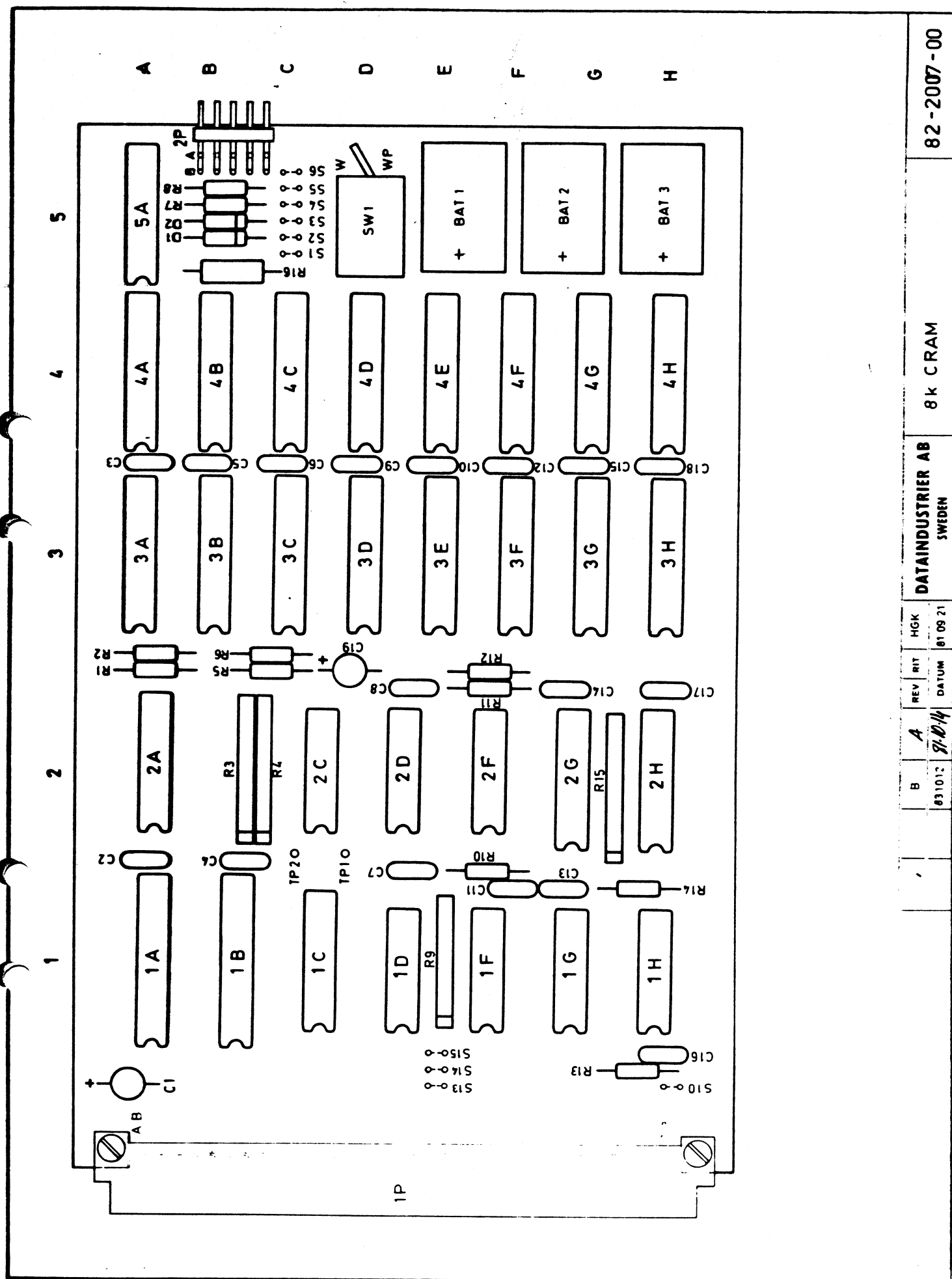
OFF = 1
ON = 0

2K (C-plan)	2K (A-plan)
1 Off	5 Off
2 Off	6 Off
3 Off	7 Off
4 Off	8 On

Byglingar för kort 2004/16, basadress: 0

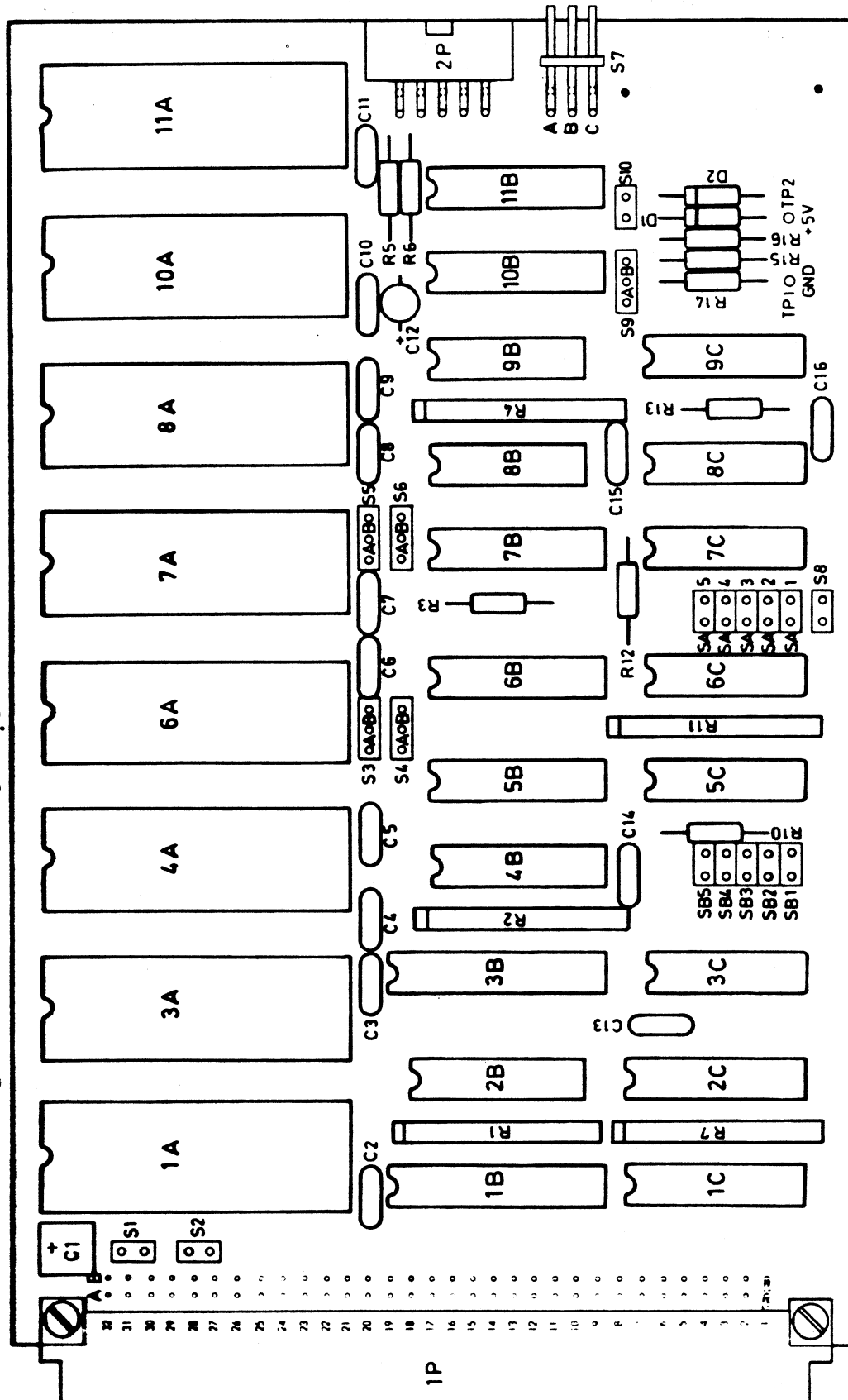






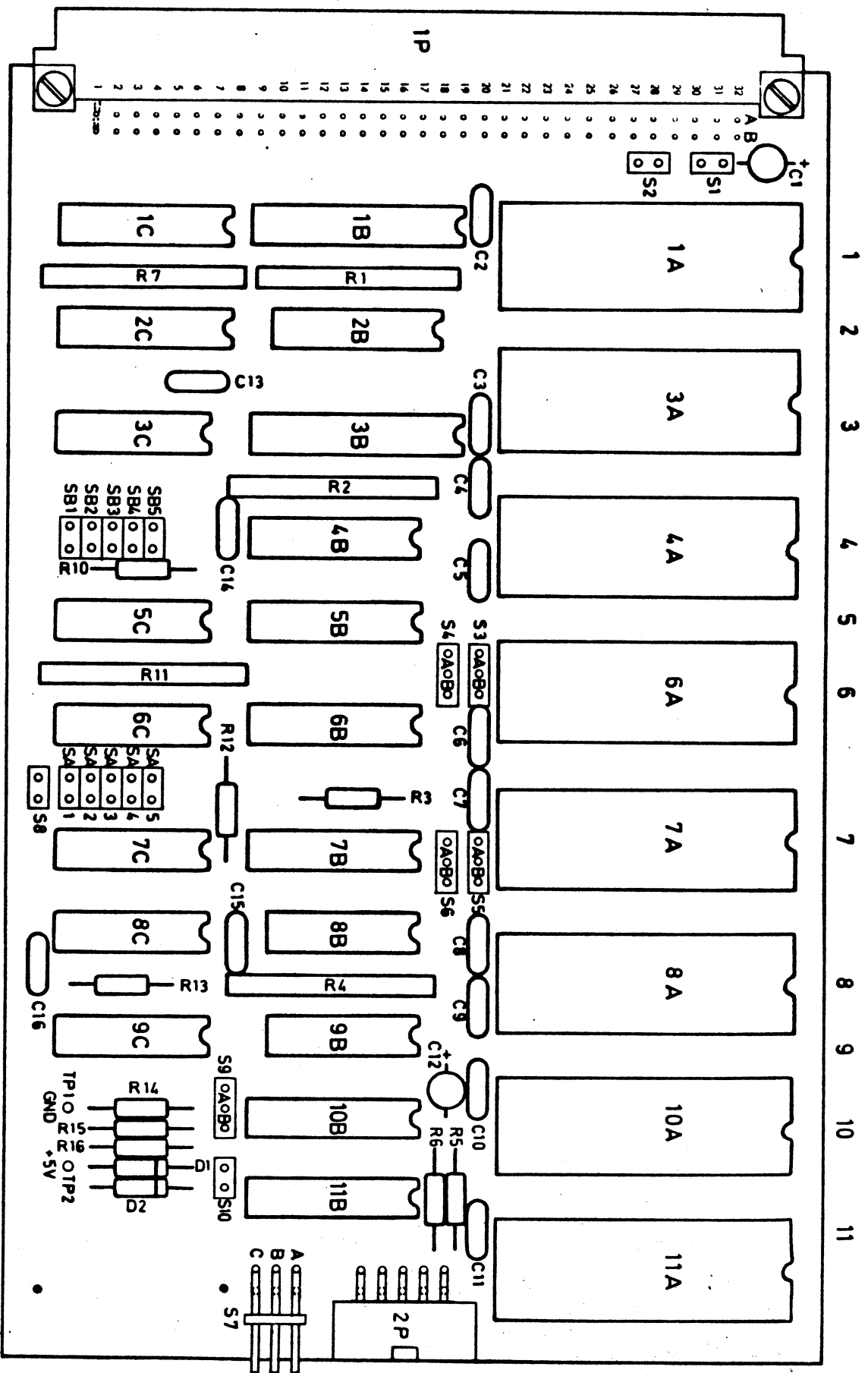
1. Företagets namn och adress
 2. Produktens namn och typ
 3. Beställarens namn och adress
 4. Beställningens datum
 5. Beställningens nummer
 6. Beställningens värde
 7. Beställningens status
 8. Beställningens kommentarer
 9. Beställningens signatur
 10. Beställningens datum
 11. Beställningens nummer

1 2 3 4 5 6 7 8 9 10 11



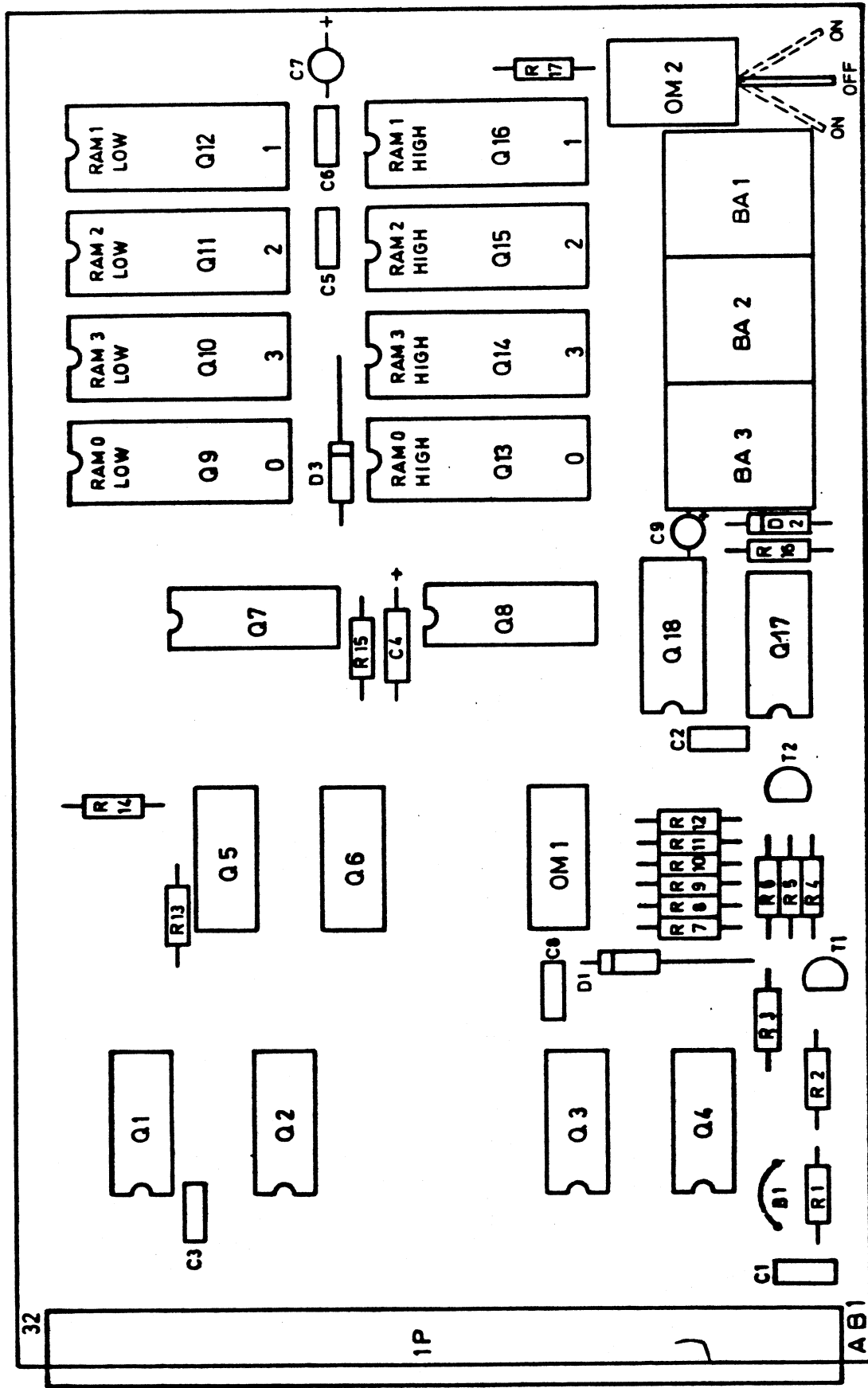
• tackas vid våglöddning/ Protect for wavesoldering

DATAINDUSTRIER AB		B - W 8 P C RAM		82-2009-00	
SWEDEN					
C	B	A	REV	MIT	HGK
84.03.26	83.03.01	82.12.28	DATUM	82.05.27	



• täckes vid våglöding/Protect for wavesoldering

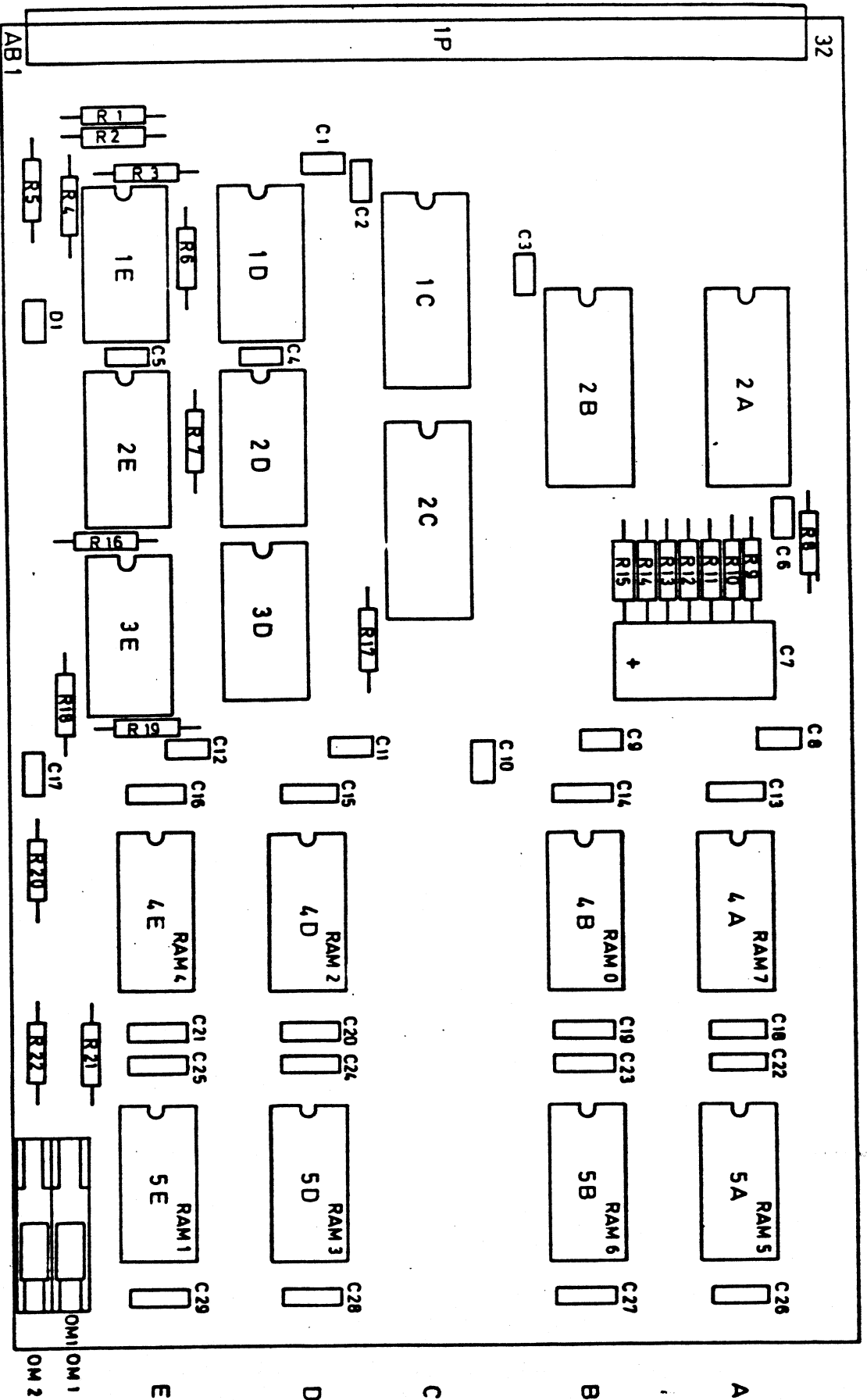
B	A	REV	INT	HGK	DATAINDI	RIER AB	SWISS	-W 8 P CRAM	82-2019-00
63 030	82 12 28	DATUM	82 09 27						



NOTE RAM 0, THE FIRST $1/4$ K, CORRESPONDS WITH SELECTED BASE ADDRESS JUMPER B1 INSTALLED - WAIT-STATE(S) BY MEM ROY *

CODE PLUG FOR
BASE ADDRESS,
LOC OM 1

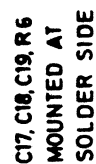
1 2 3 4 5



NOTE
RAM 0 CONTAINS THE LEAST SIGNIFICANT BIT (0)
OF THE MODULE ARRAY.

REV. NR	DATA	REVISION	DATE	82-2056-00
1	1	1	1	1
2	2	2	2	2
3	3	3	3	3
4	4	4	4	4
5	5	5	5	5

Version will be processed.



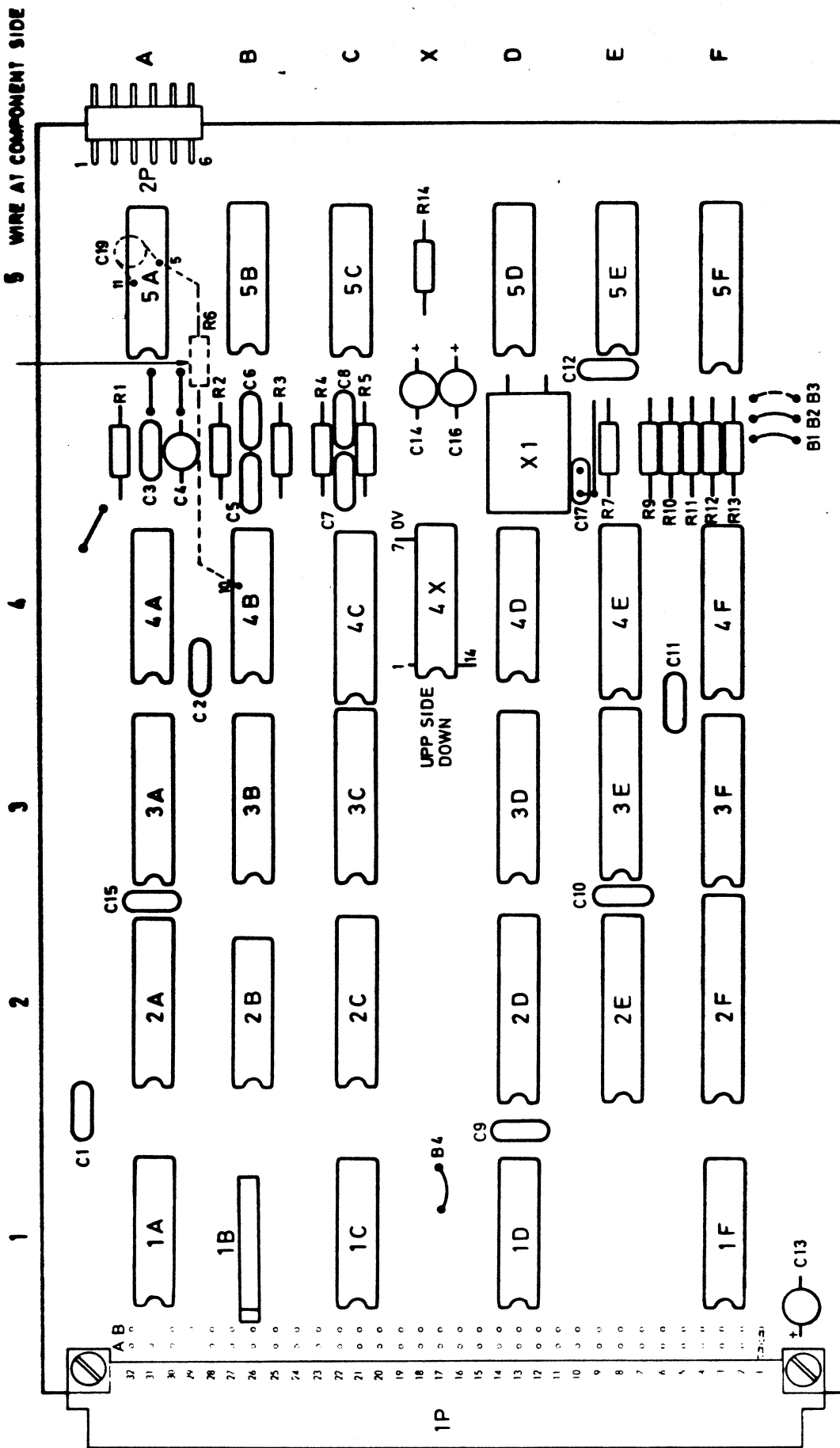
2k	4k	8k	16k	32k	1k
1	2	3	4	5	6

MUST BE MOUNTED AFTER A MODEL

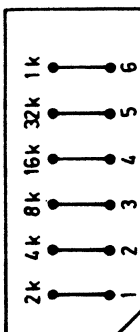
82-2066-00

1k SV RAM 64 x 16

8 WIRE AT COMPONENT SIDE



CODE PLUG FOR
BASE ADDRESS
LOC. 1A



COMMENT: THE 1k MEMORY ARRAY IS PLACED IN

2 D, BITS 3-0

2E, BITS 7-6

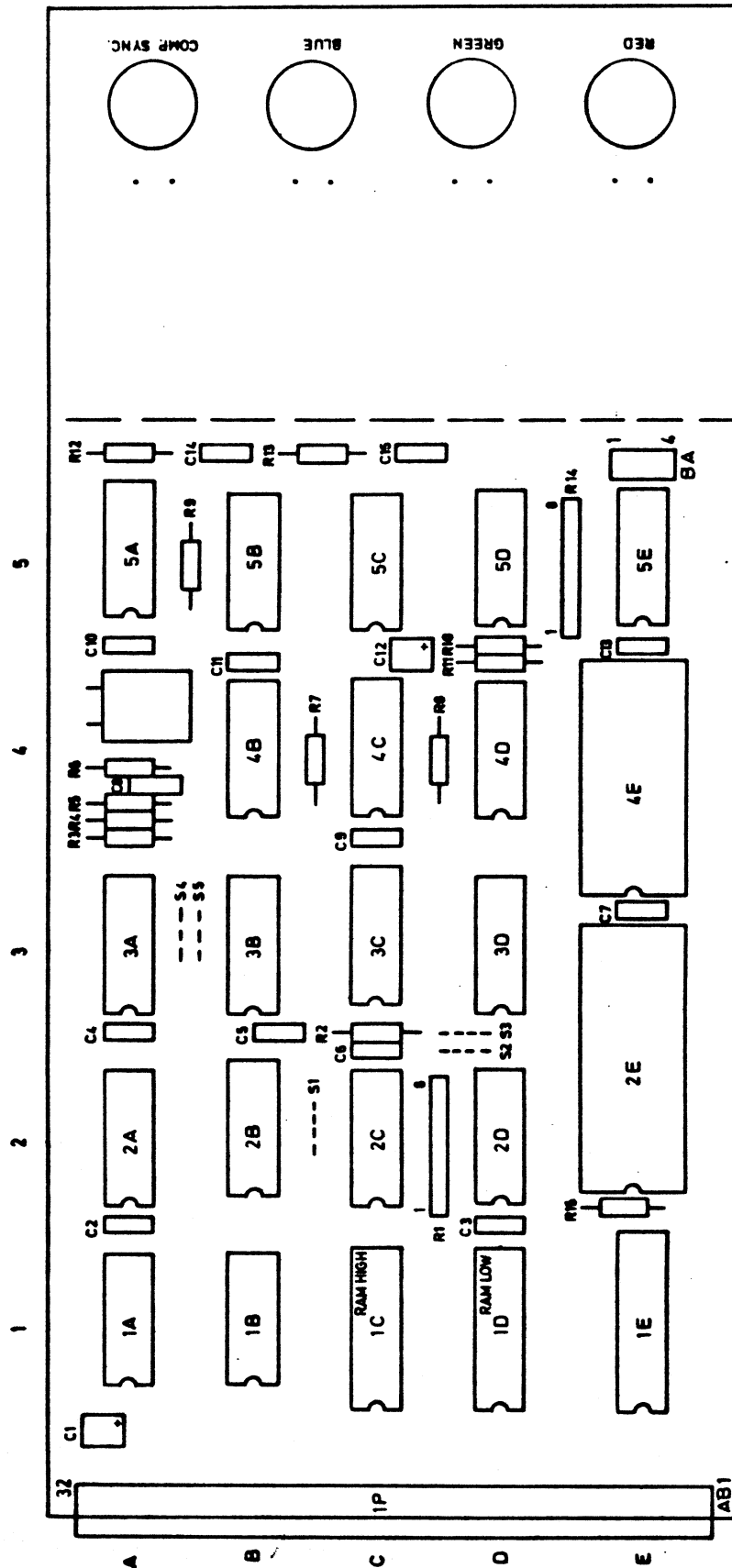
JUMPER B4 INSTALLED = WAIT STATES BY MEMRDY *

C17, C18, C19, R6 MOUNTED AT SOLDER SIDE

MUST BE MOUNTED AFTER A MODEL

		A	REV	RIT	HGK	DATAINDUSTRIER AB SWEDEN
		83 10 31	DATUM	83.01.23		

1k SV RAM 40 x 12	82-2066-10
-------------------	------------



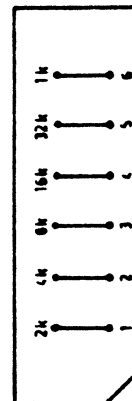
COMMENT JUMPER S1 INSTALLED - MEMREQ # CONNECTED

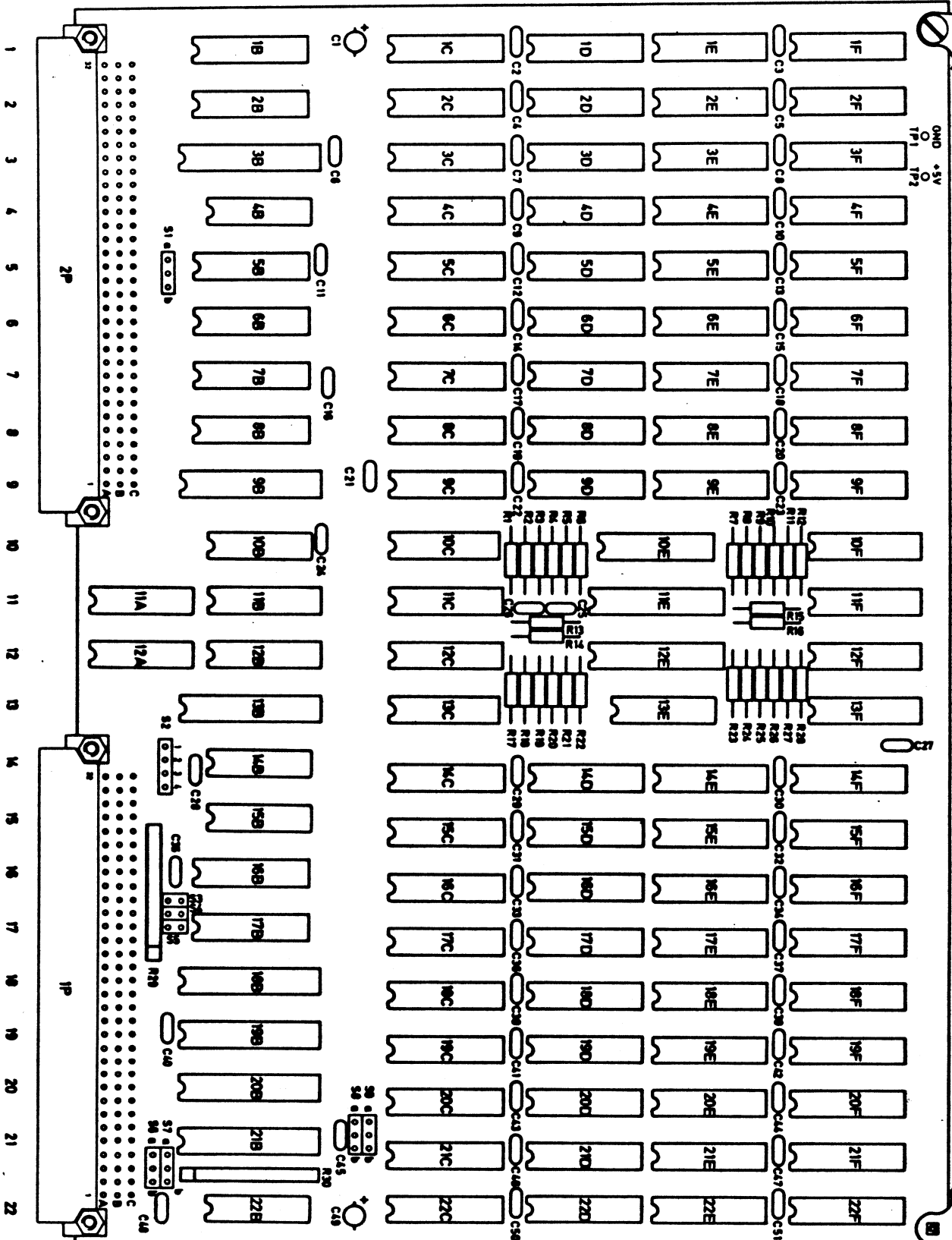
- S2 • • COLOUR OUTPUT
- S3 • • BLACK / WHITE OUTPUT (ON P2 : 2B BLUE)
- S4 • • COMPOSITE SYNC
- S5 • • NEGATIVE SYNC

TROM IS PLACED ON LOC 2E

TIC • • • • 4E

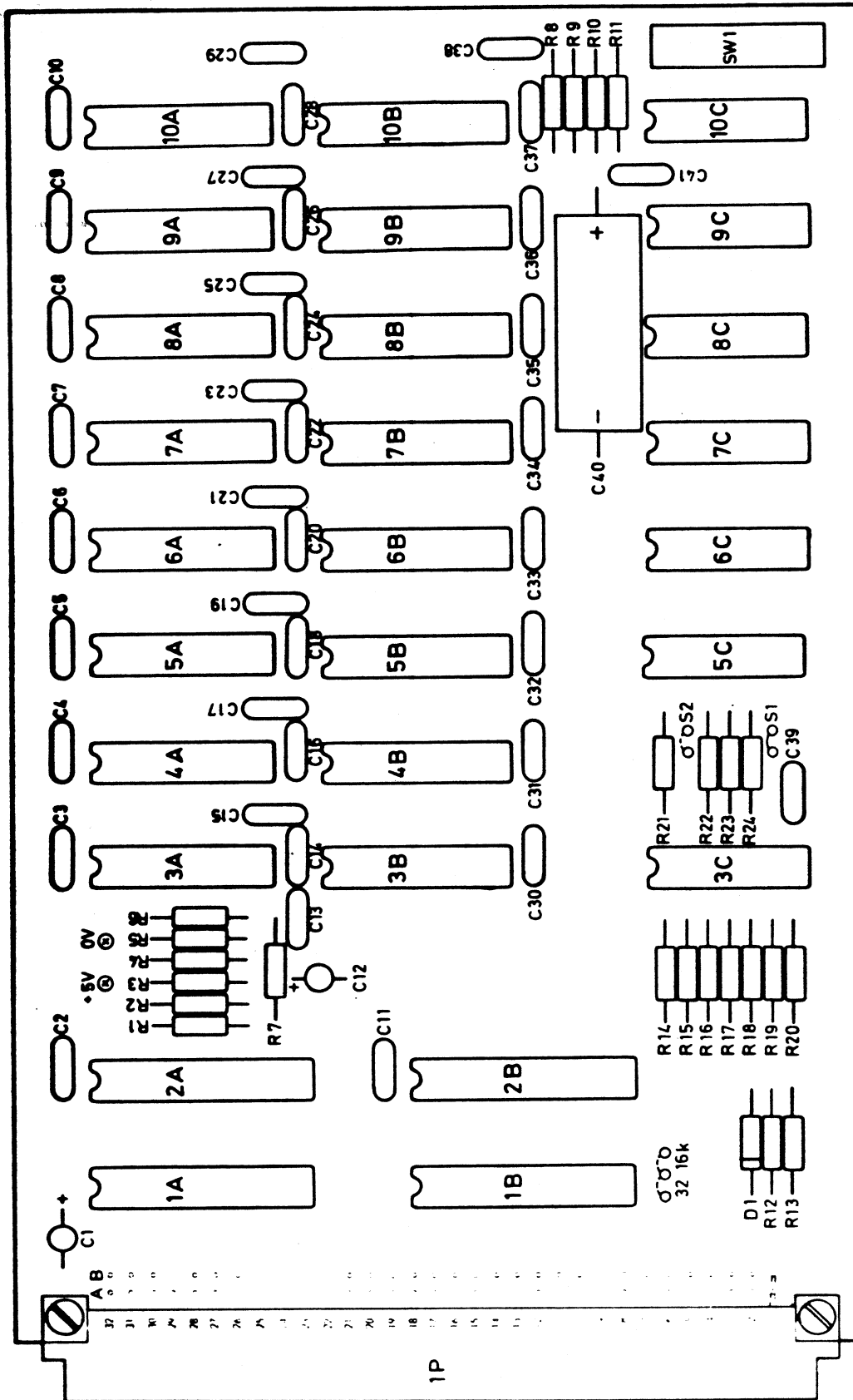
CODE PLUG FOR
BASE ADDRESS
LOCATION 3D





1. The board may not be repaired without the approval of the manufacturer.
2. The board may not be repaired without the approval of the manufacturer.
3. The board may not be repaired without the approval of the manufacturer.

1 2 3 4 5 6 7 8 9 10



Cover strapping options S1, S2, 16k, 32k during wave soldering

SW1	S2	16k	32k	Address space
S	0	0	0	0-32k
S	0	0	0	32k-64k
S	0	0	0	0-16k
S	0	0	0	16k-32k
S	0	0	0	32k-48k
S	0	0	0	48k-64k
S	0	0	0	0-32k*
S	0	0	0	32k-64k*
S	0	0	0	0-32k**
S	0	0	0	32k-64k**

*1) Selected by a high level on SEL⁺ signal

**1) Selected by a low level on SEL⁺ signal

DATAINDUSTRIER AB
SWEDEN

M K

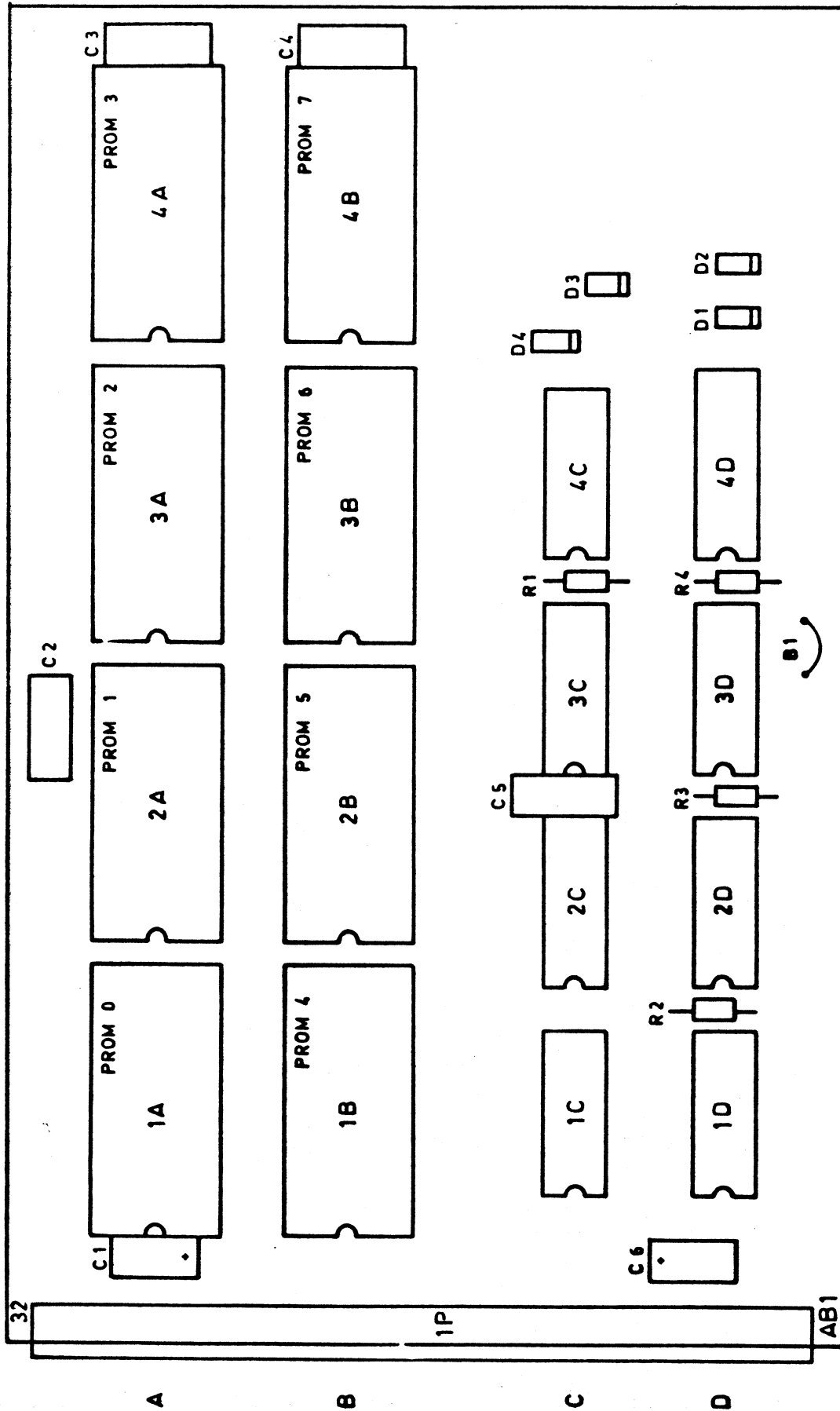
REV RIT

DATUM

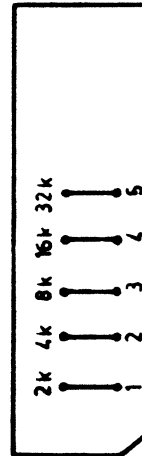
8104.09

32 K D-RAM

82-2112-00

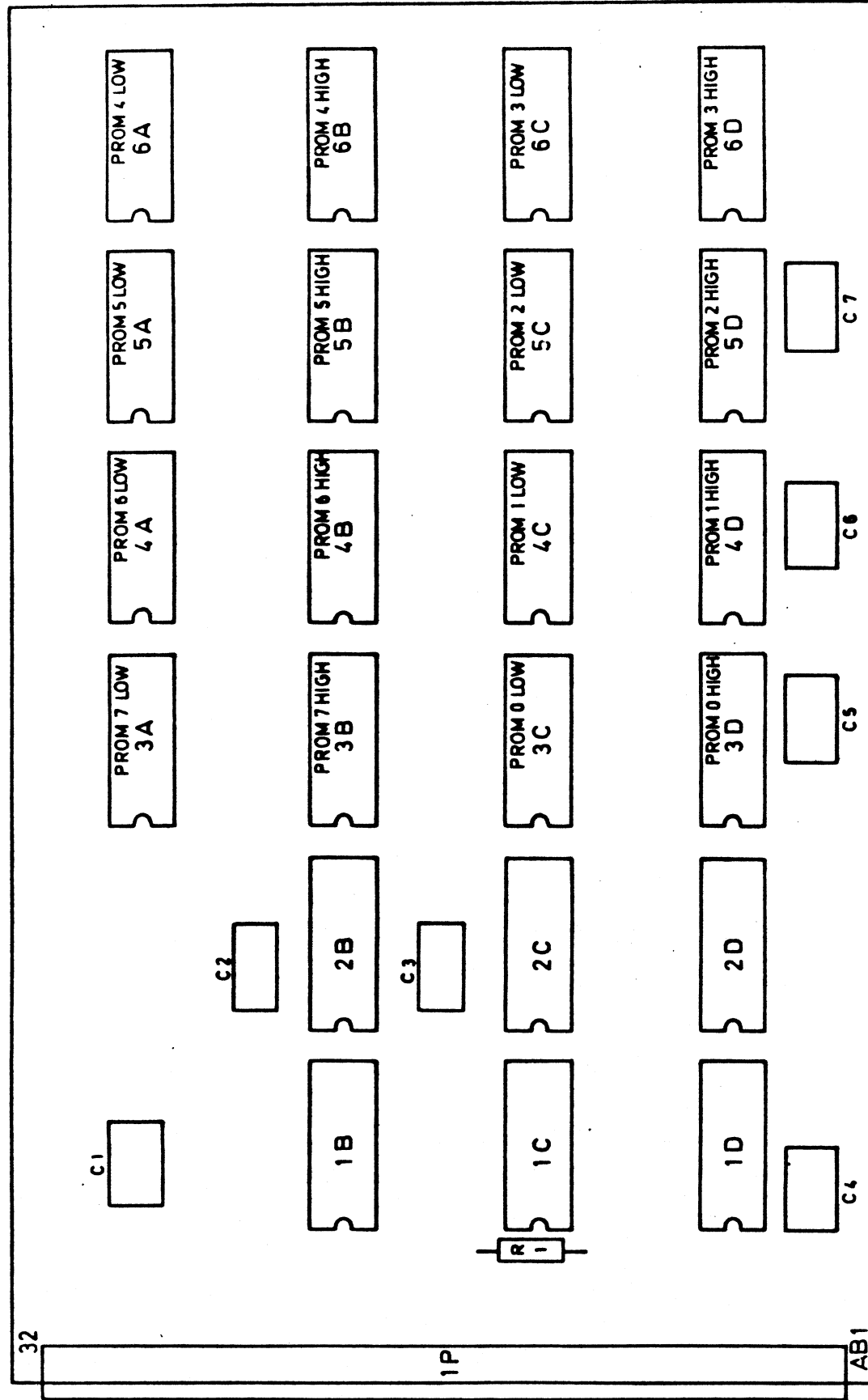


COMMENT: THE FIRST 256 BYTE MEMOR ARRAY
IS PLACED IN PROM 0 WHICH
CORRESPONDS WITH THE BASE ADDRESS
JUMPER B1 INSTALLED - WAIT STATES BY MEMRDY *

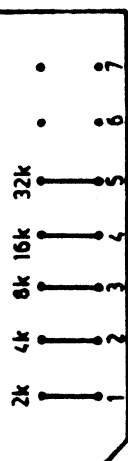


CODE PLUG FOR
BASE ADDRESS
LOC. 3C

1 2 3 4 5 6



NOTE PROM 0 THE LEAST SIGNIFICANT SEGMENT, CORRESPONDS WITH THE SELECTED BASE ADDRESS.



CODE PLUG FOR
BASE ADDRESS, LOC. 1D

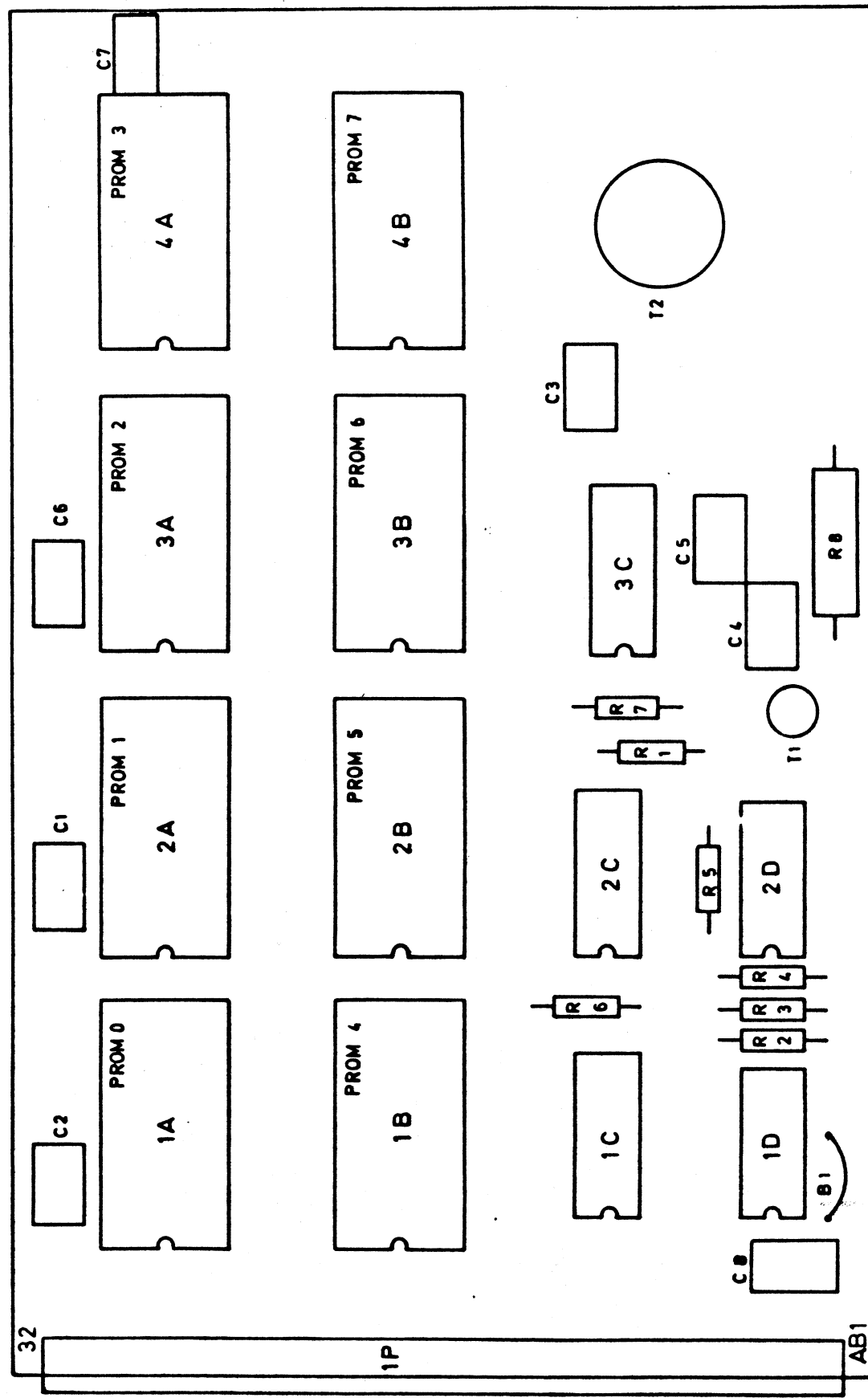
DATAINDUSTRIER AB		2 K F PROM	82-3004-00
A	REV. NR		
7903	DATUM		
TÄBY		SWEDEN	

1

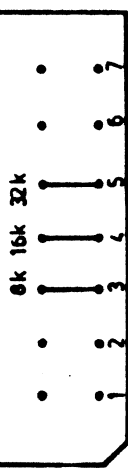
2

3

4

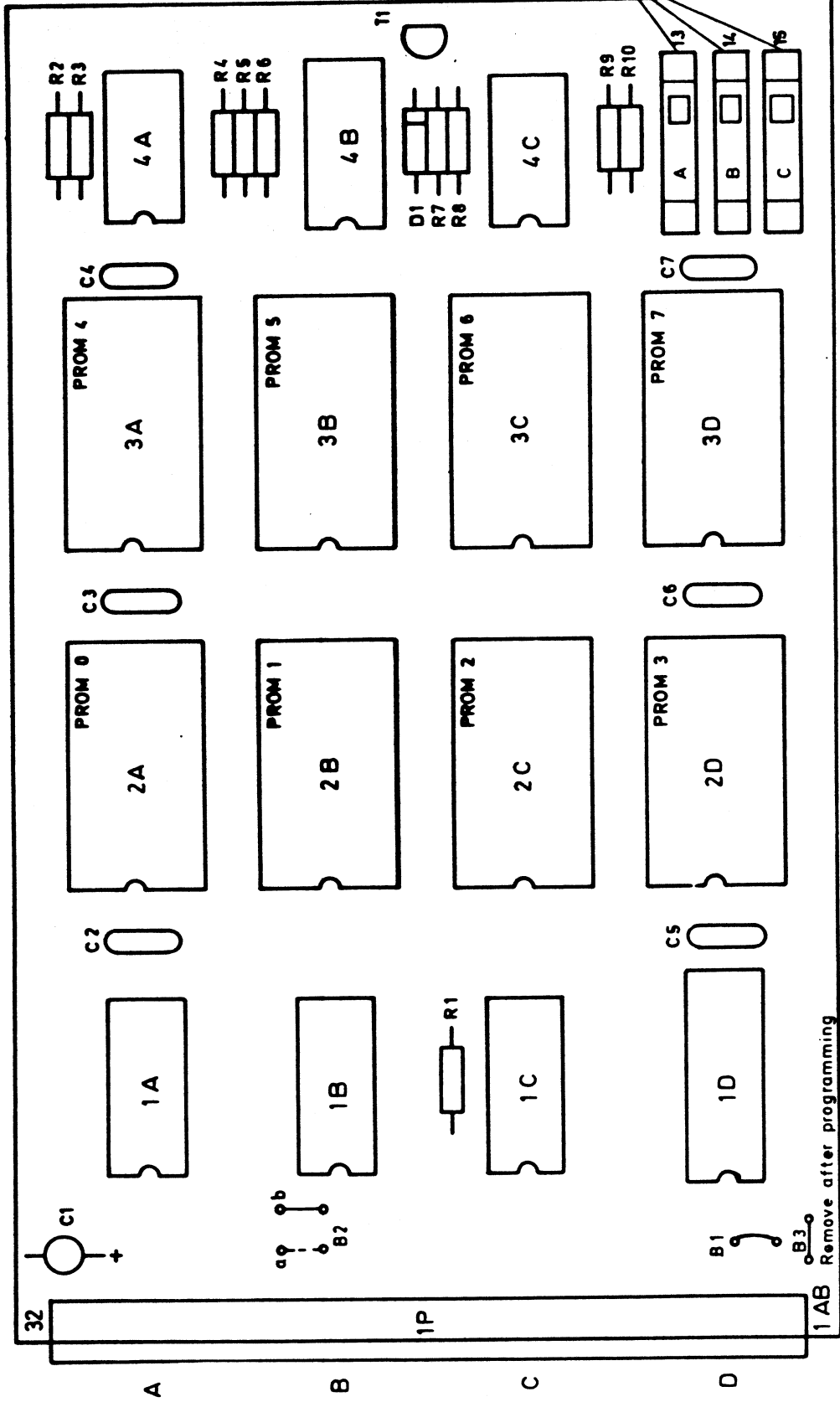


NOTE PROM 0 MEMORY SEGMENT CORRESPONDS WITH THE SELECTED BASE ADDRESS JUMPER B1 INSTALLED - WAIT-STATE(S) BY MEMRDY *

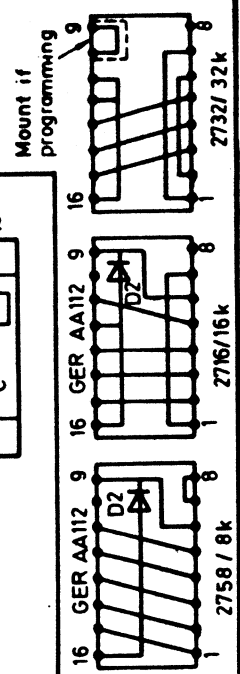


CODE PLUG FOR
BASE ADDRESS, LOC. 2D

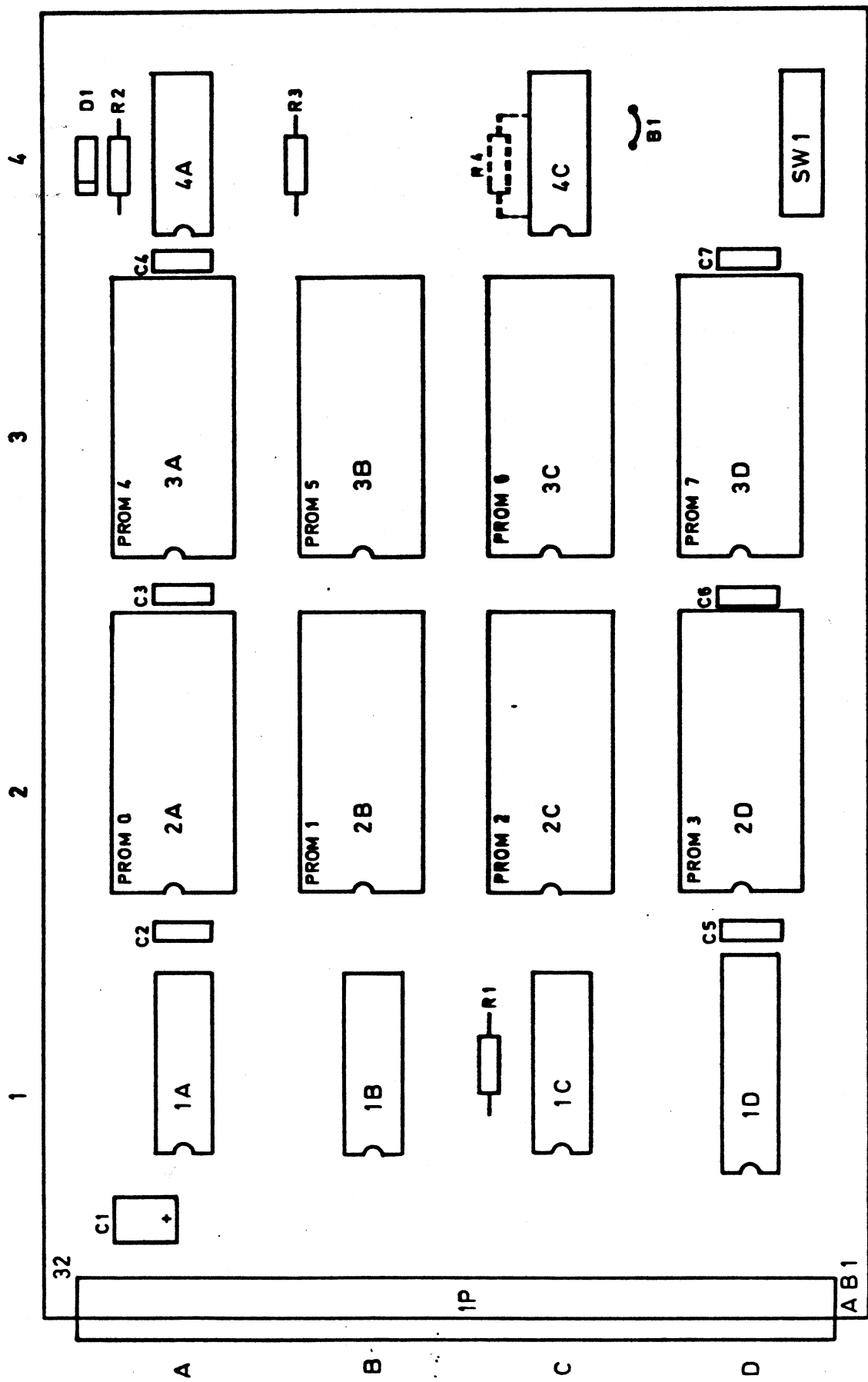
1 2 3 4



NOTE PROM 0 MEMORY SEGMENT CORRESPONDS WITH THE SELECTED BASE ADDRESS.
PROM TYPE CORRESPONDS WITH IDENTIFICATIONS PLUGG

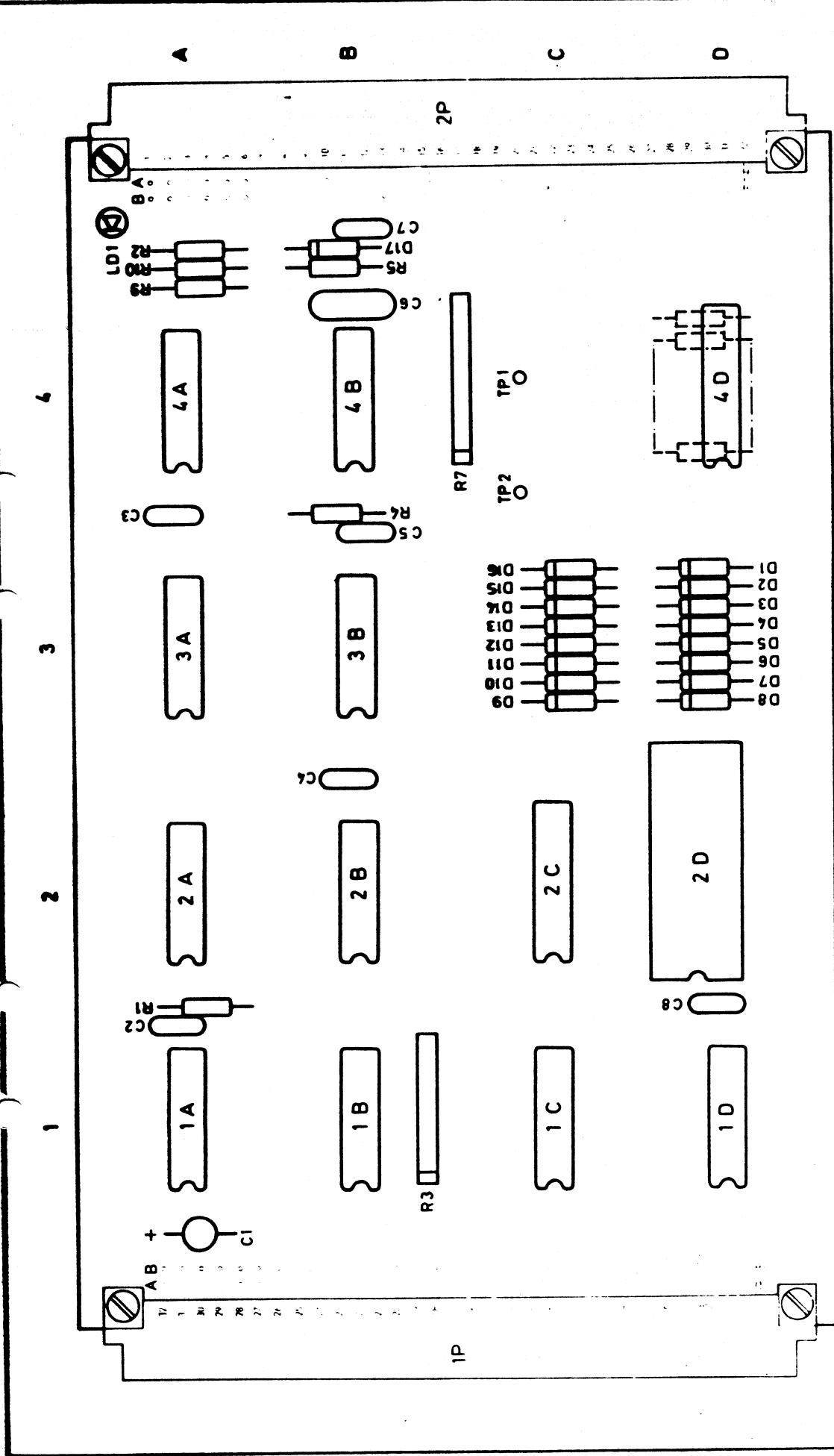


DATAINDUSTRIER AB				TÄBY SWEDEN		8/16/32 k E PROM		82-3061-00	
D	C	B	A	REV NR	DATUM				
82 02 22	81 04 05	80 12 02	80 07 25						

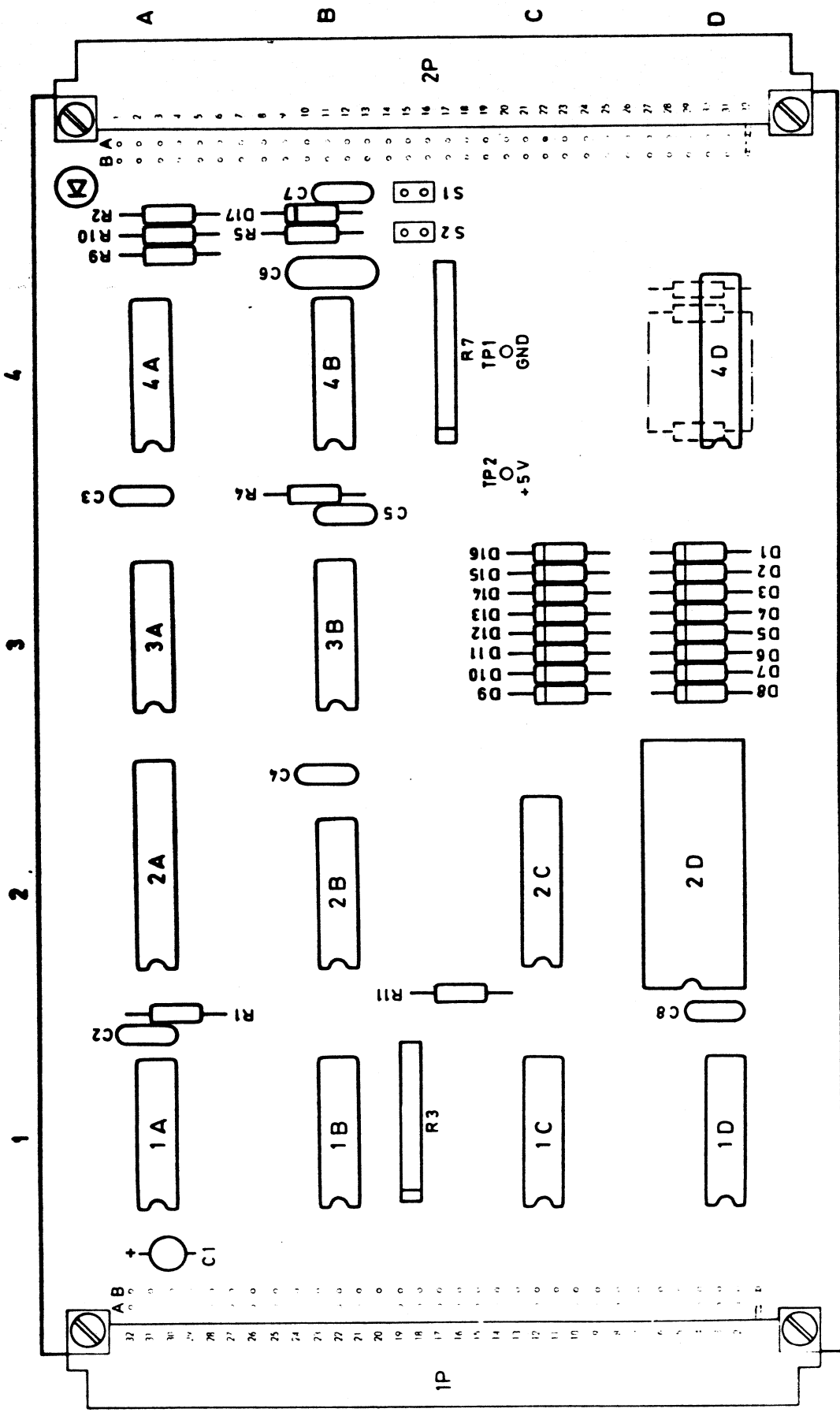


COMMENT: THE LEAST SIGNIFICANT MEM ARRAY
IS PLACED IN PROM 0 LOC. 2A.
BASE ADDRESS: SW 1 = ON - 0 k
SW 1 = OFF - 32 k

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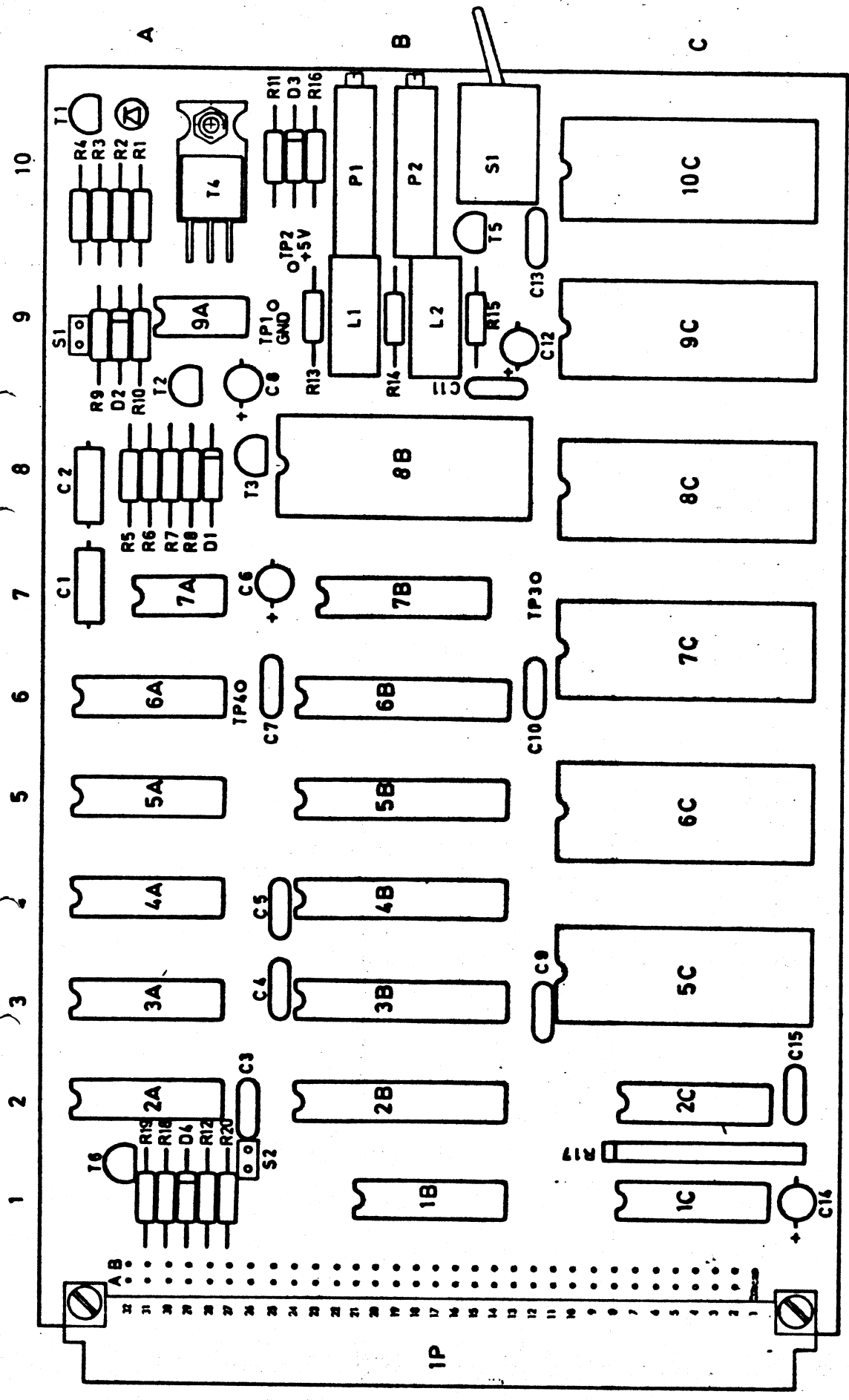
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DATAINDUSTRIER AB		PRINTER INTERFACE		82-4001-10	
SWEDEN		CENTRONICS			
HGK	REV	BIT	DATUM		
830502	A		83 0830		

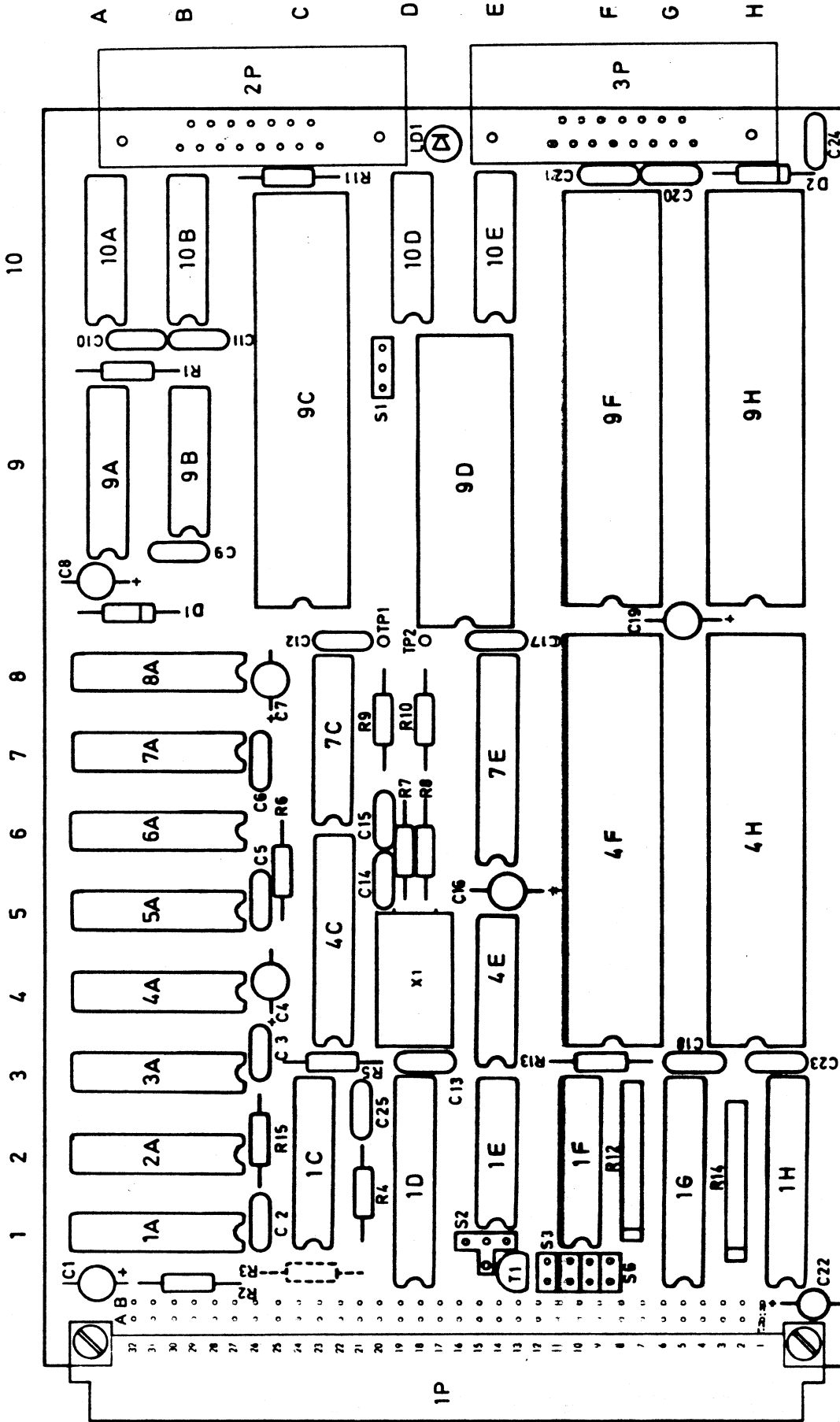
	B	A	REV	INT	HOK/DL	DATAINDUSTRIER AB SWEDEN	COURT - POS	82-4002-10
C								
960	M.C.O.S.04.0117		SERIES		020815			

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 be protected



				DATAINDUSTRIER AB		EE PROM 10 k		82-4003-00	
				SWEDEN					
				REV	RIT	HCK			
				DATUM		930518			

Order number: 8648 925

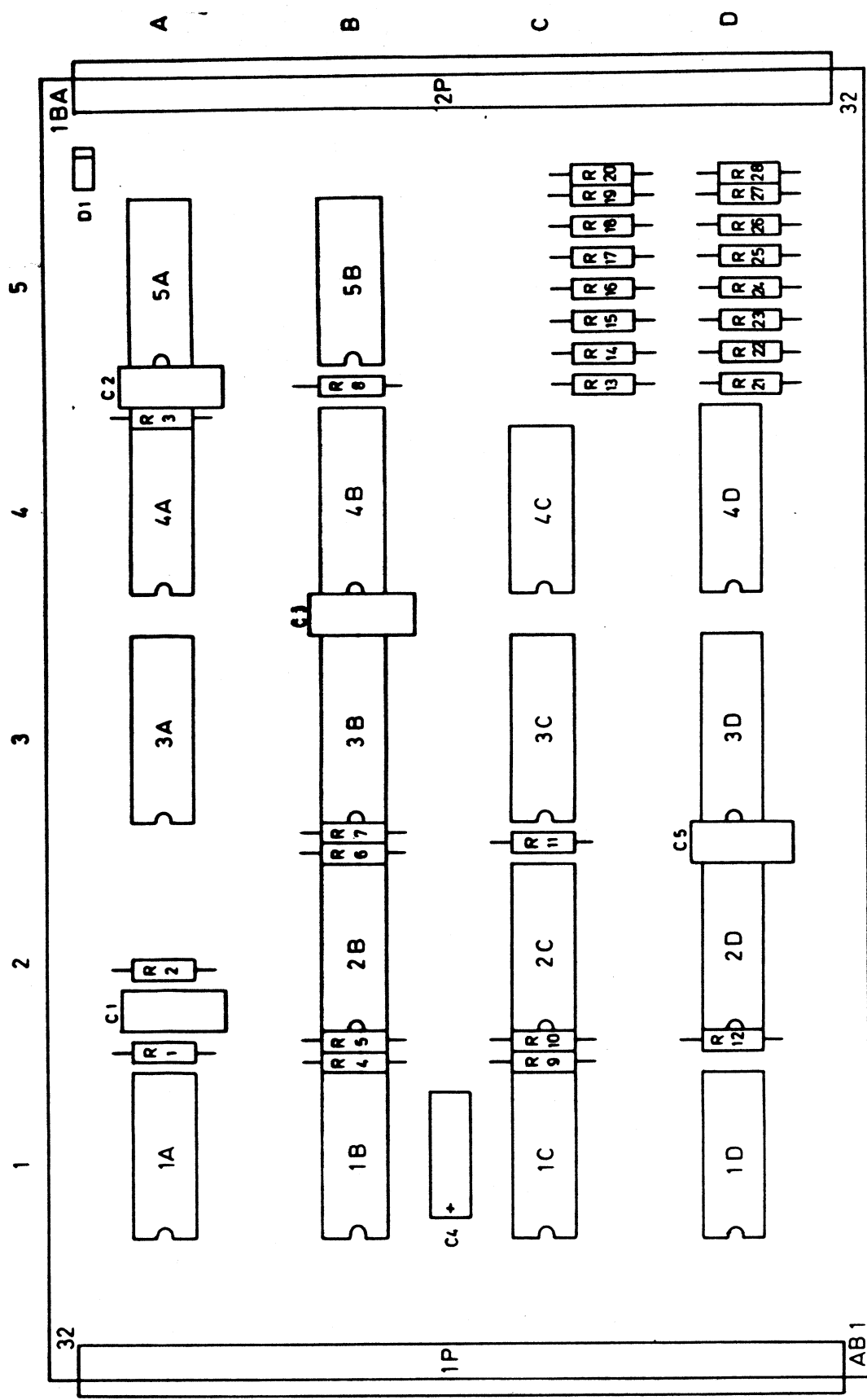


E	D	C	B	A	REV	DATE
04.01.2002	10.22	02.04	20.02	03.25	02.04.04	02.04.04

DATA INDUSTRIER AB
SWEDEN

1 COM I/O

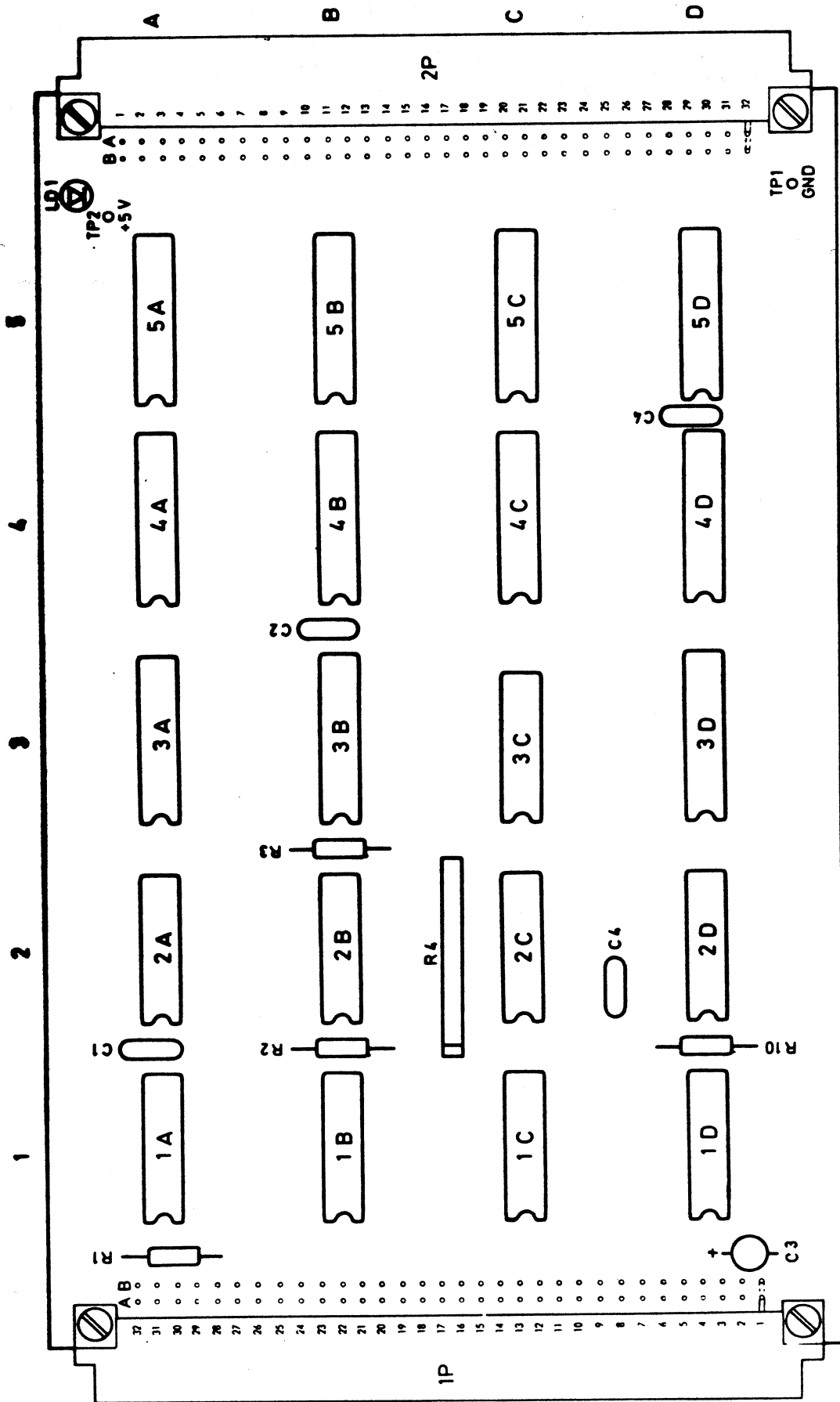
82-4004-00



COMMENT : 2 B = CODE PLUG FOR CHANNEL SELECTION

A		REV NR	DATAINDUSTRIER AB	16 U 81 TTL	82-4005-00
79.03		DATUM	TABY SWEDEN		

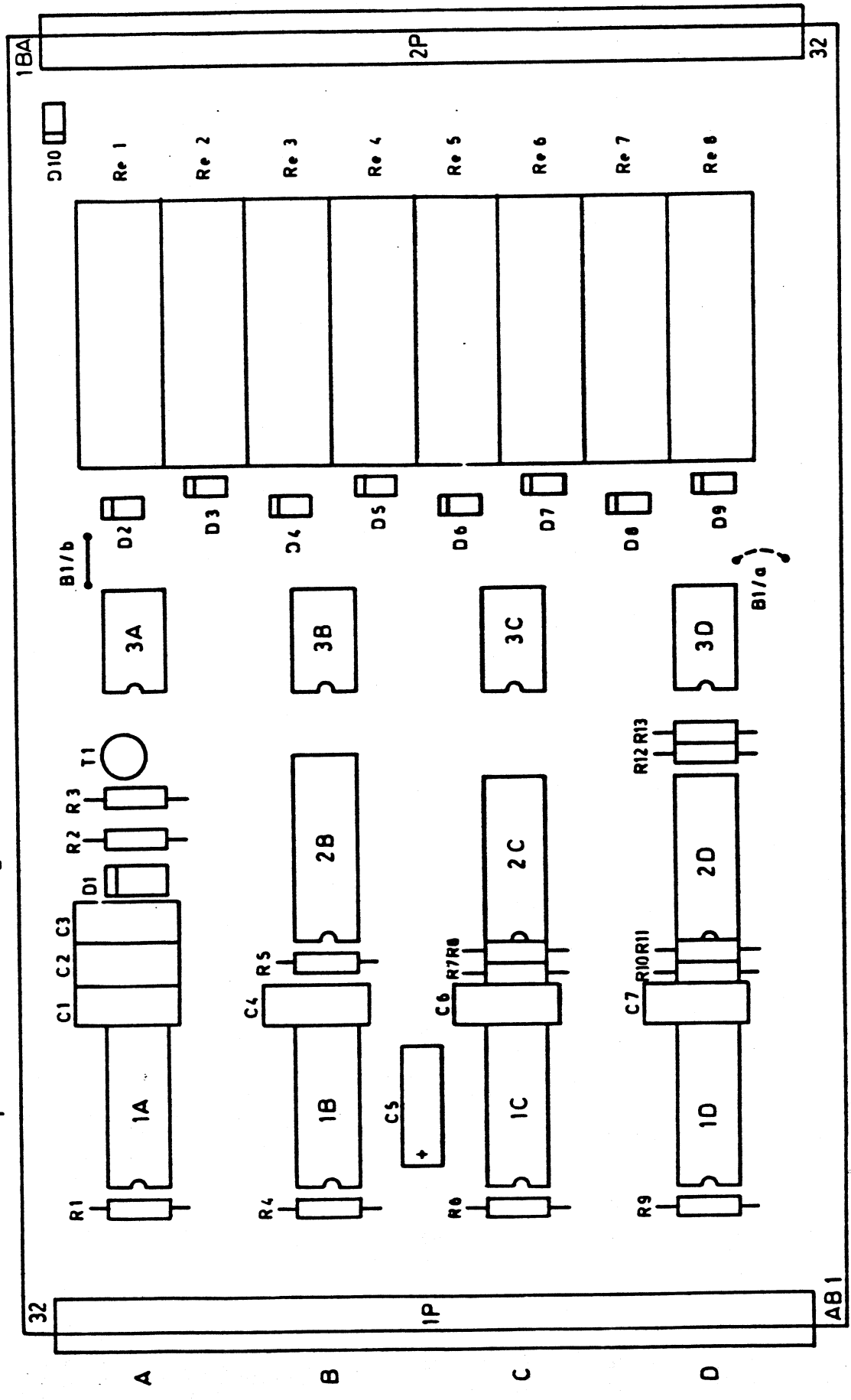
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COMMENT: 2 C-CODE PLUG FOR CHANNEL SELECTION

				DATAINDUSTRIER AB SWEDEN		32U 16 1	82-4006-10
REV	INT	DATUM	HGK	8305.17			

1 2 3 4



COMMENT: 2D = CODE PLUG FOR CHANNEL SELECTION
 JUMPER B1/b IS INSTALLED - INTERNAL +12V SUPPLY TO THE RELAYS
 EXTERNAL RELAY SUPPLY VIA 2P: 32 A/B IS FURNISHED BY REMOVING B1/b AND INSTALLING B1/a

A		REV. NR	DATAINDUSTRIER AB		8 RELAY OUTPUT		82-4007-00
75.03		DATUM	TÄBY	SWEDEN			

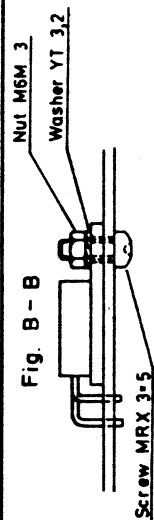


Fig. 8-B

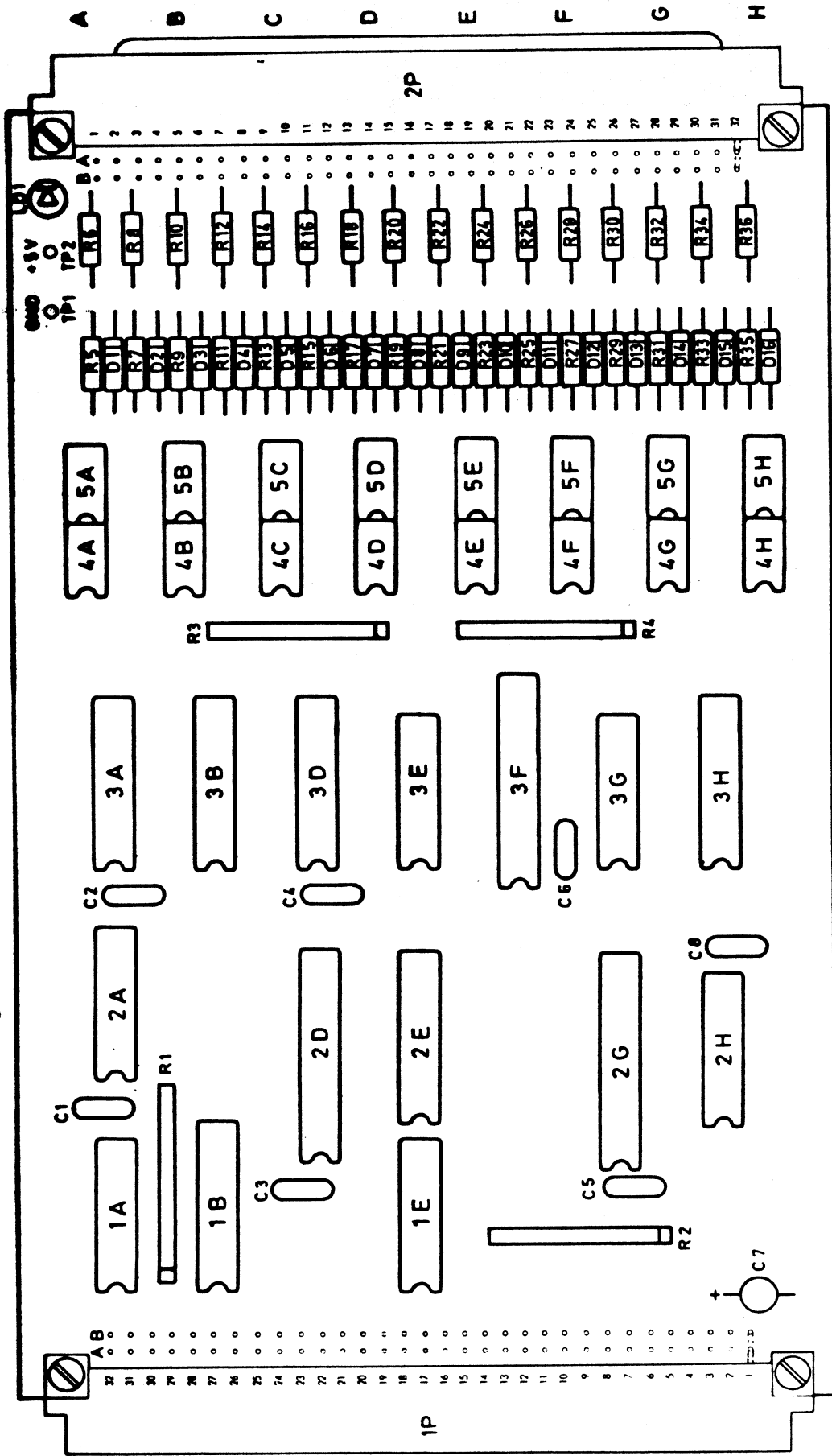
COMMENT: 1G = CODE PLUG FOR CHANNEL SELECTION

B	A	REV. NR	HGK	DATAINDUSTRIER AB
830315	79.03	DATUM		TÄBY SWEDEN

8 TRANSISTOR OUTPUT

82-4009-00

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DATAINDUSTRIER AB SWEDEN				OPTO IN WITH 8 INTERRUPTS 82-4011-10			
REV	RIT	M/K	DATUM				

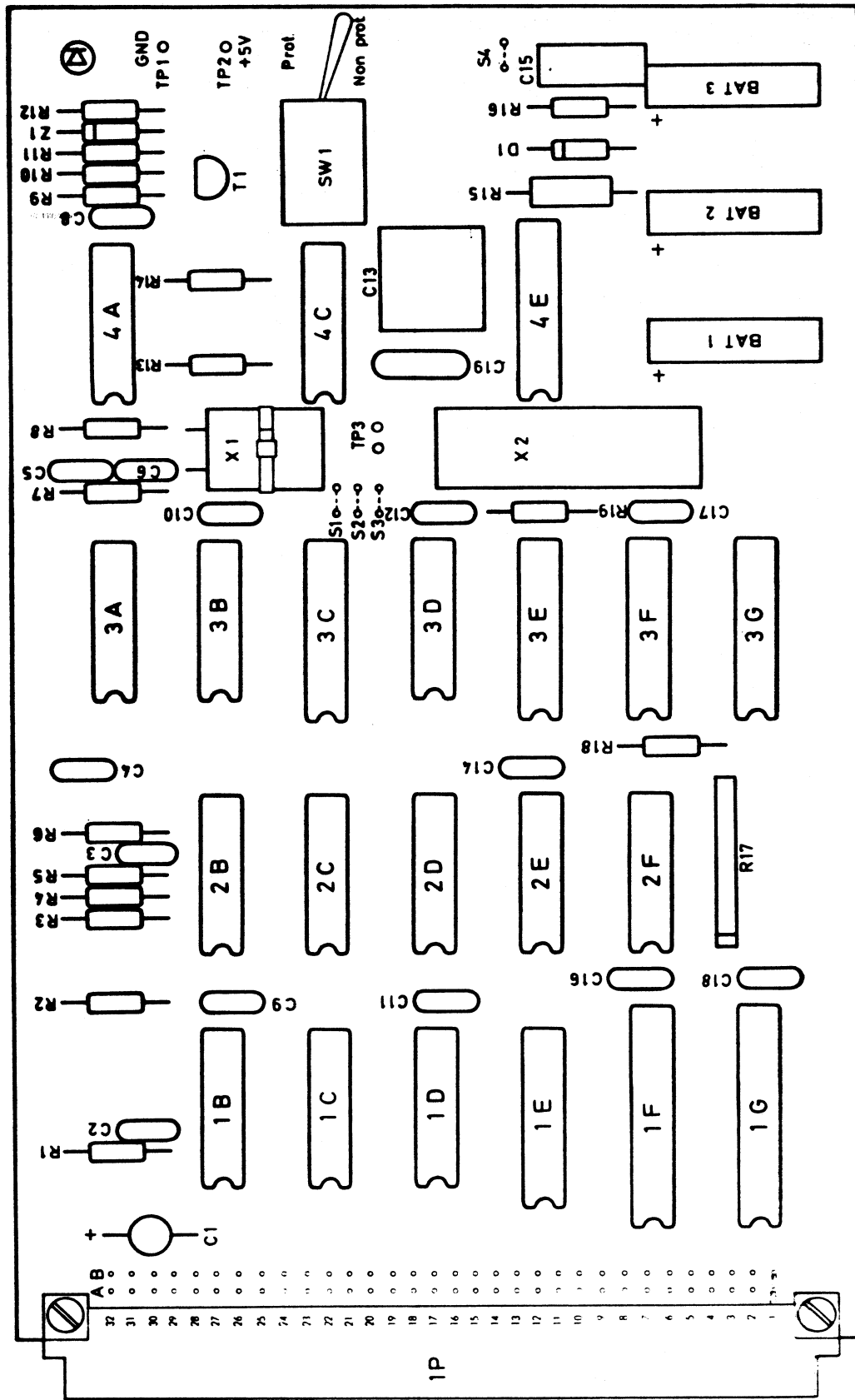
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4

3

2

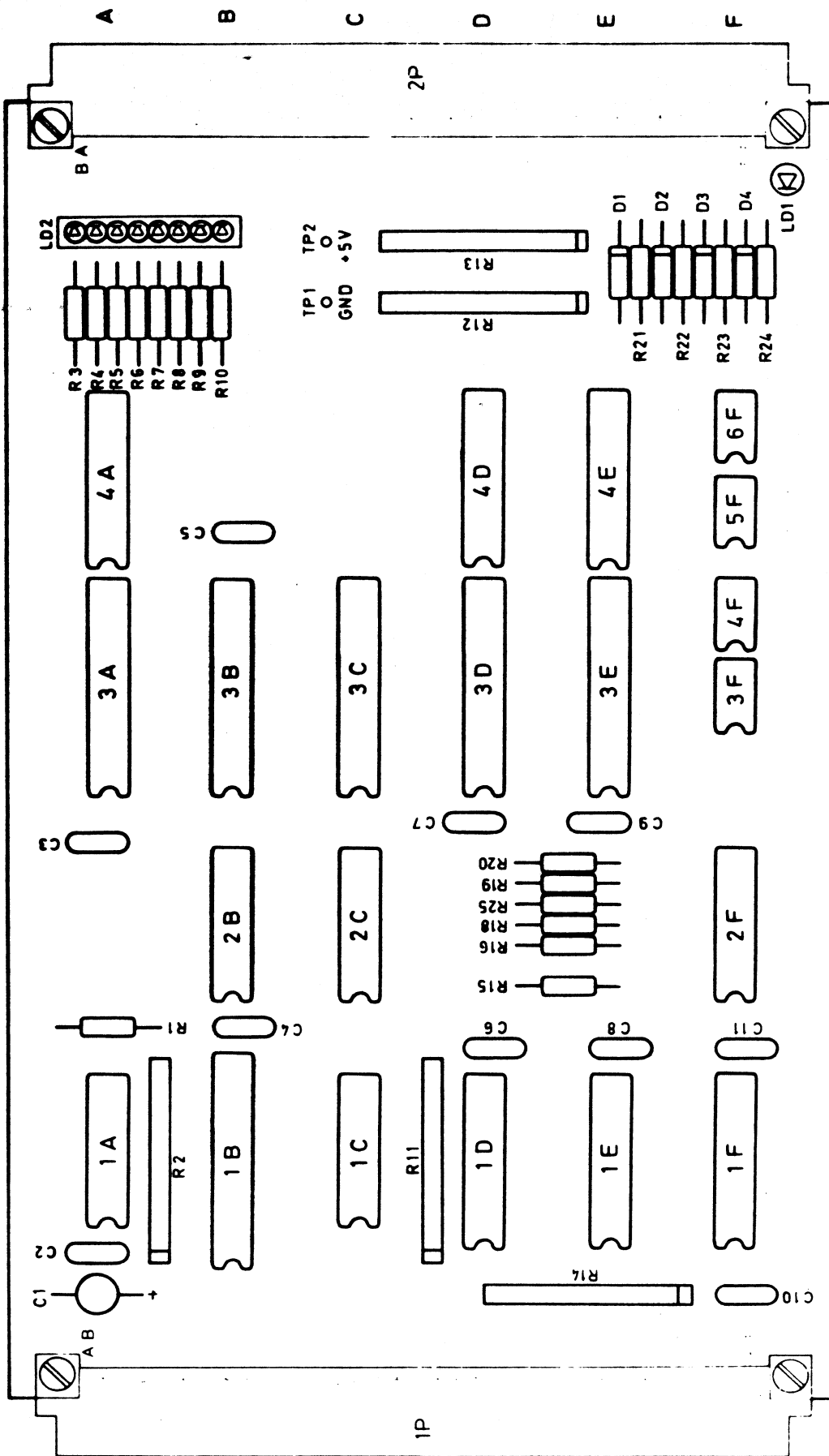
1



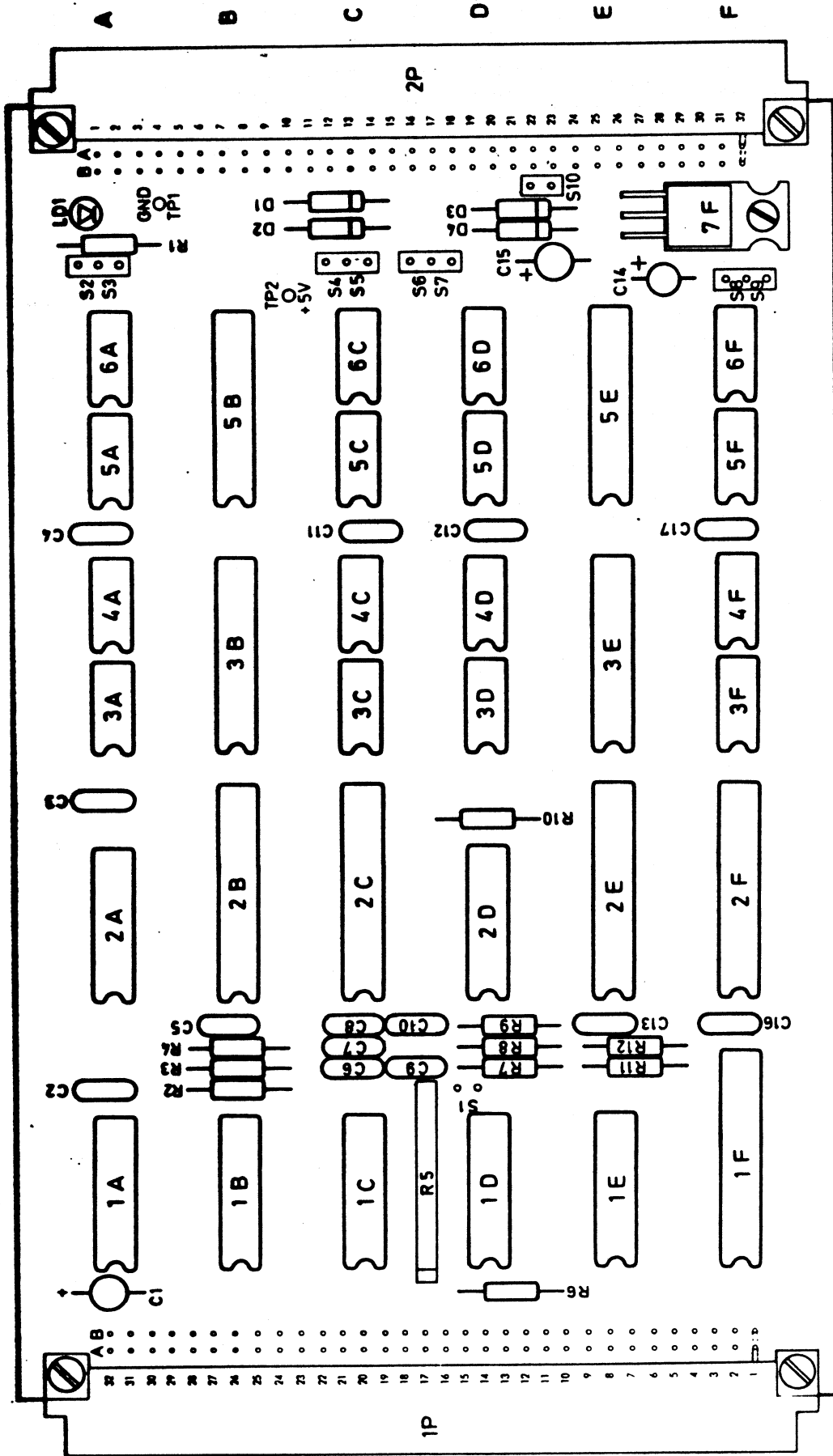
DATAINDUSTRIER AB SWEDEN				INT-REAL TIME CLOCK		82-4012-00	
C	B	A	REV	RIT	HGK		
031031	030609	820526	DATUM	010104			

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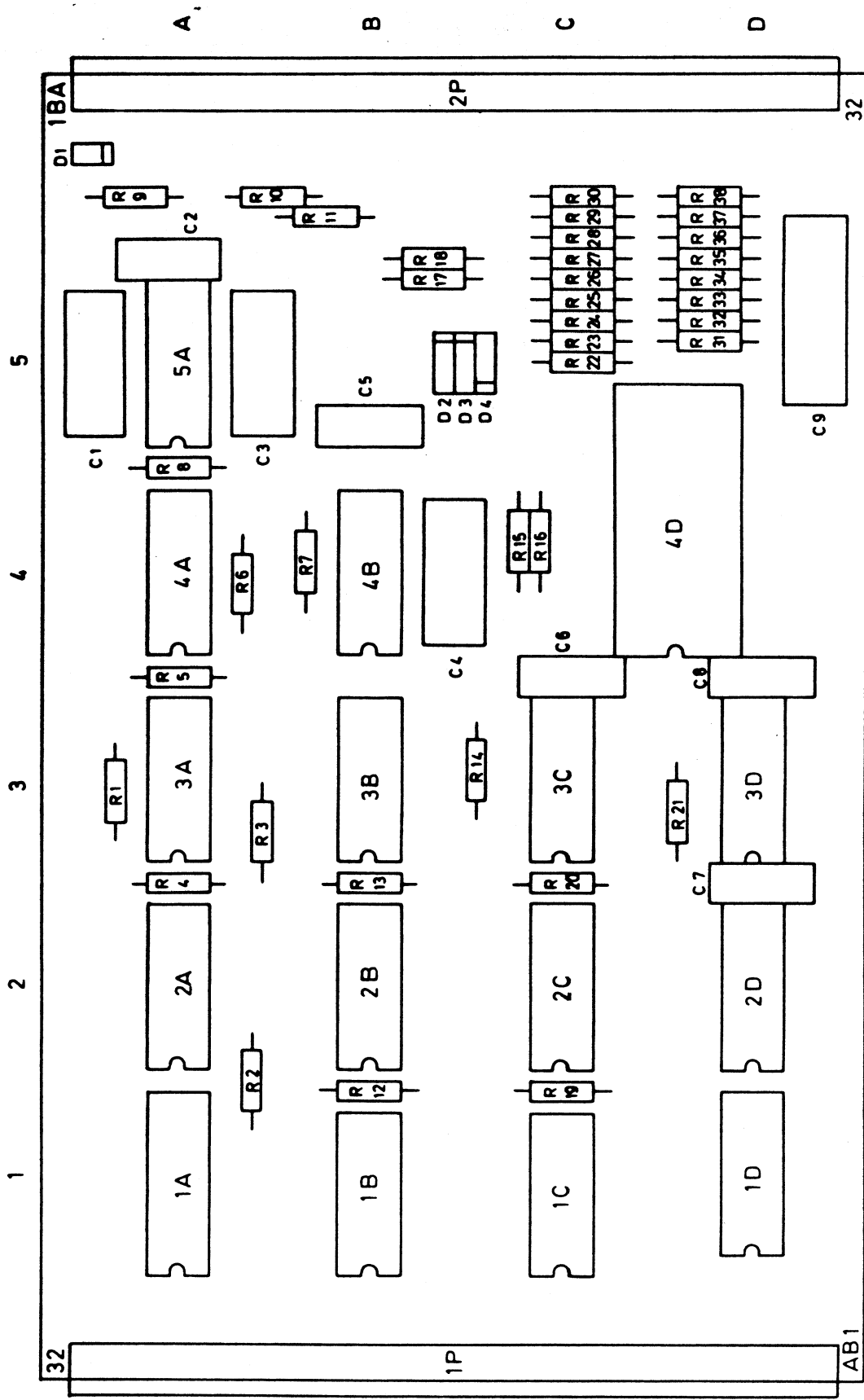
1 2 3 4



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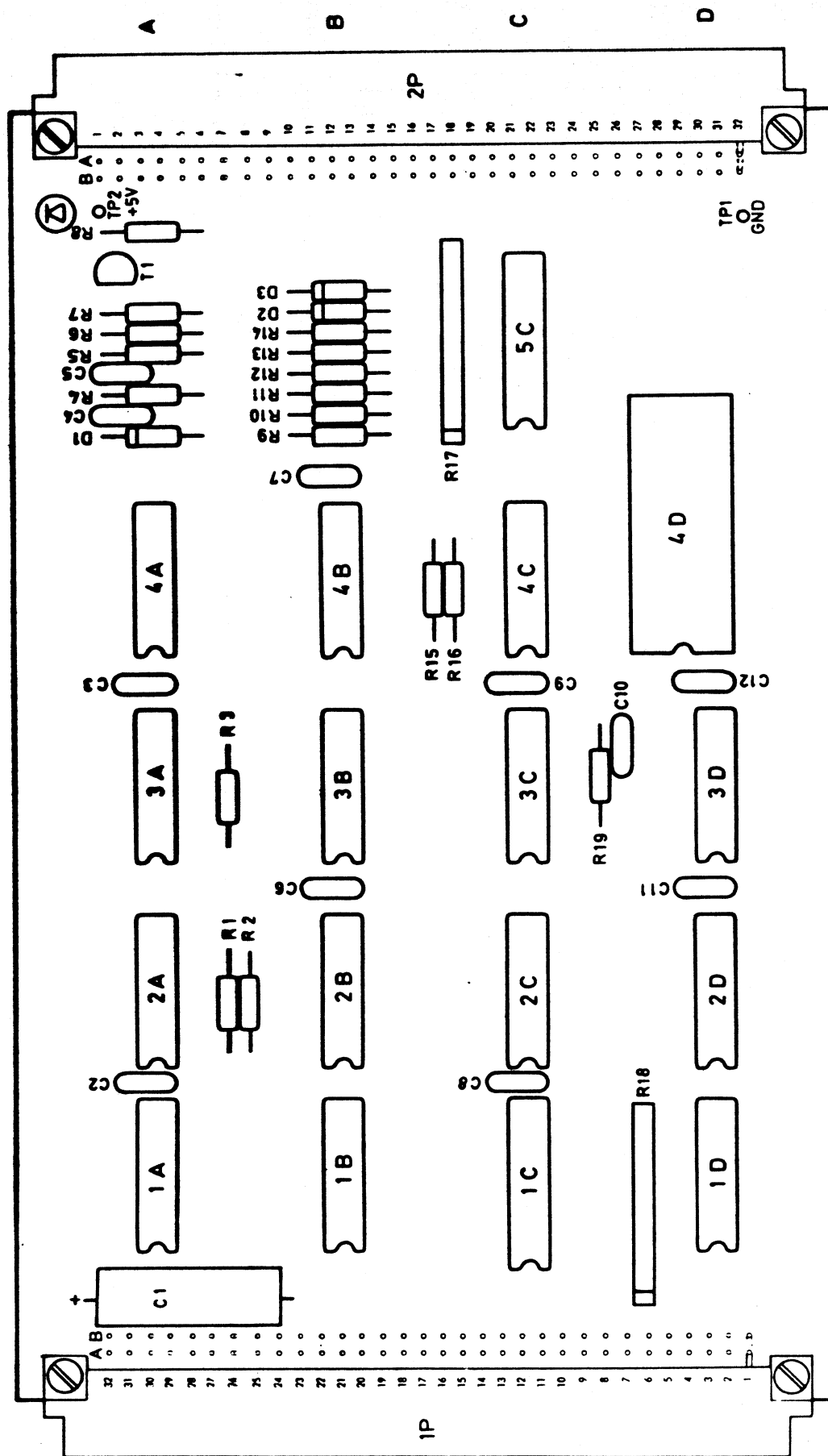
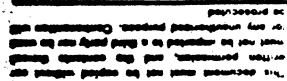


DATAINDUSTRIER AB				16 DC-MOTOR		82-4014-00	
SWEDEN							
A		REV		RTT		SJ	
030301		030301		020615		020615	
DATE		DATE		DATE		DATE	



COMMENT: 1A = CODE PLUG FOR CHANNEL SELECTION.

	A	REV NR	DATAINDUSTRIER AB	SP1 - SEND		82-4015-00
	79 03	DATUM	TÄBY SWEDEN			

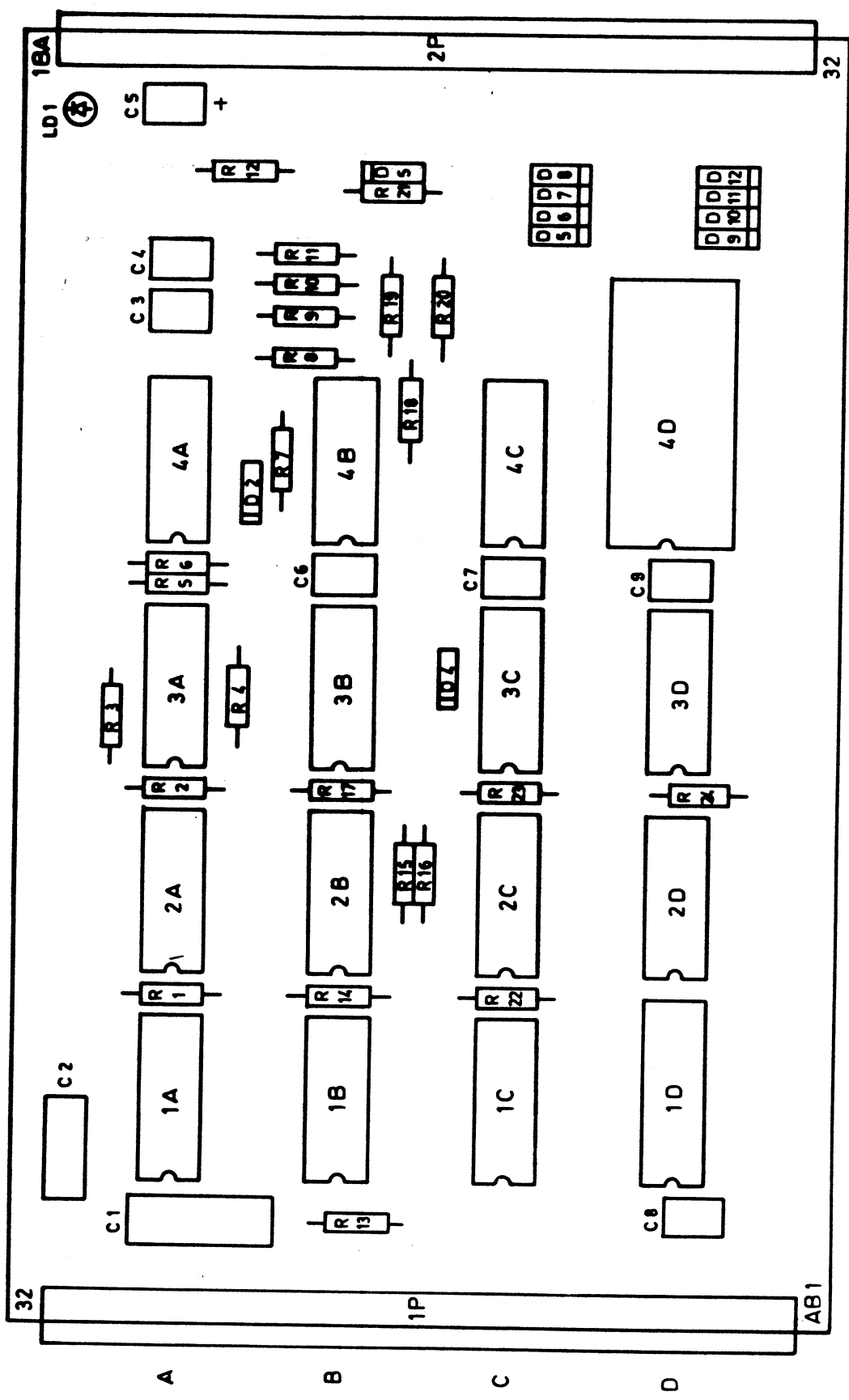


1

2

3

4



COMMENT : 1A = CODE PLUG FOR CHANNEL SELECTION

B		A	REV. NR	HGK	DATAINDUSTRIER AB		SP 1 RECEIVER		82-4016-00	
830216		79 03	DATUM		TÄBY	SWEDEN				

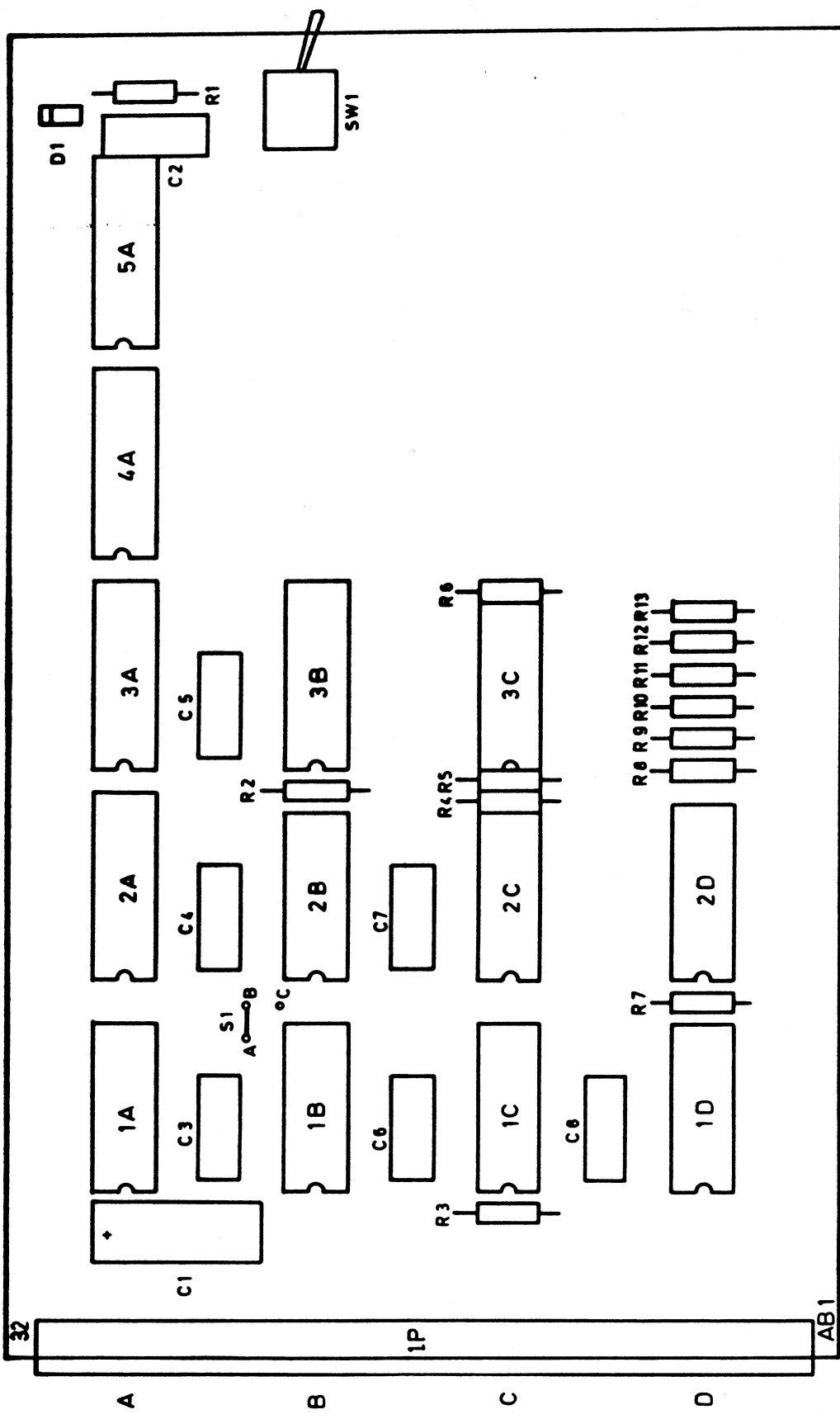
5

4

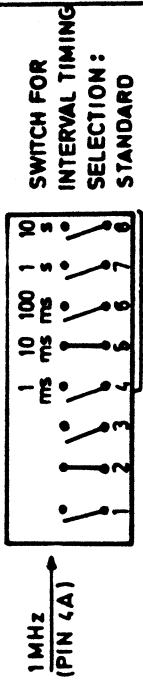
3

2

1

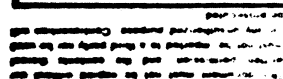


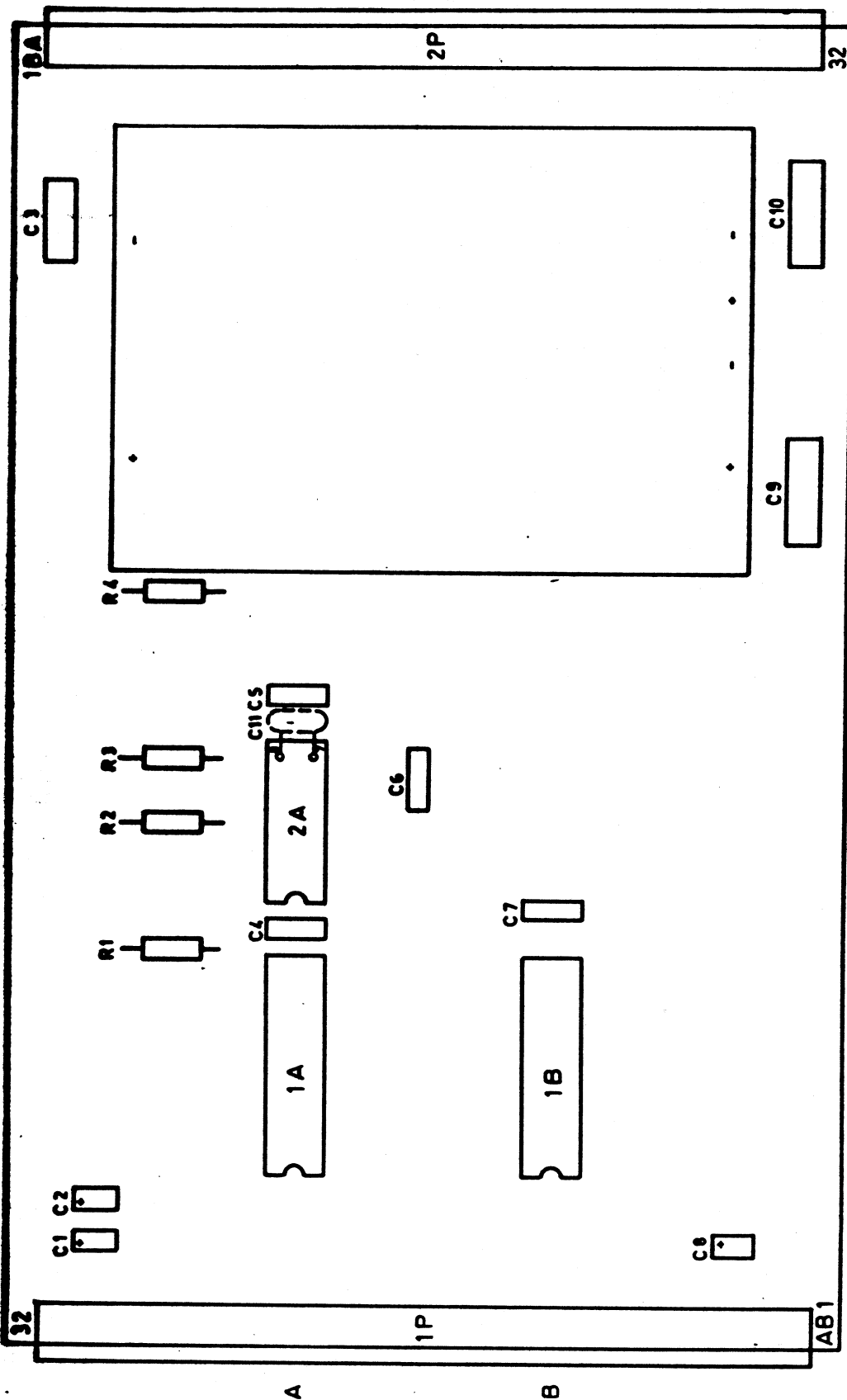
COMMENT: 2D = CODE PLUG FOR CHANNEL SELECTION
3B = FOR INTERVAL TIMING SELECTION
JUMPER S1 A-B INSTALLED: CUT B-C
SW 1 CONTROLS STATUSWORD: BIT 1



1/10 PER STEP
1:1ST DIVIDER 2:2ND DIVIDER
SELECT INTERVAL

B		A		REV. NR		DATAINDUSTRIER AB	
01.05.11	79 03					TÄBY	SWEDEN
				INTERVAL CLOCK			
				82-4018-00			





COMMENT : THE CARD MODULE IS AVAILABLE AS BUS EXPANDER AND / OR $\pm 12V$ SUPPLY:

IF ONLY AS BUS EXPANDER - DOES NOT INCLUDE DC/DC-CONVERTER

" " " " 12V SUPPLY - INCLUDES THE DC/DC - CONVERTER AND DOES NOT INCLUDE 1A-2A, 2B, C1 AND C8

DATAINDUSTRIER AB		BUS EXP		82-4023-00
D	C	B	REV. NR	
83.01.03	82.10.07	73.03	DATUM	

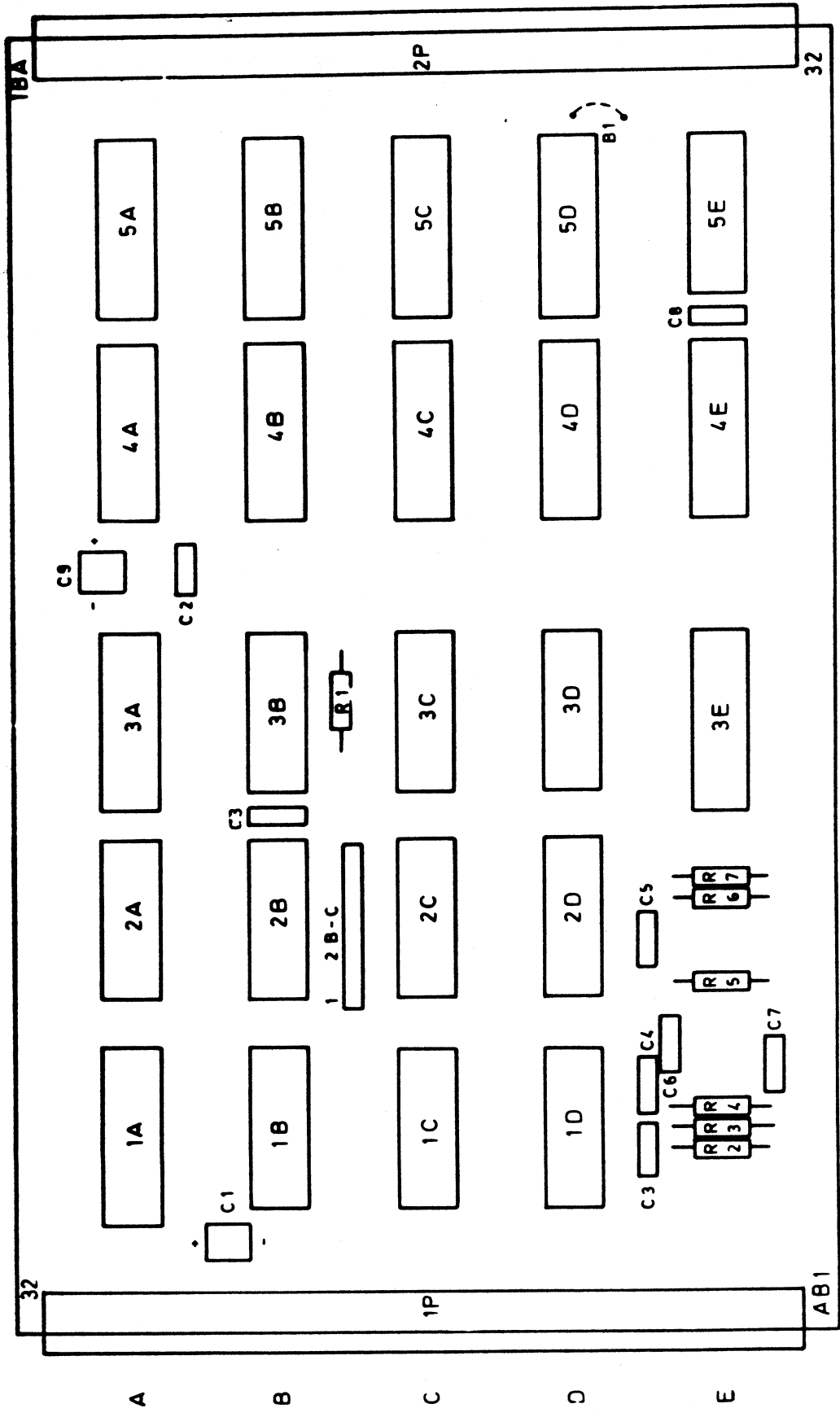
5

4

3

2

1



COMMENT 2B = CODE PLUG FOR CHANNEL SELECTION.

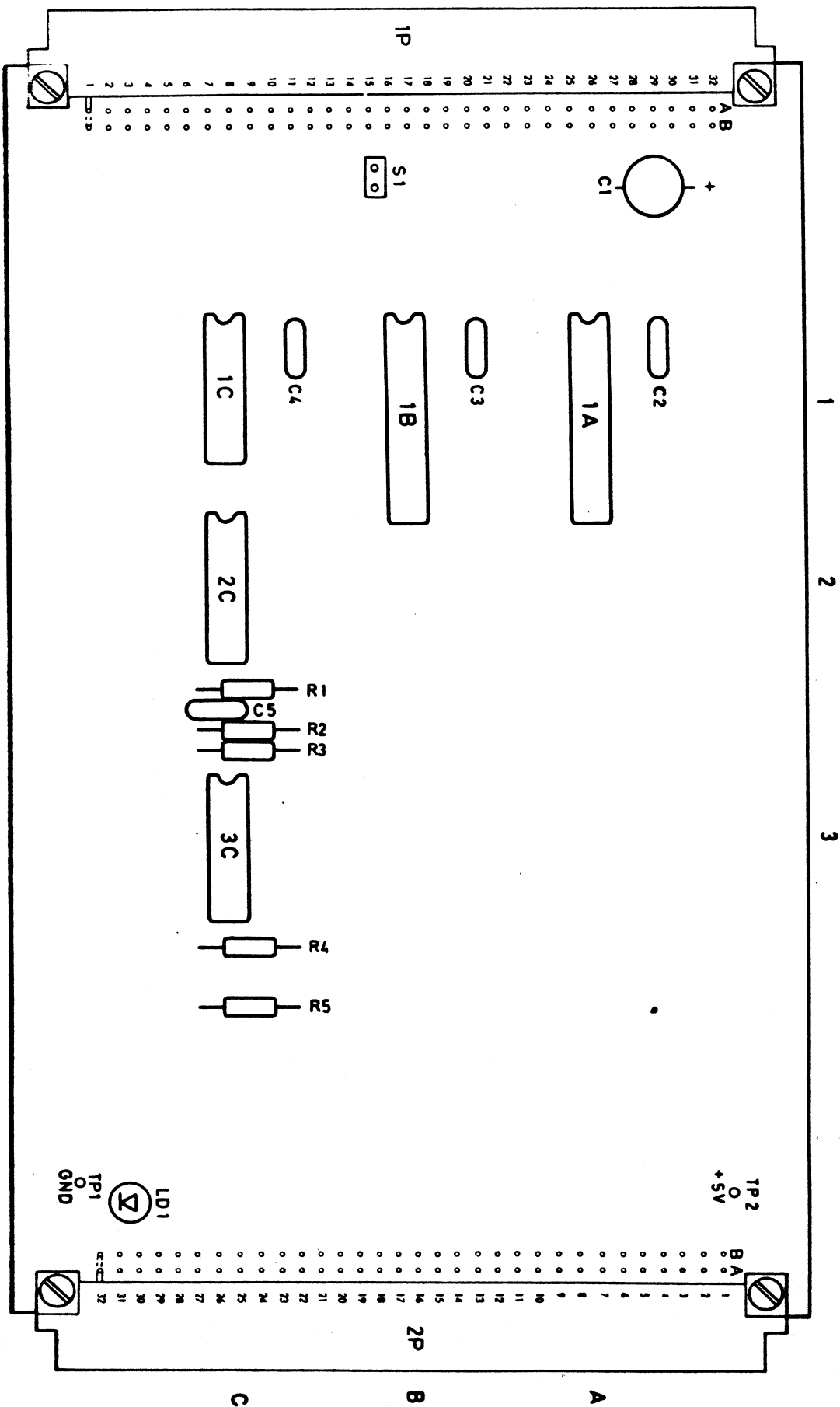
JUMPER B1 NOT INSTALLED = SHIELD NOT CONNECTED TO 4680 SYSTEM 0V.

IEC INTERFACE		DATAINDUSTRIER AB	
		TÄBY	SWEDEN
		73.03	DATUM
		C	REV NR

82-4025-00



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[illegible]

1 2 3 4

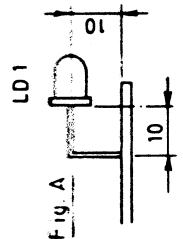
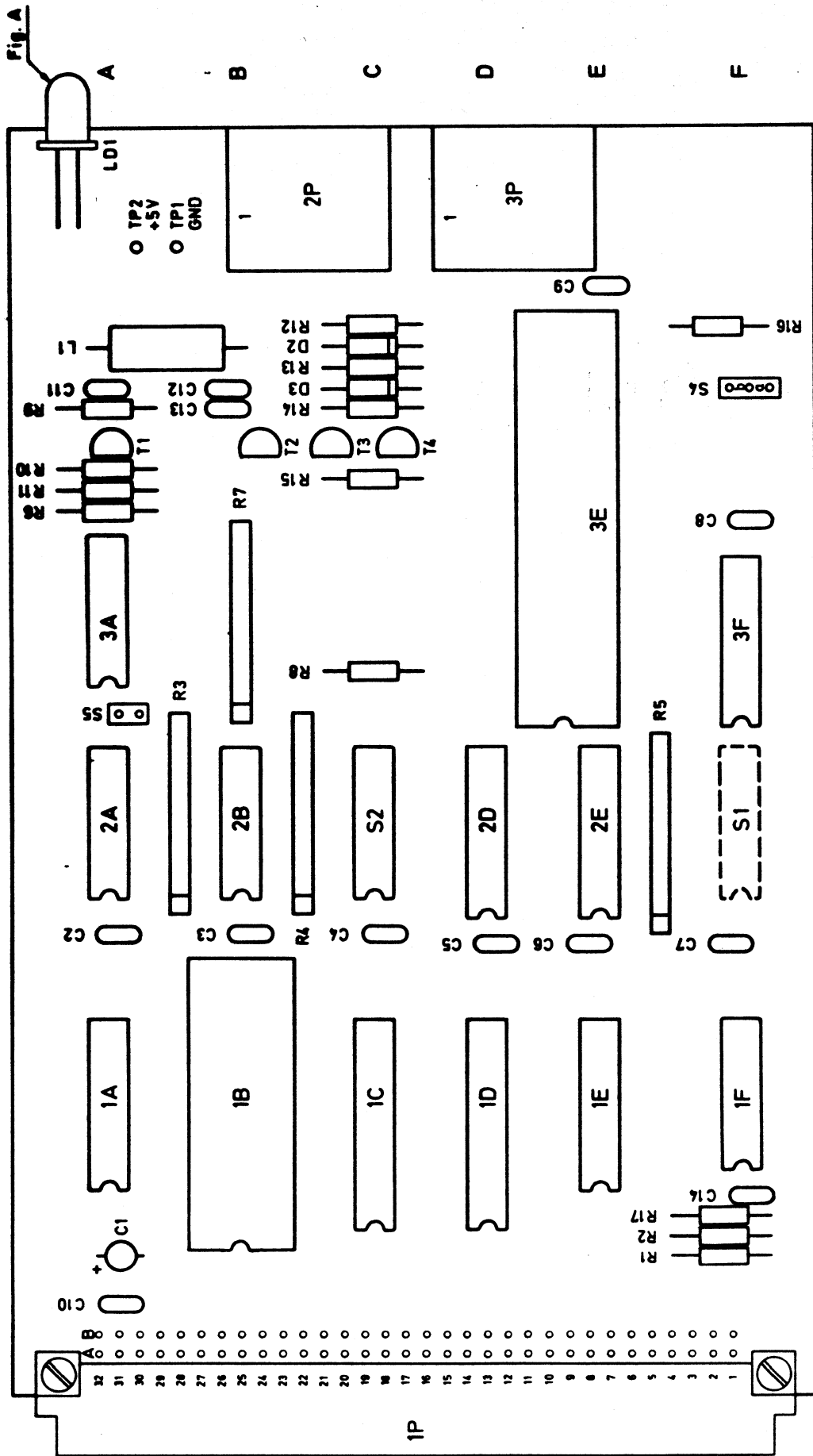
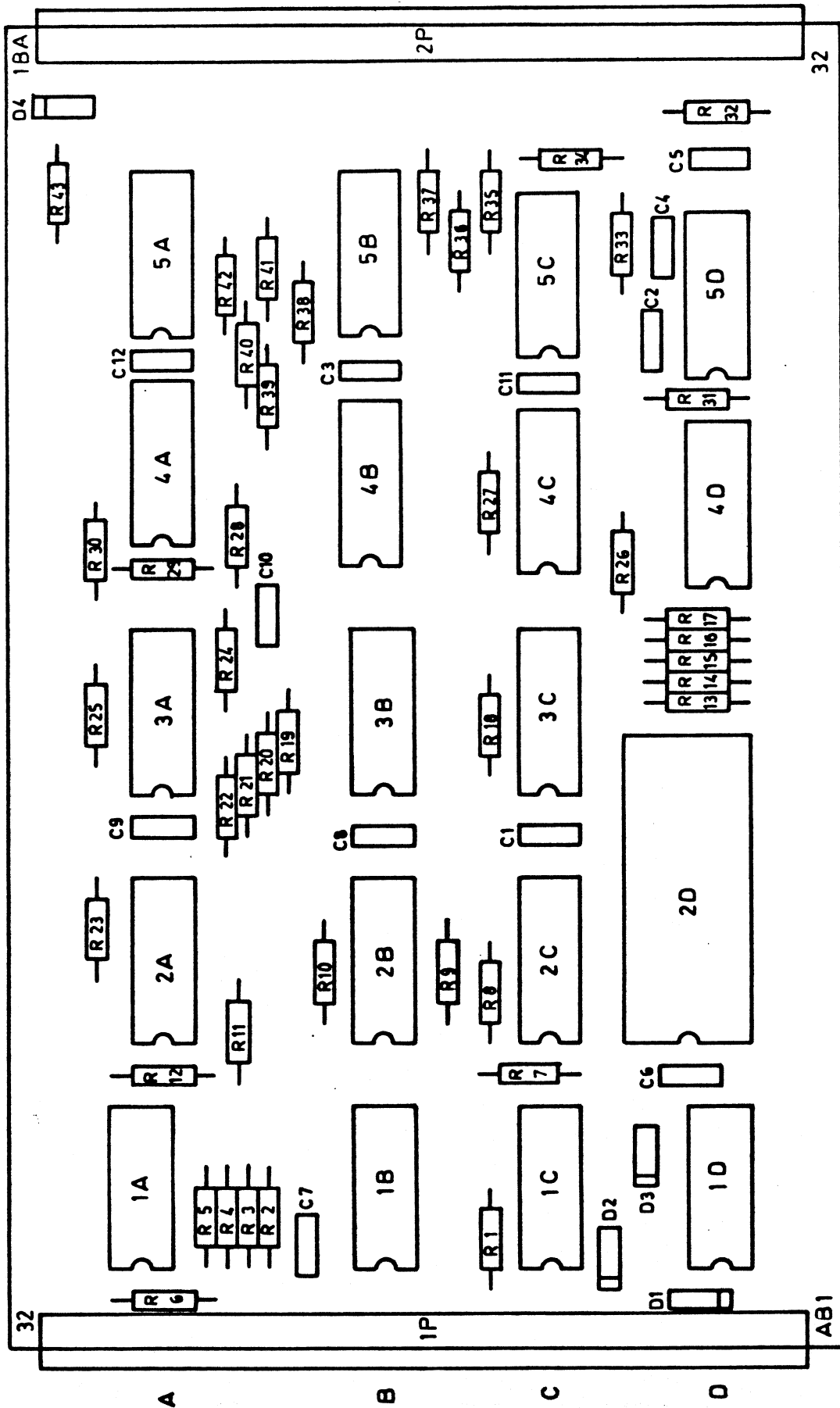


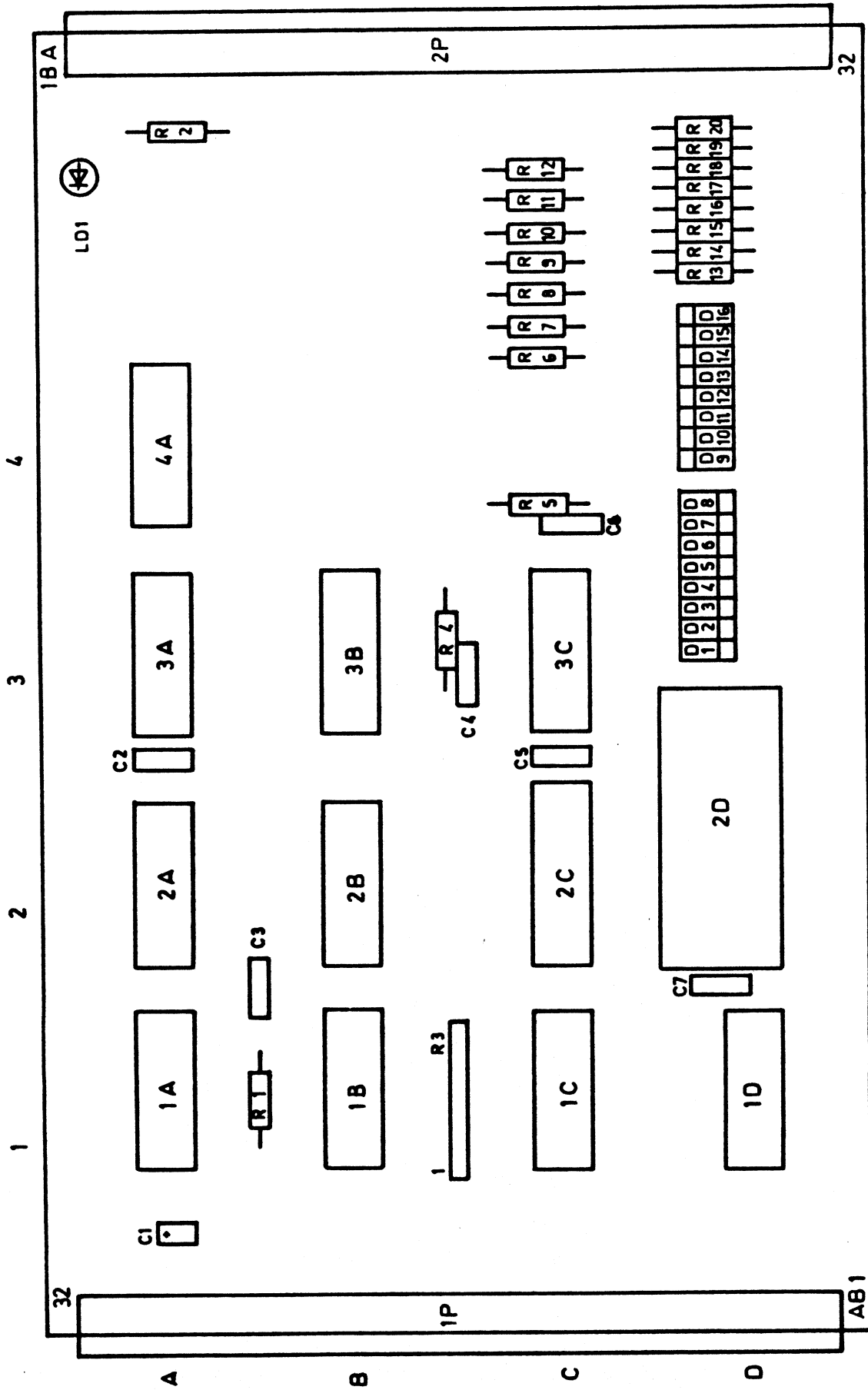
Fig. A Mounting advice

S4 pos. a: transfer speed 18.75 kbaud
 " b: 37.5 kbaud
 S5: OFF = 4010 compatible
 ON = 802/806 compatible

ABC NETWORK				82 - 4035 - 00			
INTERFACE - 2							
DATA INDUSTRIES AB							
MK/MS							
REV							
A							
94.01.09							
DATE							
831101							



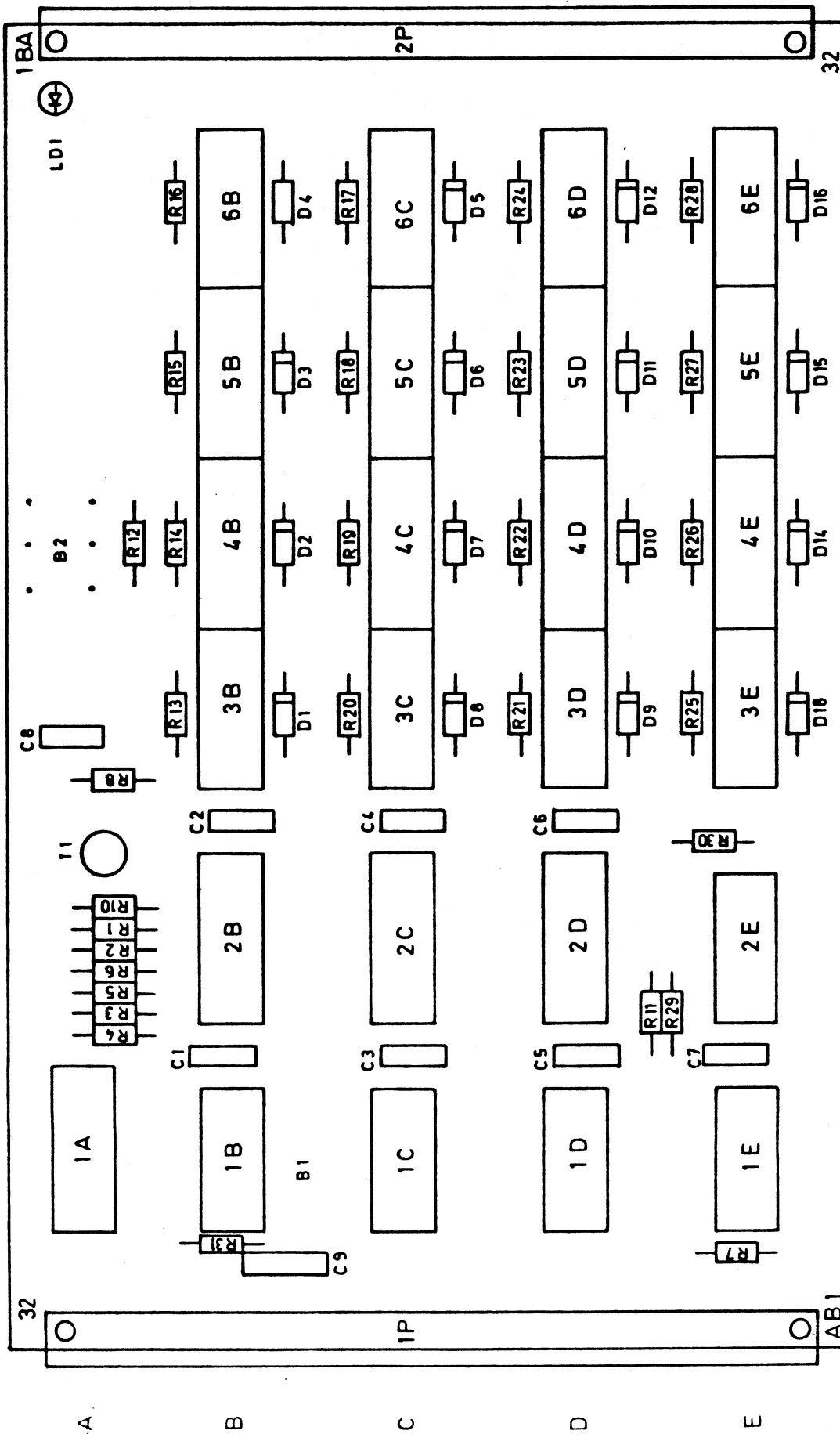
COMMENT: 2A - CODE PLUG FOR CHANNEL SELECTION.



COMMENT: 1B - CODE PLUG FOR CHANNEL SELECTION

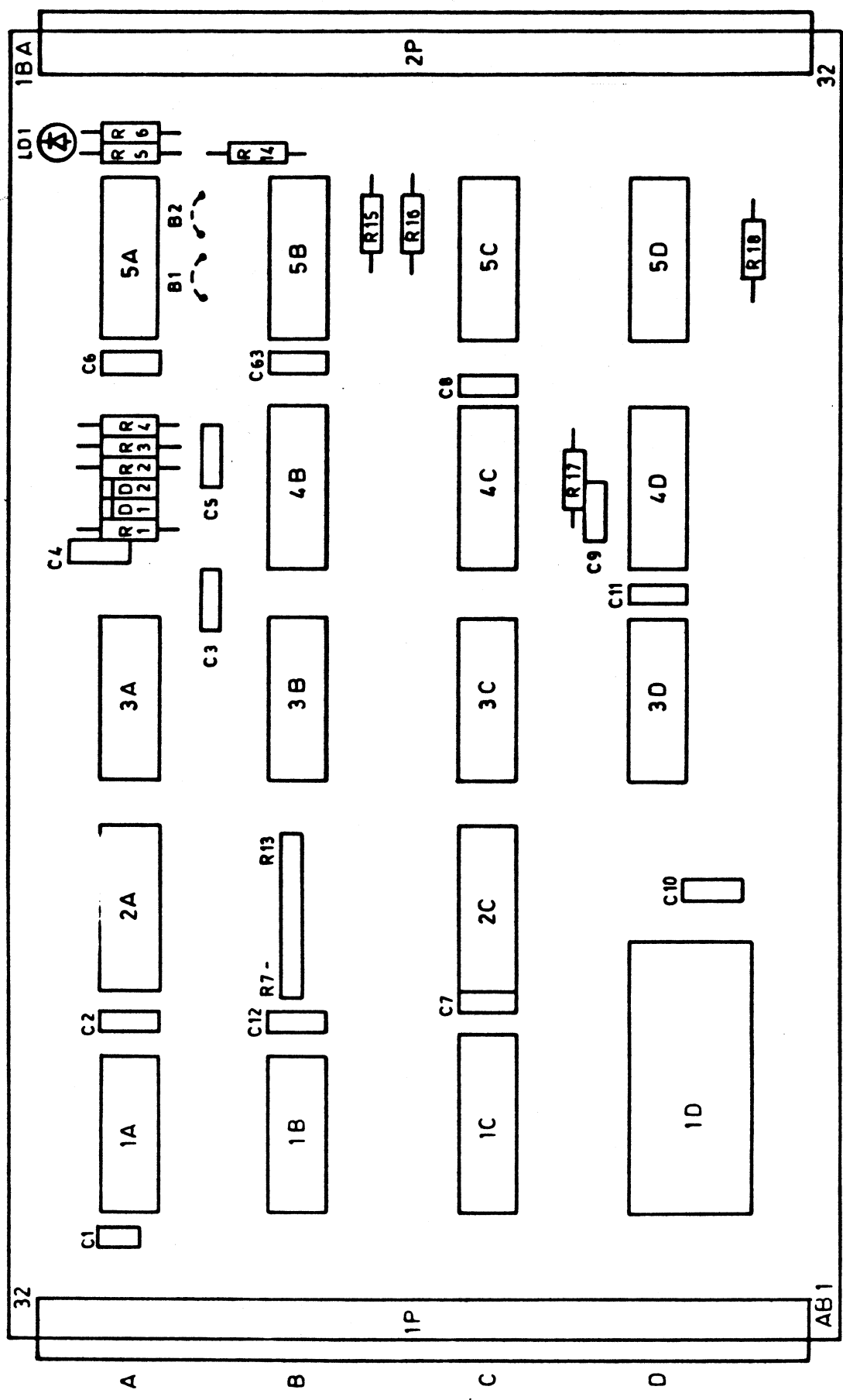
DATAINDUSTRIER AB		PRINTER INTERFACE	
B	REV. NR	TÄBY	SWEDEN
	79 03		
	DATUM	CENTRONICS	
		82-4038-00	

1 2 3 4 5 6



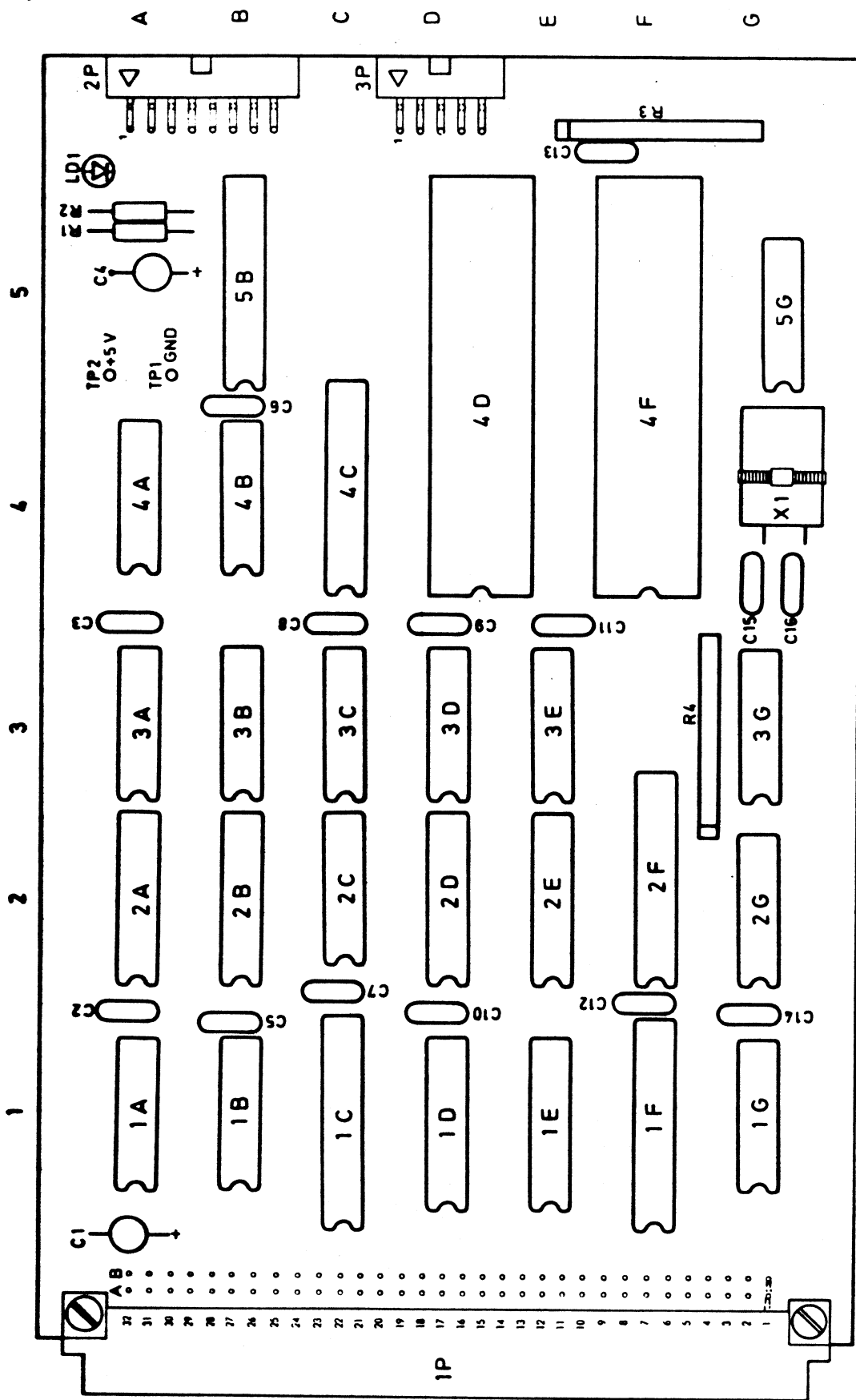
REV. NR	DATAINDUSTRIER AB	1/16 REED	82-4039-00
DATUM	1A BY	SWEDEN	

1 2 3 4 5



COMMENT: 2A = CODE PLUG FOR CHANNEL SELECTION
 JUMPER B1 INSTALLED GROUND TO BD 2 1
 " B 2 " " " 2 0

1. The customer has to be informed that the product is not to be used for any other purpose than the one intended for. The customer has to be informed that the product is not to be used for any other purpose than the one intended for.



A	REV	INT	HOK
830908	DATUM	8306.02	

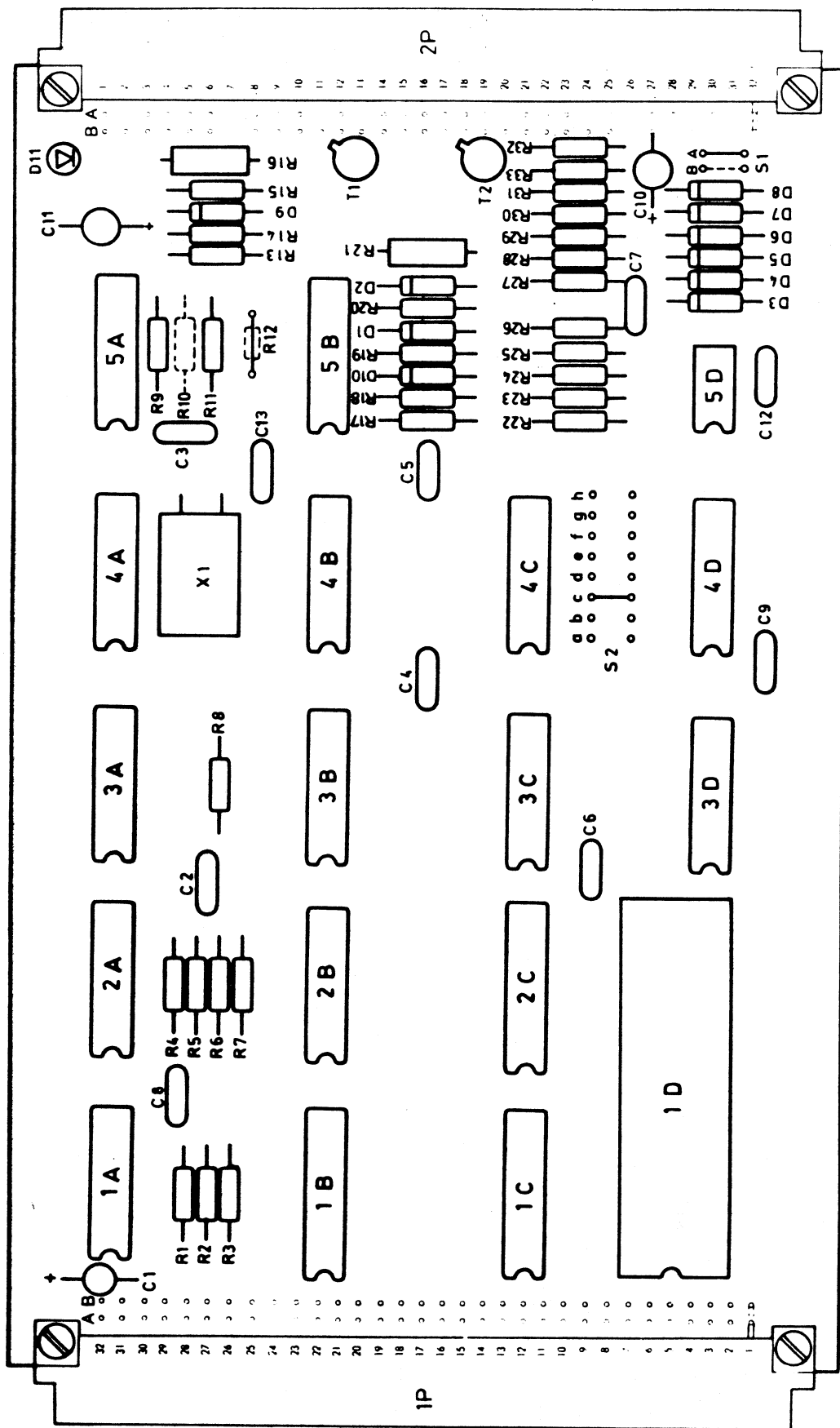
DATAINDUSTRIER AB
SWEDEN

STEP CONTROLLER

82-4066-01

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1 2 3 4 5



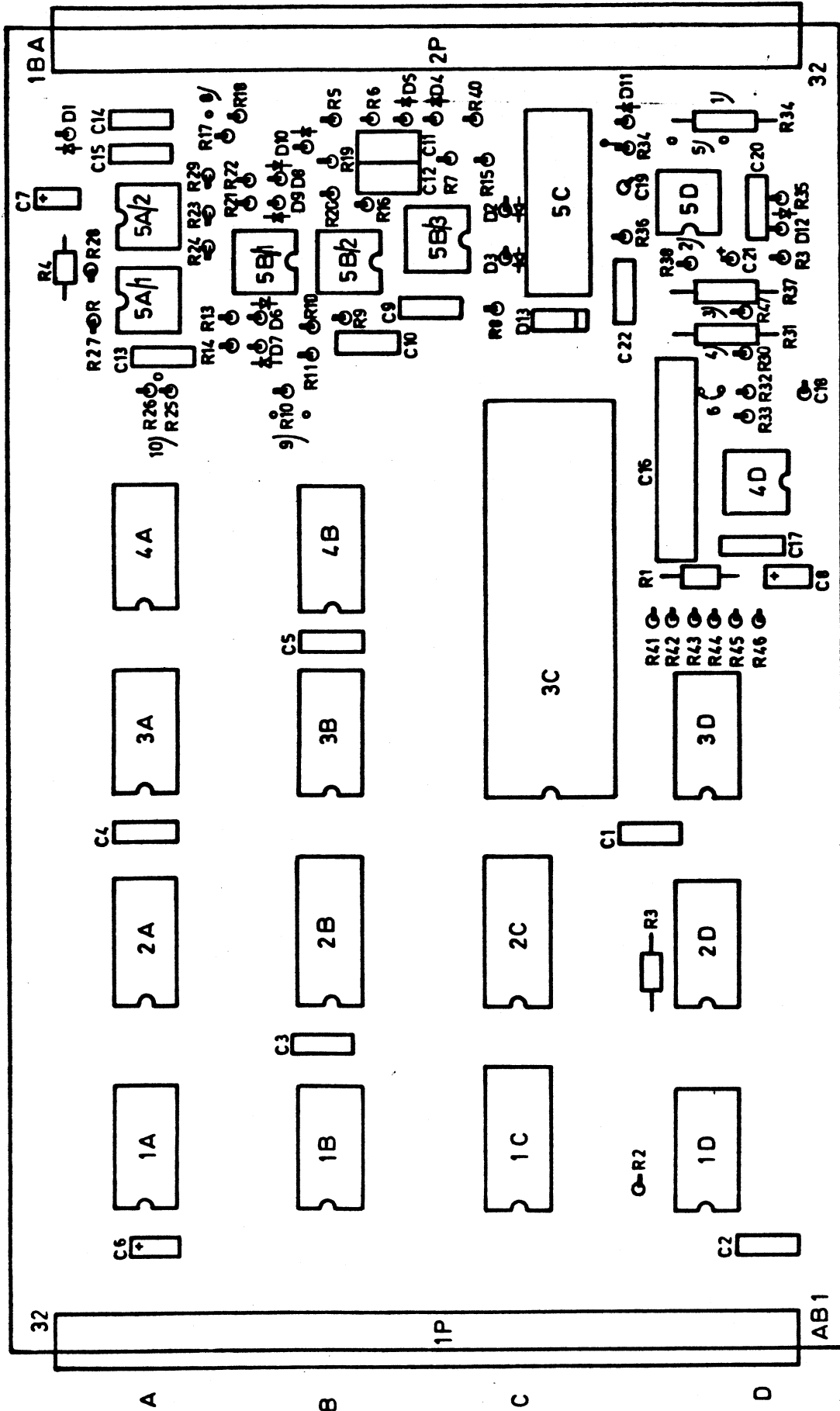
C	B	A	REV	RIT	DATE	REV
	84.06.25	831031	81.06.09			8104.02

DATAINDUSTRIER AB
SWEDEN

SLING UART

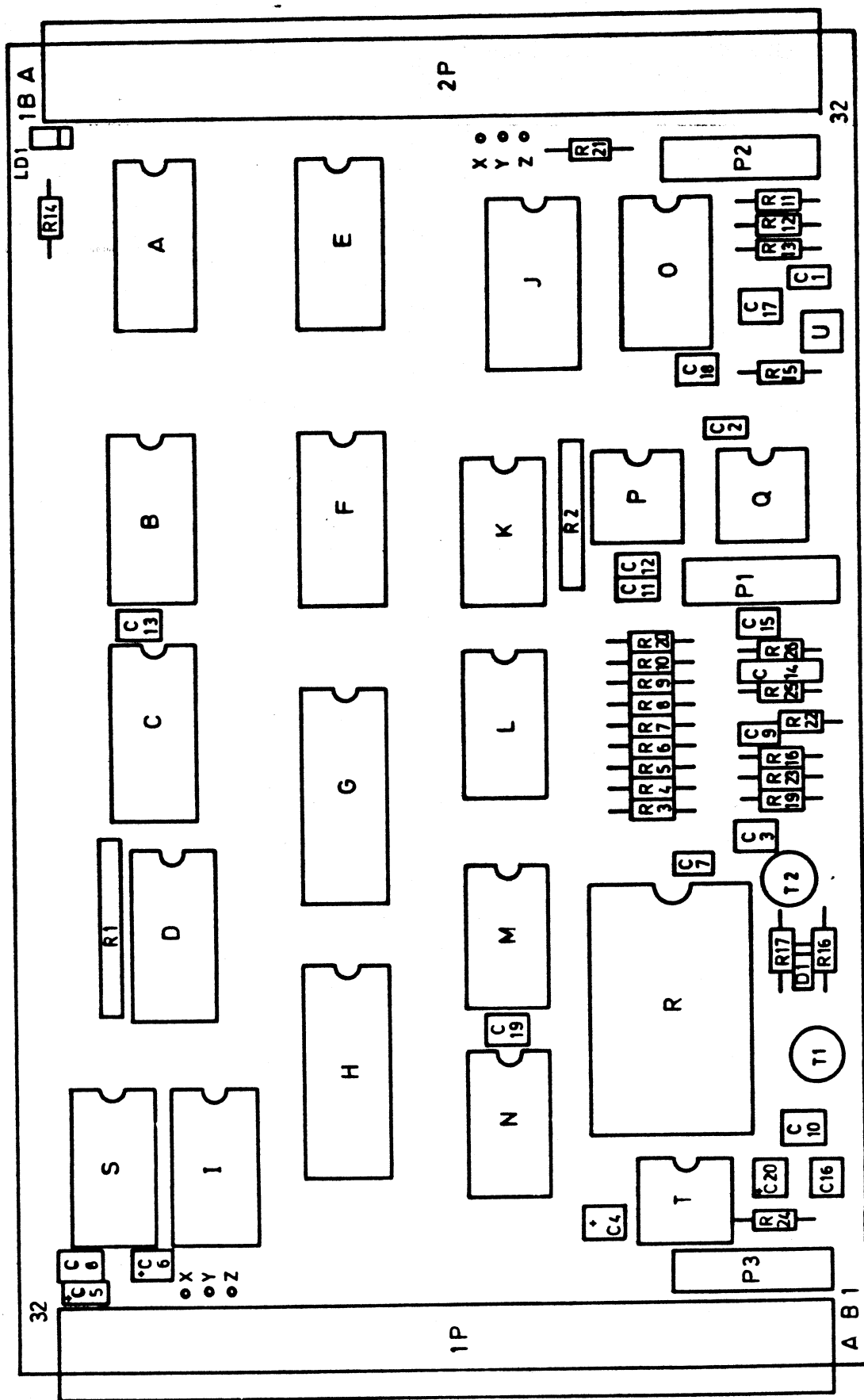
82-4073-00

1 2 3 4 5



COMMENT : 3D = CODE PLUG FOR CHANNEL SELECTION
THE COMMENTS 1, ETC. ARE DESCRIBED IN THE LOGIC DIAGRAM

A		REV NR	DATAINDUSTRIER AB		AUDIO CASSETTE INTERFACE		82-4074-00
79.03	DATUM		TÄBY	SWEDEN			



COMMENT : S = CODE SWITCH FOR CHANNEL SELECTION

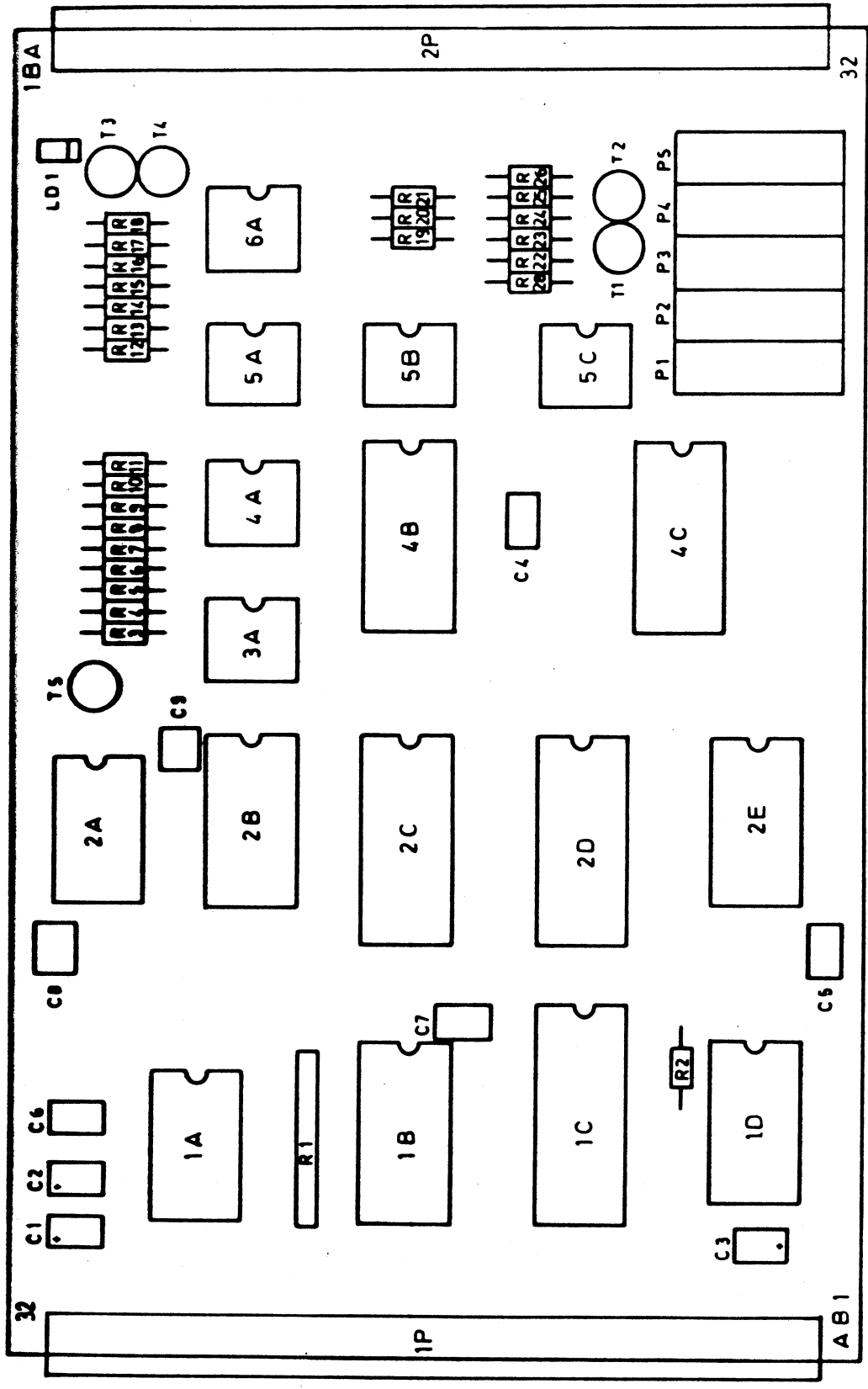
P1 ADJUSTMENT OF INTERNAL REFERENCE VOLTAGE

P2 ZERO ADJUSTMENT OF INPUT

P3 ZERO ADJUSTMENT OF A/D-CONVERTER OUTPUT
AT ± 0.1 INPUT RANGE

DATA INDUSTRIES AB		32 x 12 A/D CONVERTER		82-4082-00
A	REV NR	TABY	SWEDEN	
	79.03			

1 2 3 4 5 6



COMMENT: 1A - CODE SWITCH FOR CHANNEL SELECTION

THE OUTPUT ARE ADJUSTED BY POTENTIOMETERS AS FOLLOWS

P1 - REFERENCE VOLTAGE ADJUSTMENT

P2 - V2 OUT

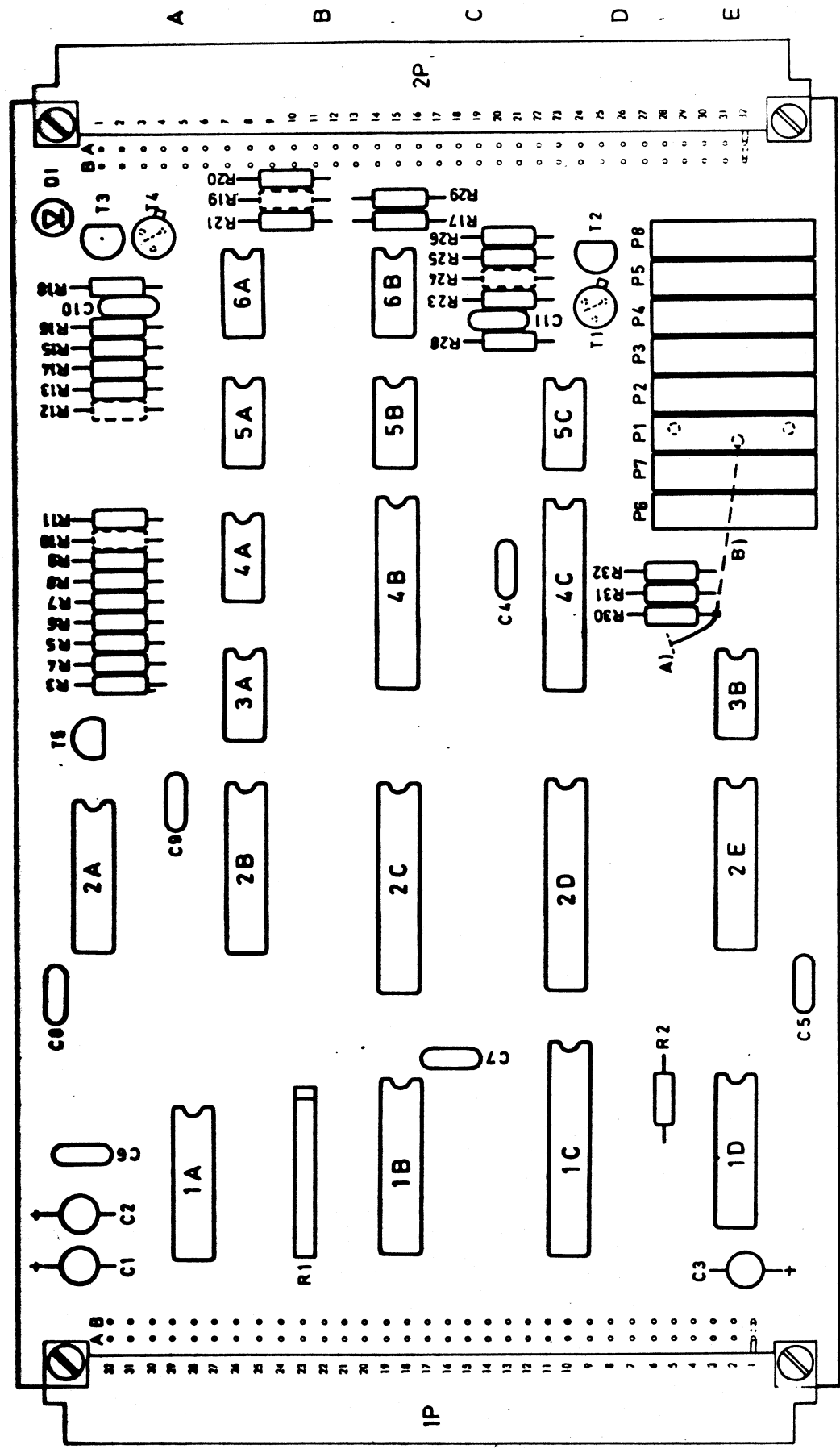
P3 - V1 OUT

P4 - I2 OUT

P5 - I1 OUT

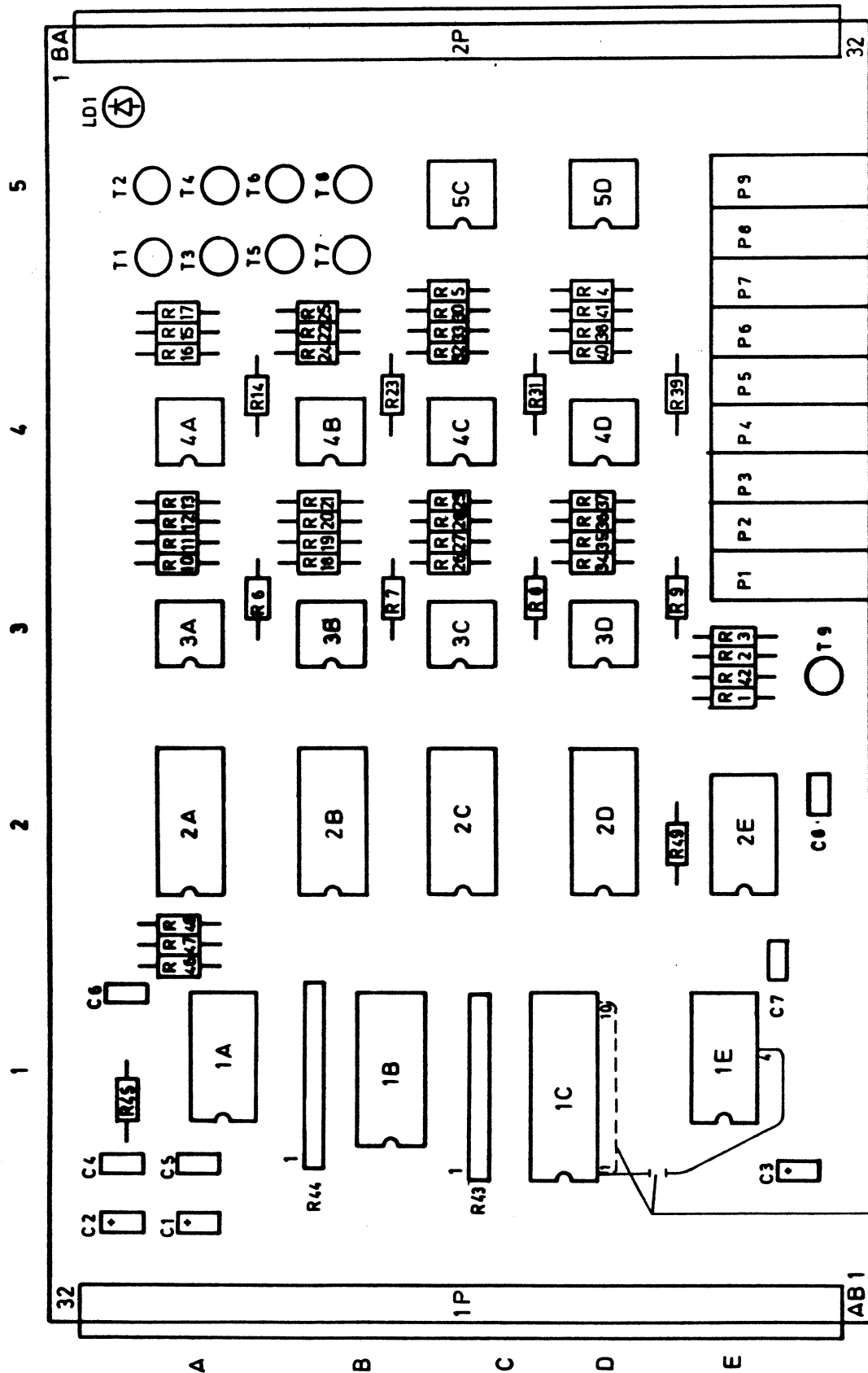
DATAINDUSTRIER AB		2 x 12 D/A CONVERTER		82-4083-00
B	REV NR	TÄBY	SWEDEN	
	7303			

1. The board is designed for use with the following components:
 2. The board is designed for use with the following components:
 3. The board is designed for use with the following components:
 4. The board is designed for use with the following components:
 5. The board is designed for use with the following components:
 6. The board is designed for use with the following components:



- A) CU-trace must be cut
- B) Cable connection on siderside of the board

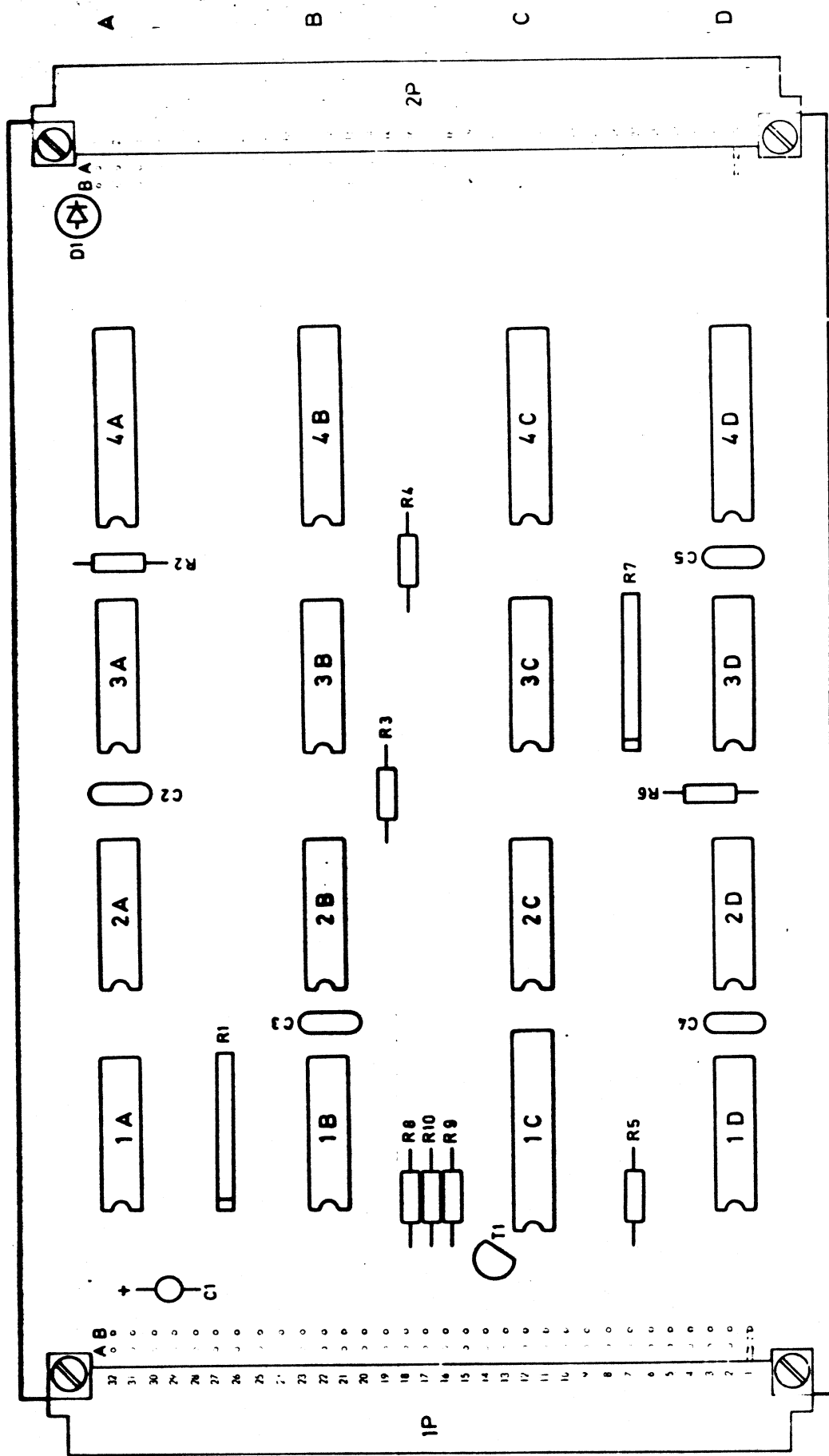
				DATAINDUSTRIER AB		12 BIT D/A 2 CHANNELS		82-4083-10	
				SWEDEN					



Strap wire 1C:1 - 1C:10
Cut trace 1C:1 - 1E:4

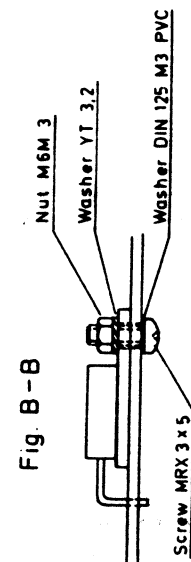
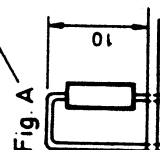
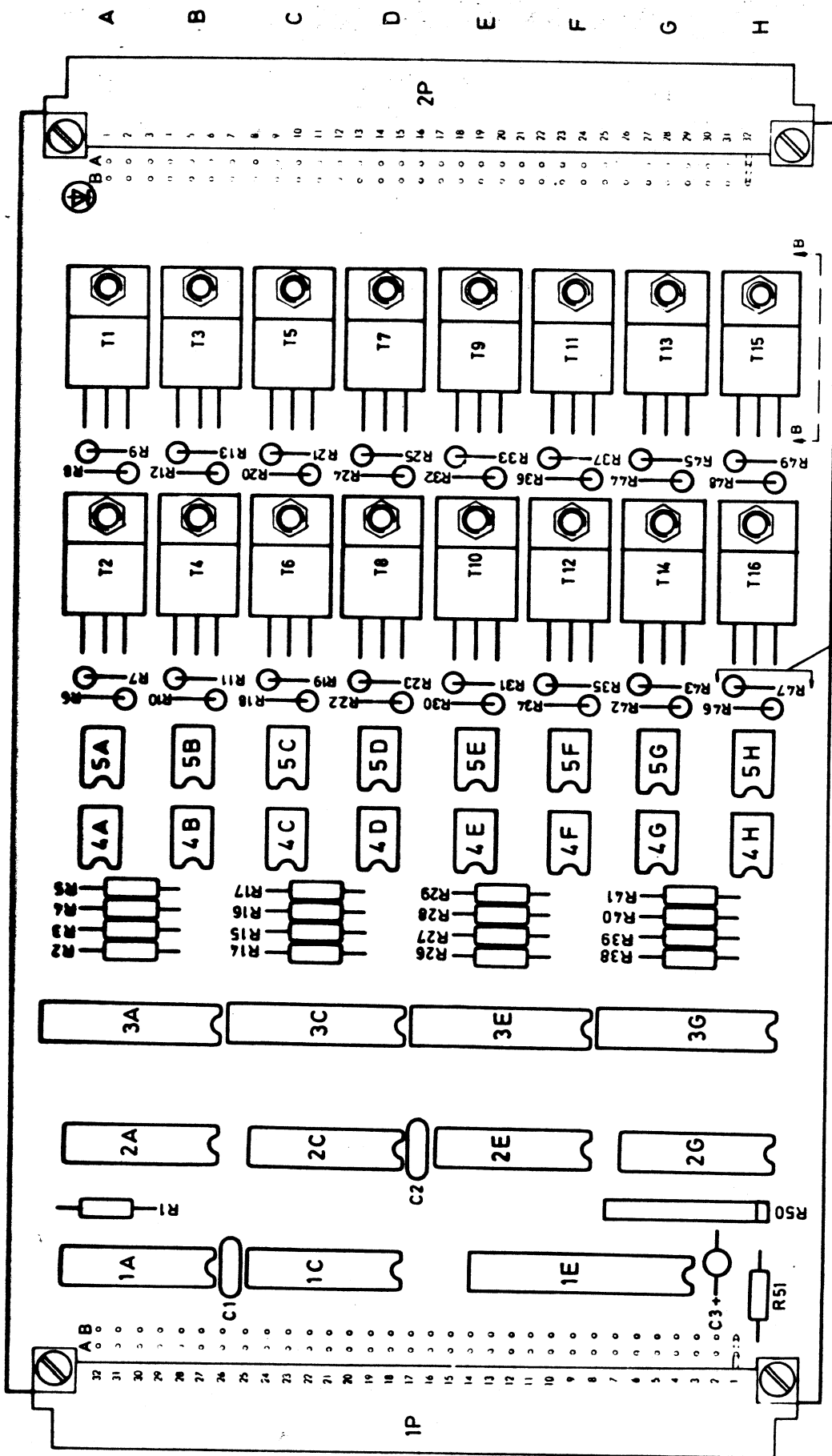
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1 2 3 4





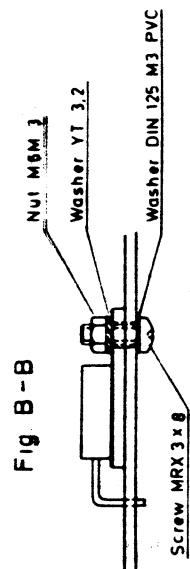
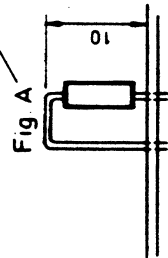
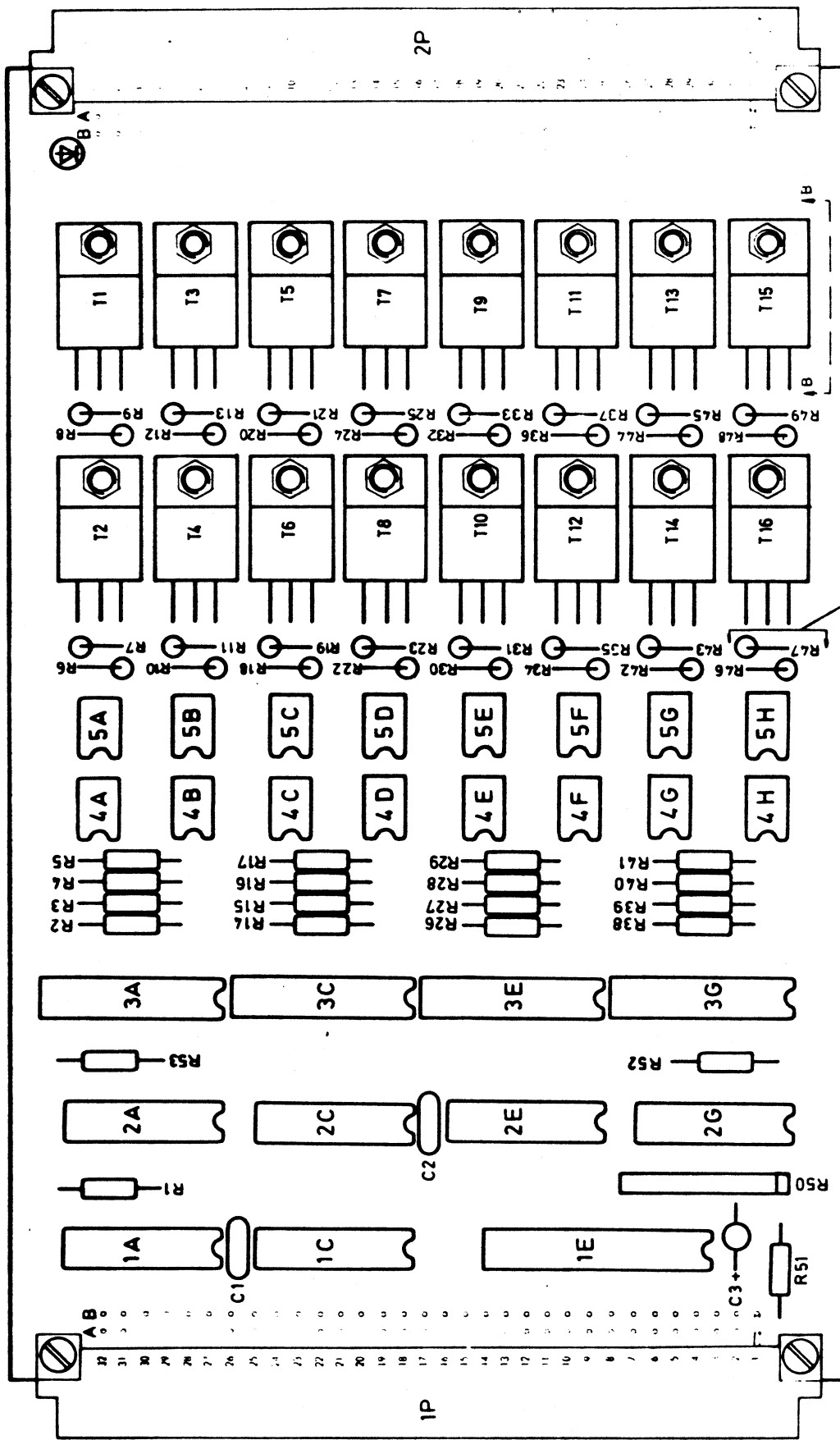
1 2 3 4 5

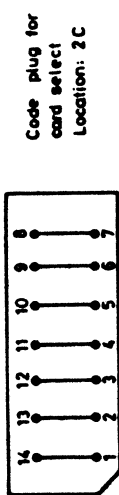
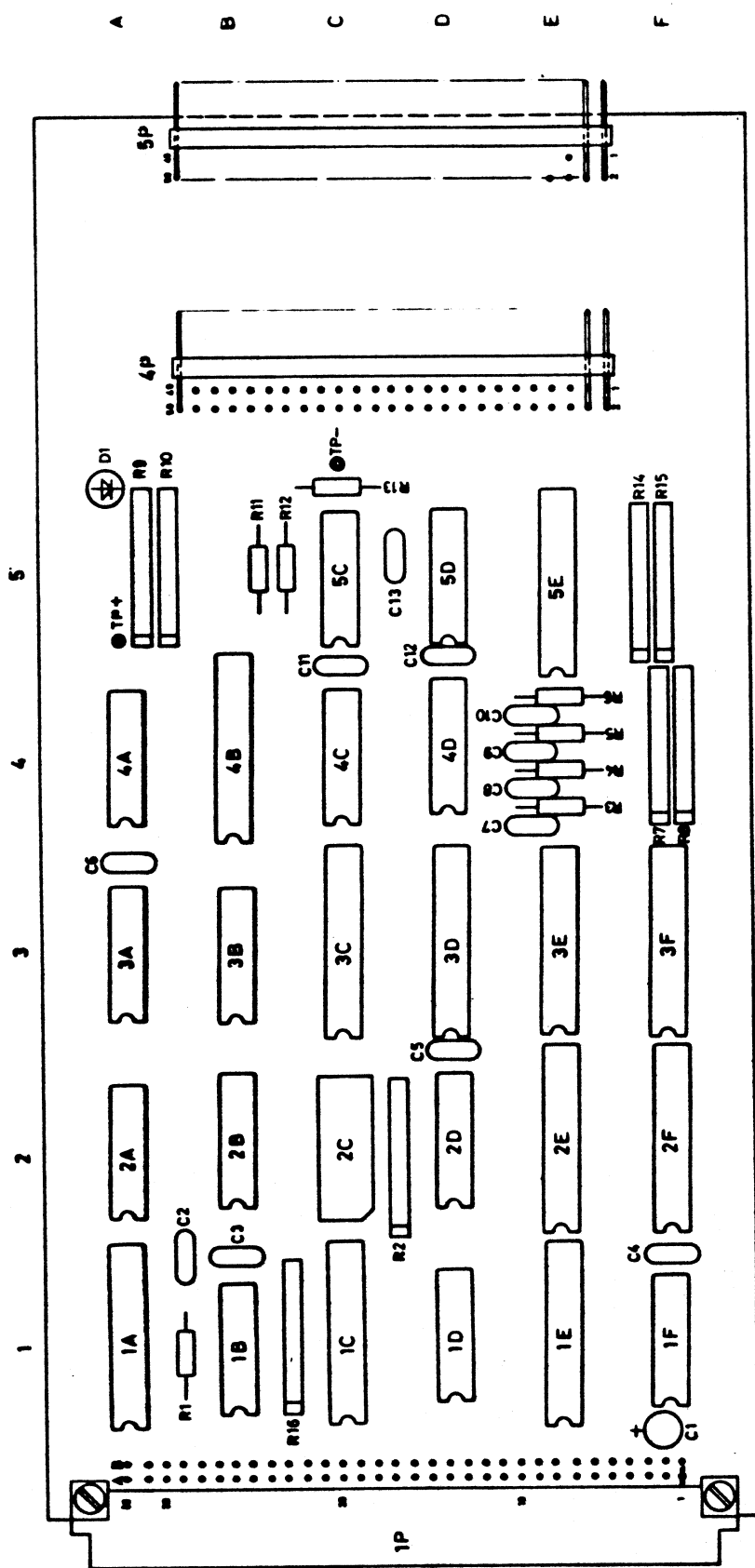


DATAINDUSTRIER AB SWEDEN		16 OPTO OUT		82-4095-00	
C	B	A	REV	MIT	HGK
830826	830314	810827	DATUM	800626	

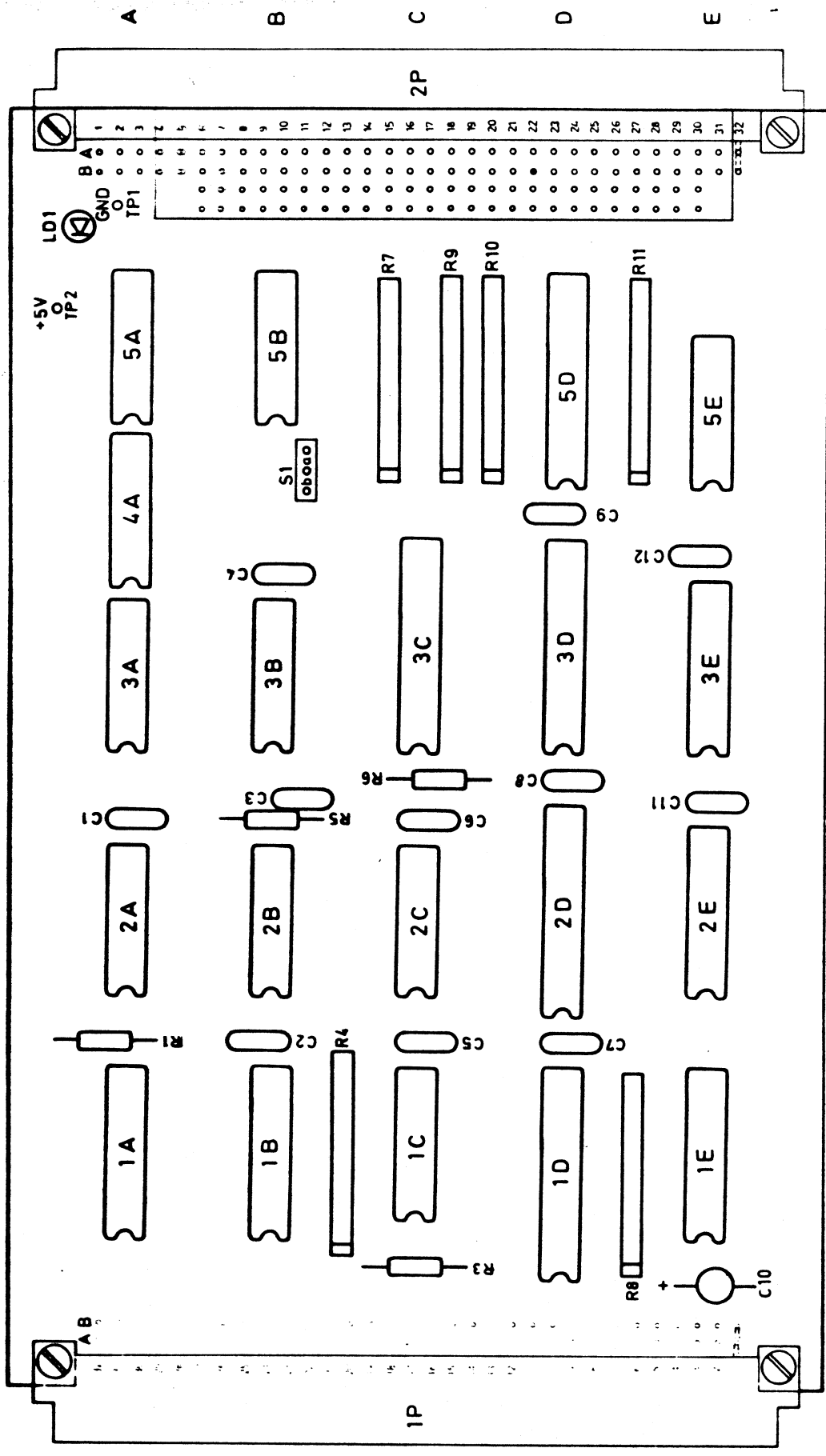
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1 2 3 4 5



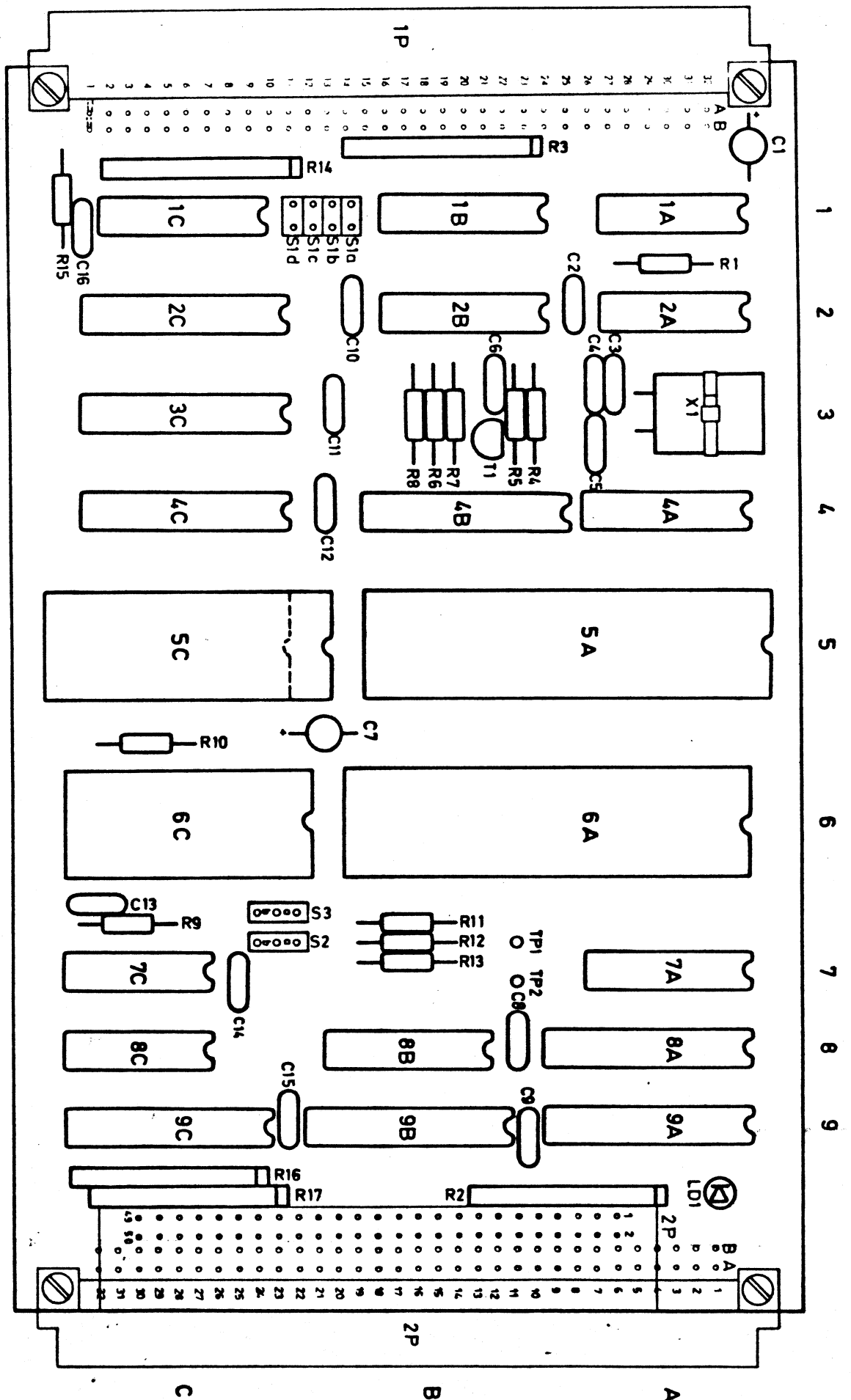


1. The product shall not be used without the
 2. The user is responsible for the safety of the
 3. The user is responsible for the safety of the
 4. The user is responsible for the safety of the



DATAINDUSTRIER AB				4680 XEBEC INTERFACE		82-4105-10	
SWEDEN							
A	REV	BIT	MK				
83.06.07			83.05.19	DATUM			

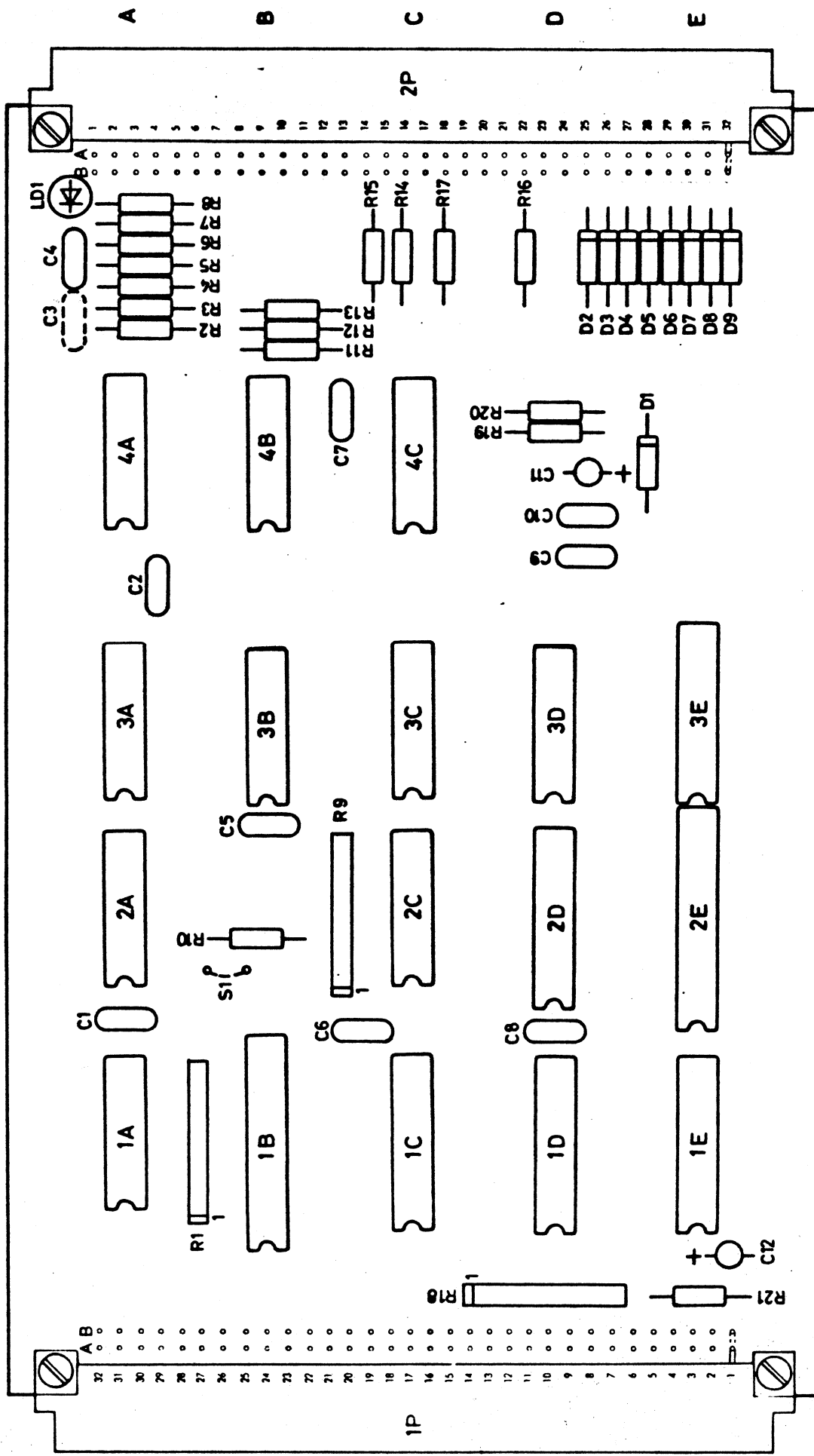
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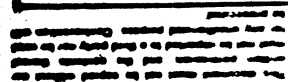
- S1: CS number (40Q-57Q)
- S2: a 2kPROM at 6C (default)
b 4kPROM at 6C (option)
- S3: a 2kRAM at 5C (default)
b 8kRAM at 5C (option)

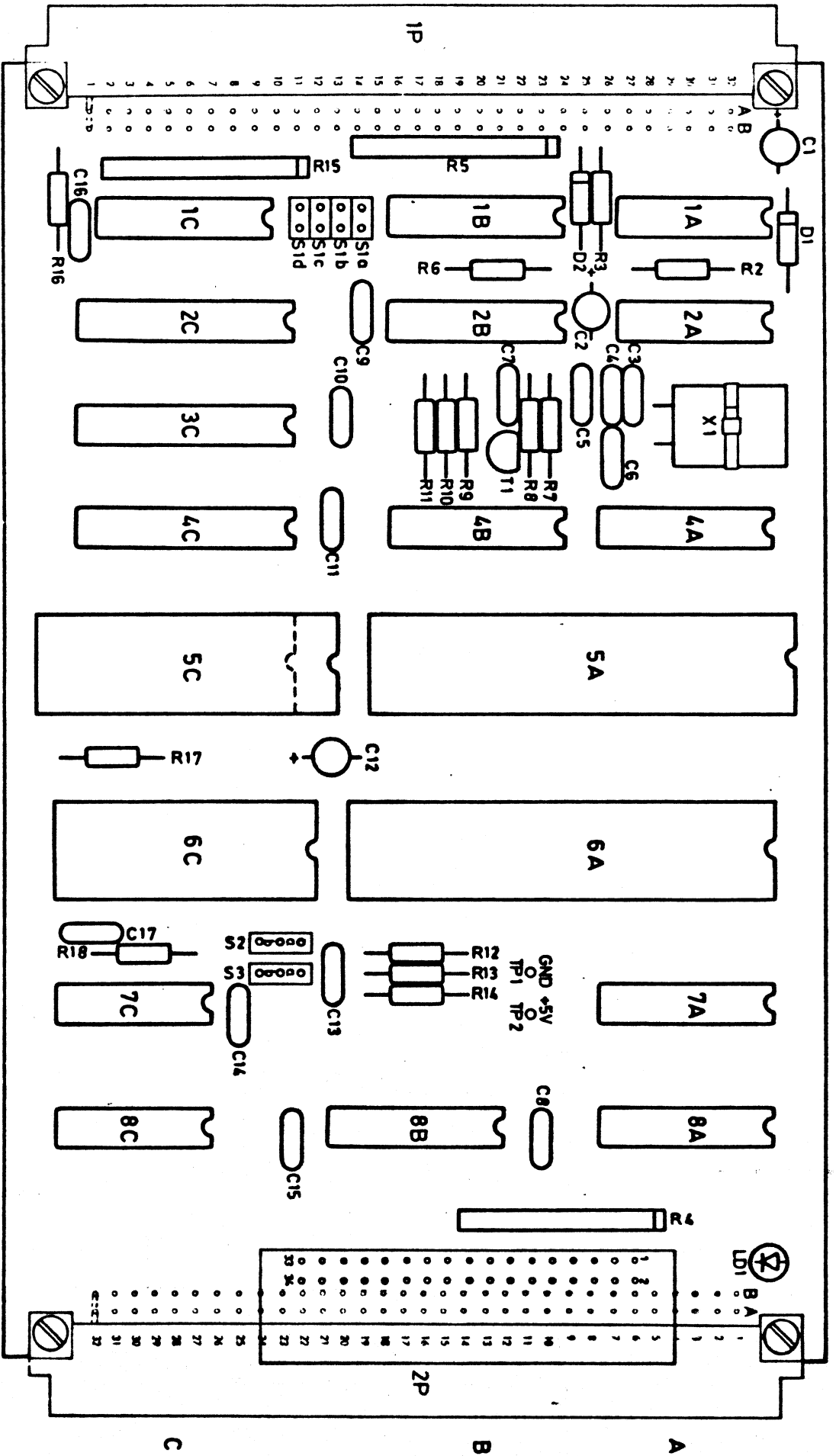
REV	BIT	M/K	DATA IN	TRIER AS	9C80 XEBEC INTERFACE	82-4107-00
03 06 09	02 05 28	02 03 01	DATUM	02 02 10		

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DATAINDUSTRIER AB				DISC INTERFACE		82-4109-00	
REV	RTY	Q.L.	DATUM				
		79-12-04					

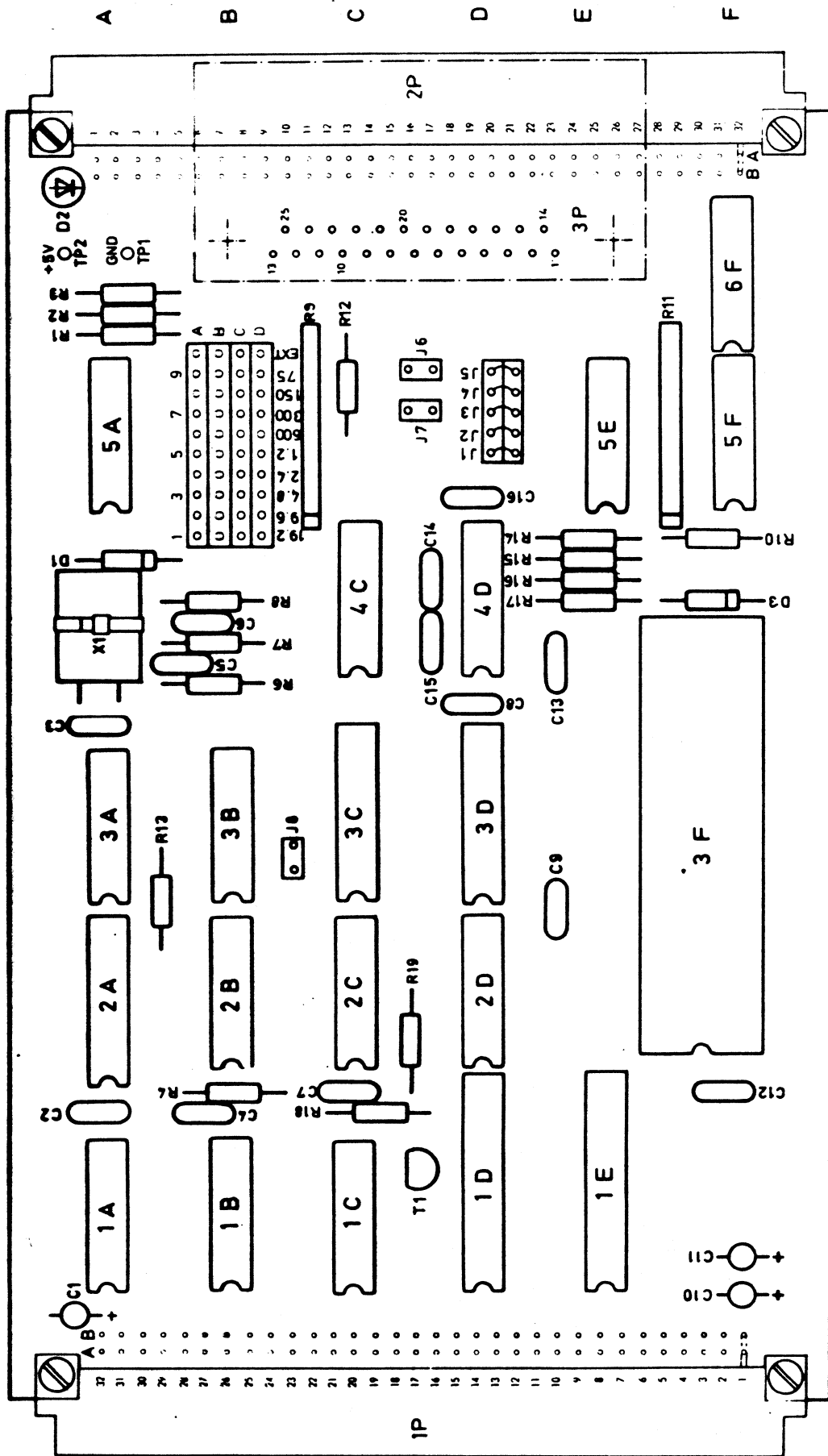




	B	A	REV	DIT	POL	DATAINDUSTRIER AB PROMEN	AD-CONVERTER 12 bit/32 channels	02-6115-10
	010817	01.08.91	000000	010808				

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1 2 3 4 5 6



J1 off = no parity
J1 on = parity
J2 off = even parity
J2 on = odd parity
J3 off = two stop bits
J3 on = one stop bit

J4 on = inhibits transmit buffer empty (Mot. spec 2)
J7 on = Mot. spec. 2 input inverted
J8 off = no interrupt on Mot. spec. 1
J8 on = interrupt on Mot. spec. 1

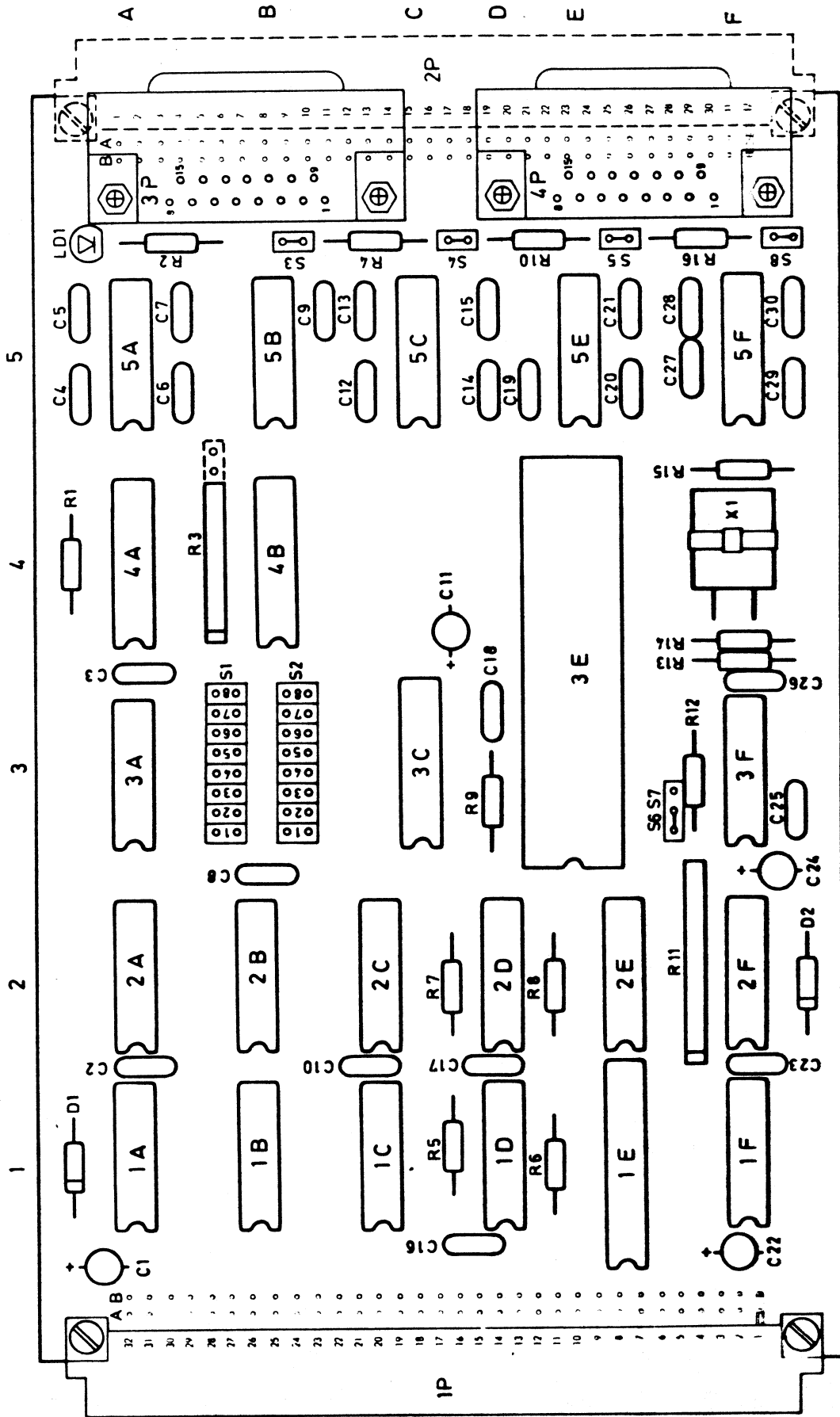
J5 J4 bits per word

J5	J4	bits per word
on	on	5
on	off	6
off	on	7
off	off	8

E		D	C	B	A	REV	MIT	DATUM	
8306 09		8304 11	820714	810409	801015			800125	

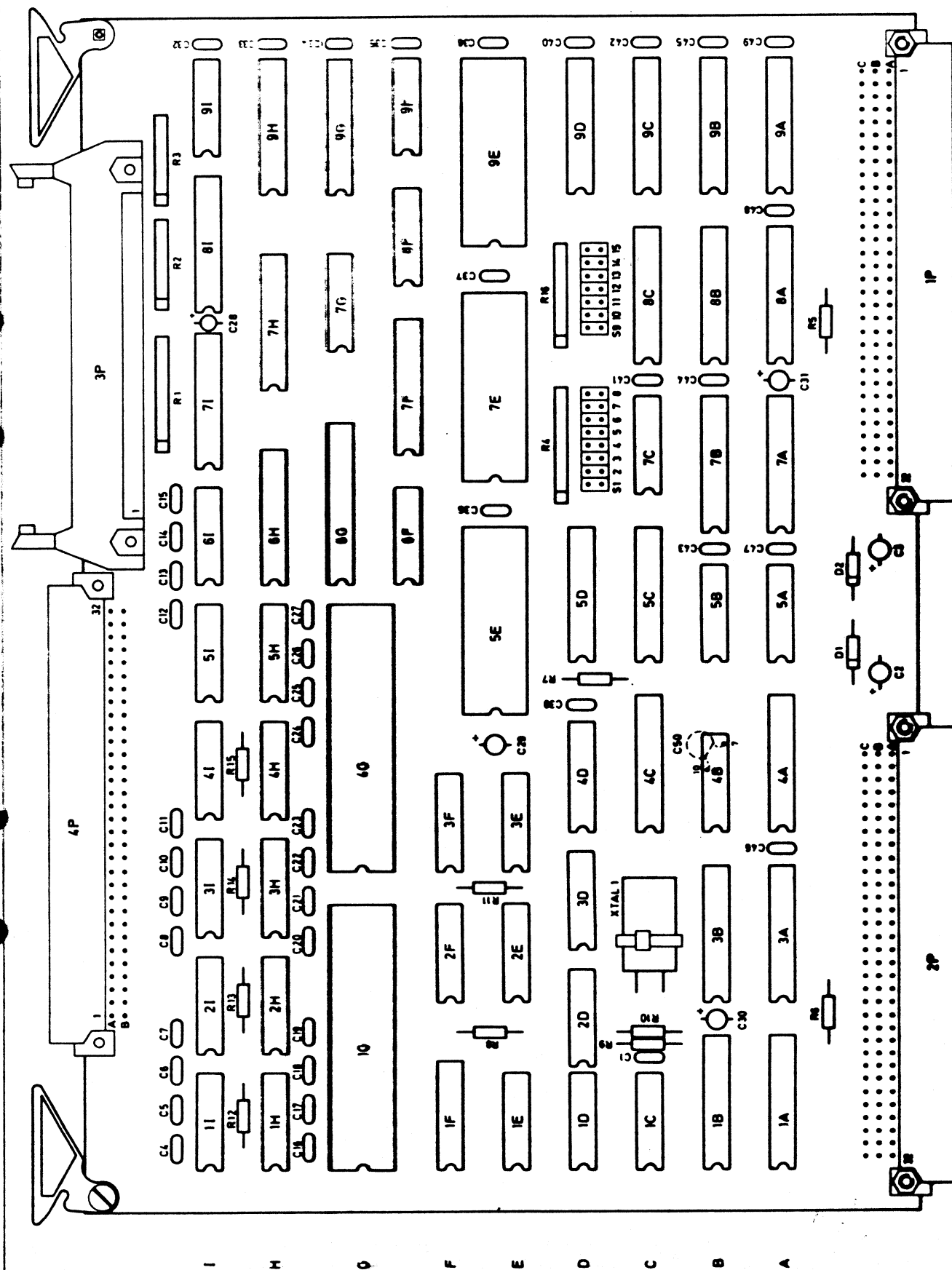
DATAINDUSTRIER AB UART - 3
SWEDEN
82-4117-00

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- S1 Baudrate select CB (default = pos. 6 300 db.)
- S2 Baudrate select CA (default = pos. 6 300 db.)
- S3 Closed = -12V to CB connector (default)
- S4 Closed = +12V to CB connector (default)
- S5 Closed = -12V to CA connector (default)
- S6 Closed for 4800 double board computer (default)
- S7 Closed for ABC and 4 MHz SBC
- S8 Closed = +12V to CA connector (default)

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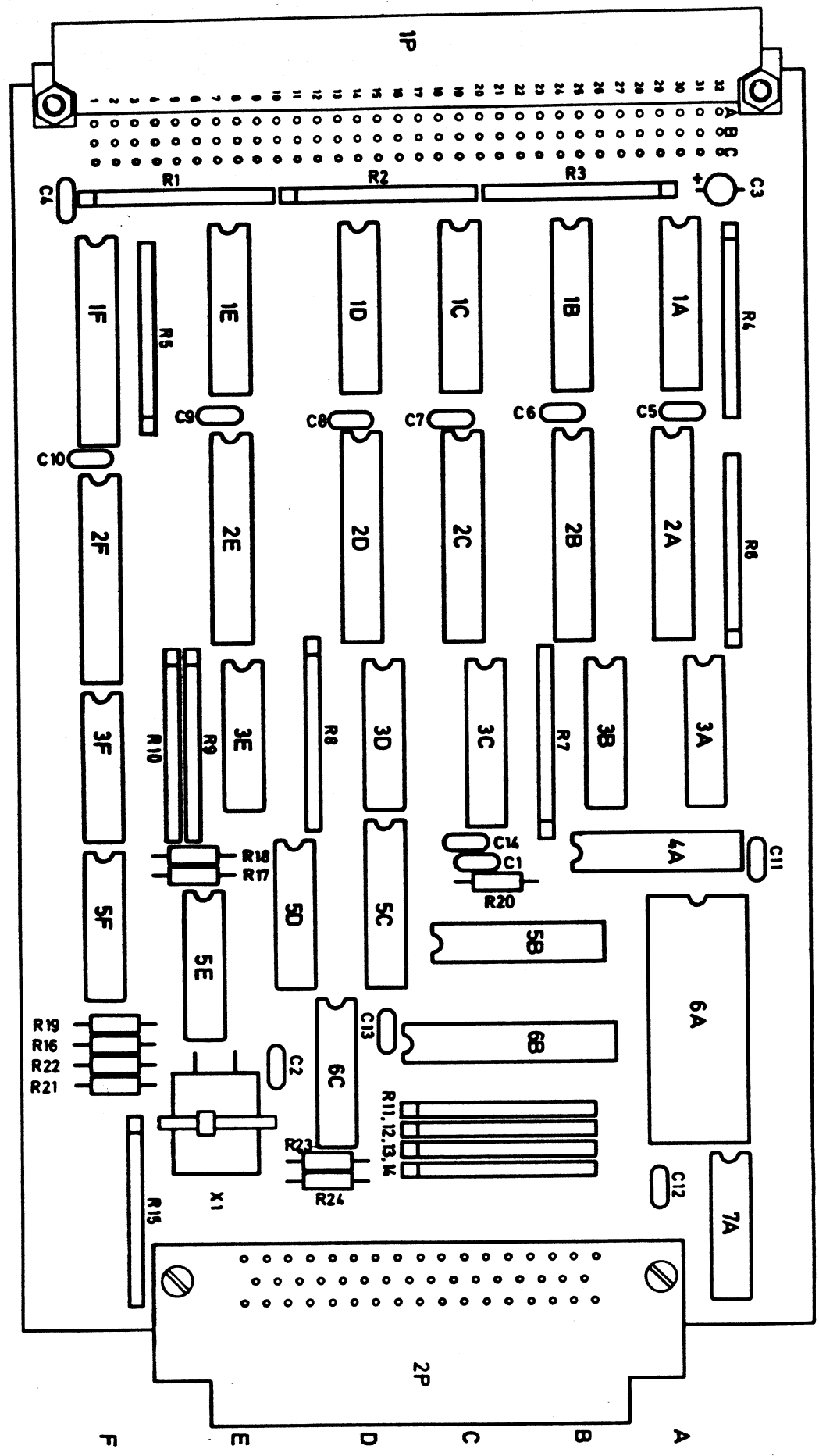


2001 610 mod. 14 10/10/01

REV.	DATE	DESCRIPTION	BY	CHKD.
1	10/10/01	10/10/01		
2	10/10/01	10/10/01		
3	10/10/01	10/10/01		
4	10/10/01	10/10/01		
5	10/10/01	10/10/01		
6	10/10/01	10/10/01		
7	10/10/01	10/10/01		
8	10/10/01	10/10/01		
9	10/10/01	10/10/01		
10	10/10/01	10/10/01		

COMBO

82-4200-00

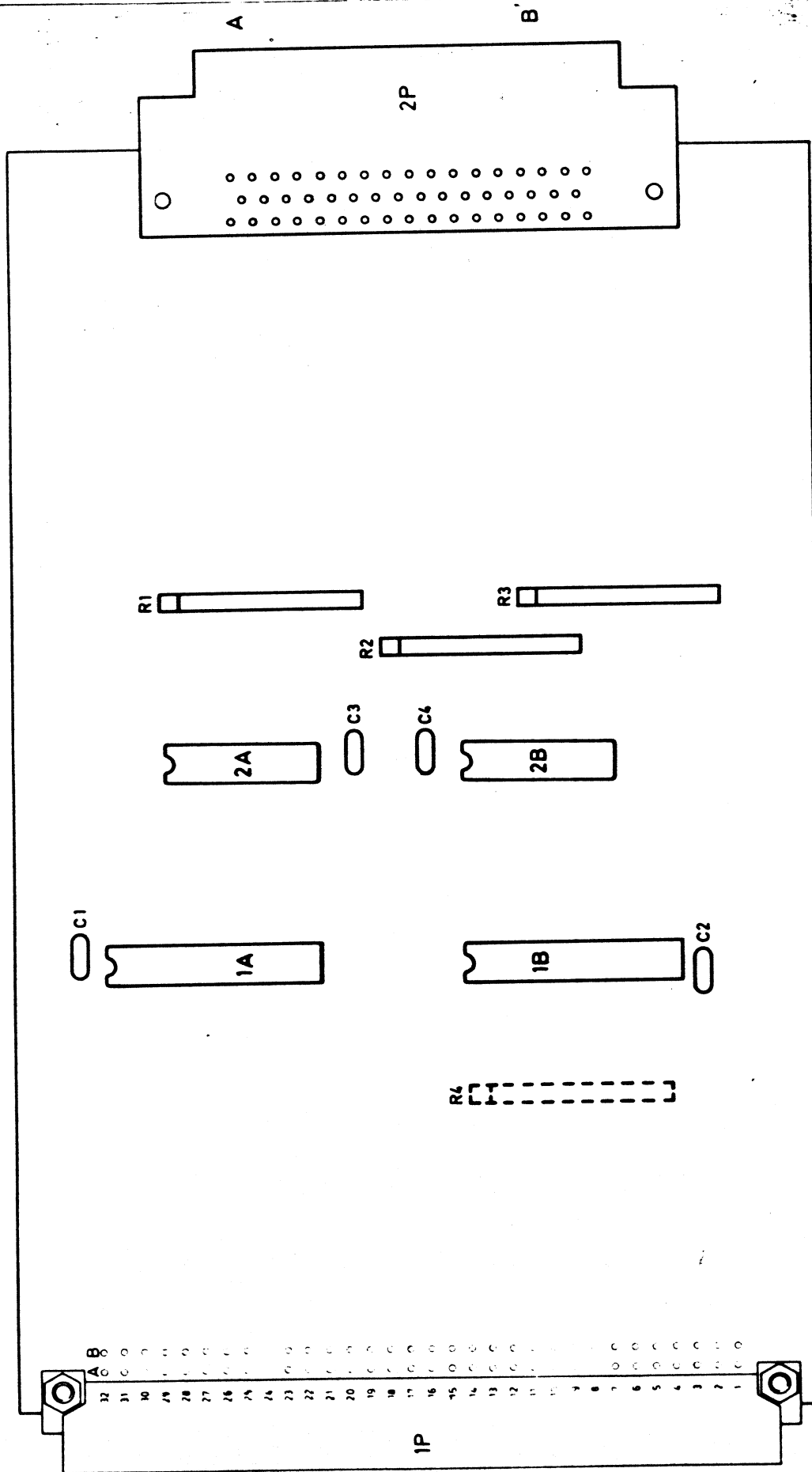


1 2 3 4 5 6 7

REV.	DESIGN	DL	MS	DATE	6/6/58
DATA SYSTEMS AND					
DMI I/O ADP EXP					
82-4202-00					

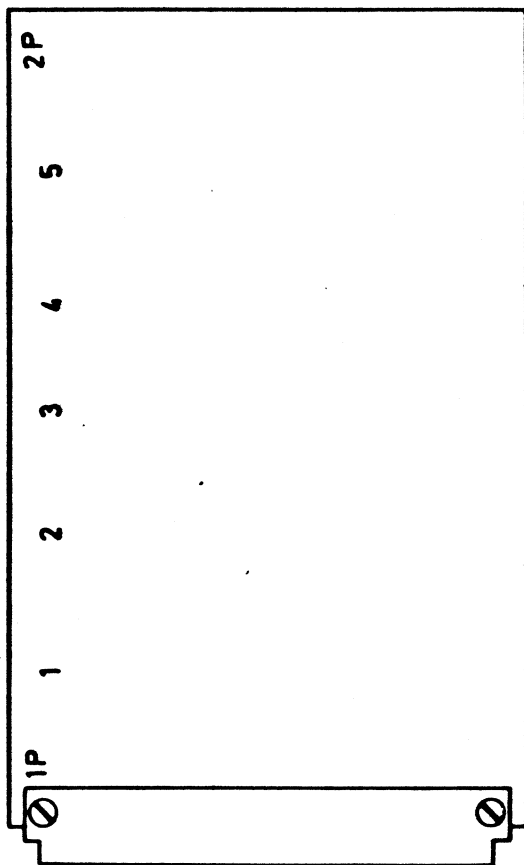
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REV		DESIGN	DATE	82-4203-00	
1	2	3	4	OMI I/O ADP DS90	82-4203-00
REVISION				82-4203-00	
DATE				82-4203-00	

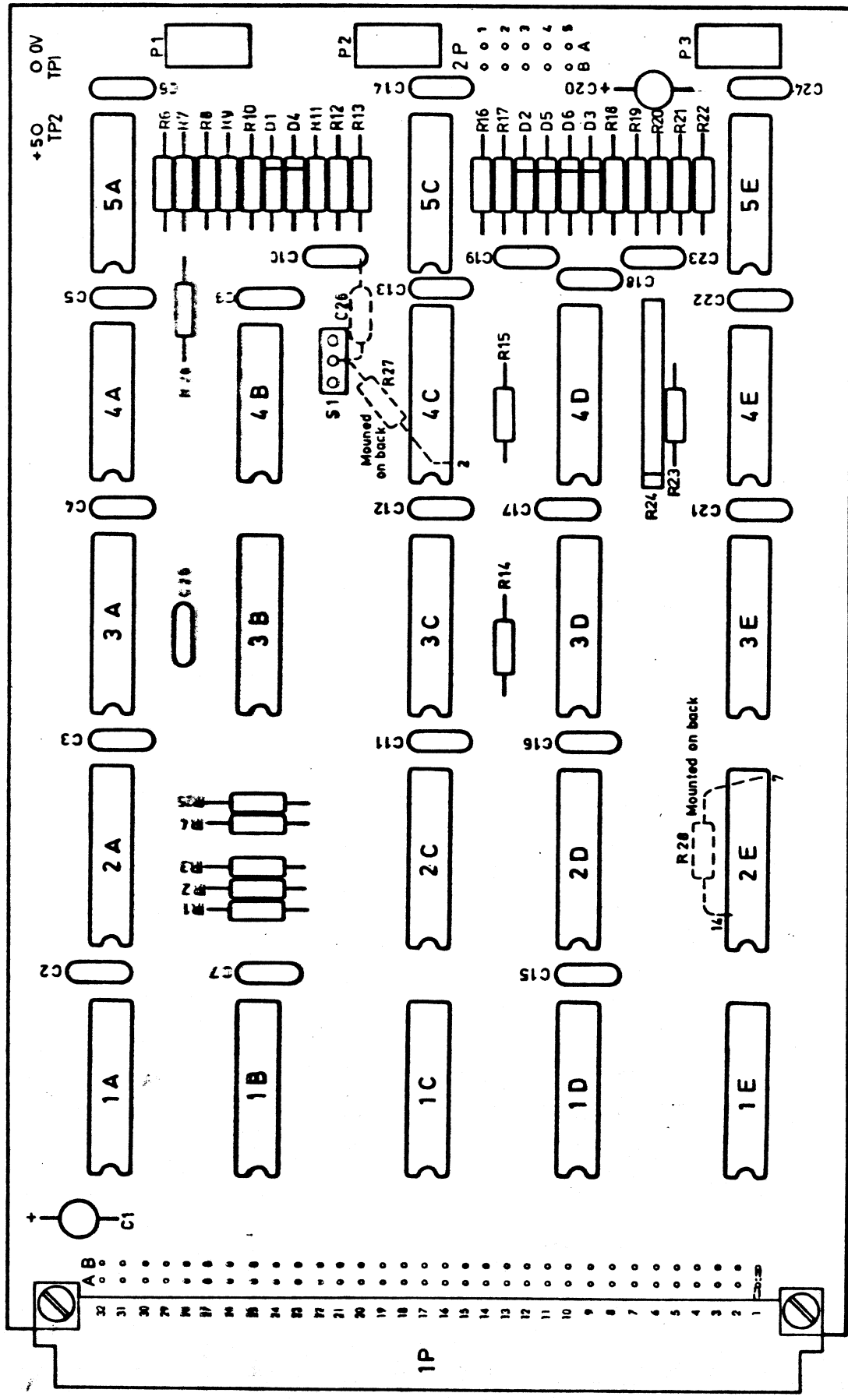
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Each hole in the board must be protected against soldering except for connector 1P

				DATA INDUSTRIES AB		WRAP BOARD		82-5001-00
				REV.	DESIGN	HGK	021206	
				DATE				

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G-MIX		DATAINDUSTRIER AB		82-5034-00	
SWEDEN		G-MIX		82-5034-00	
C	B	A	REV	RIT	HGK
030205	02.02.22	8104.27	DATUM	8012.05	

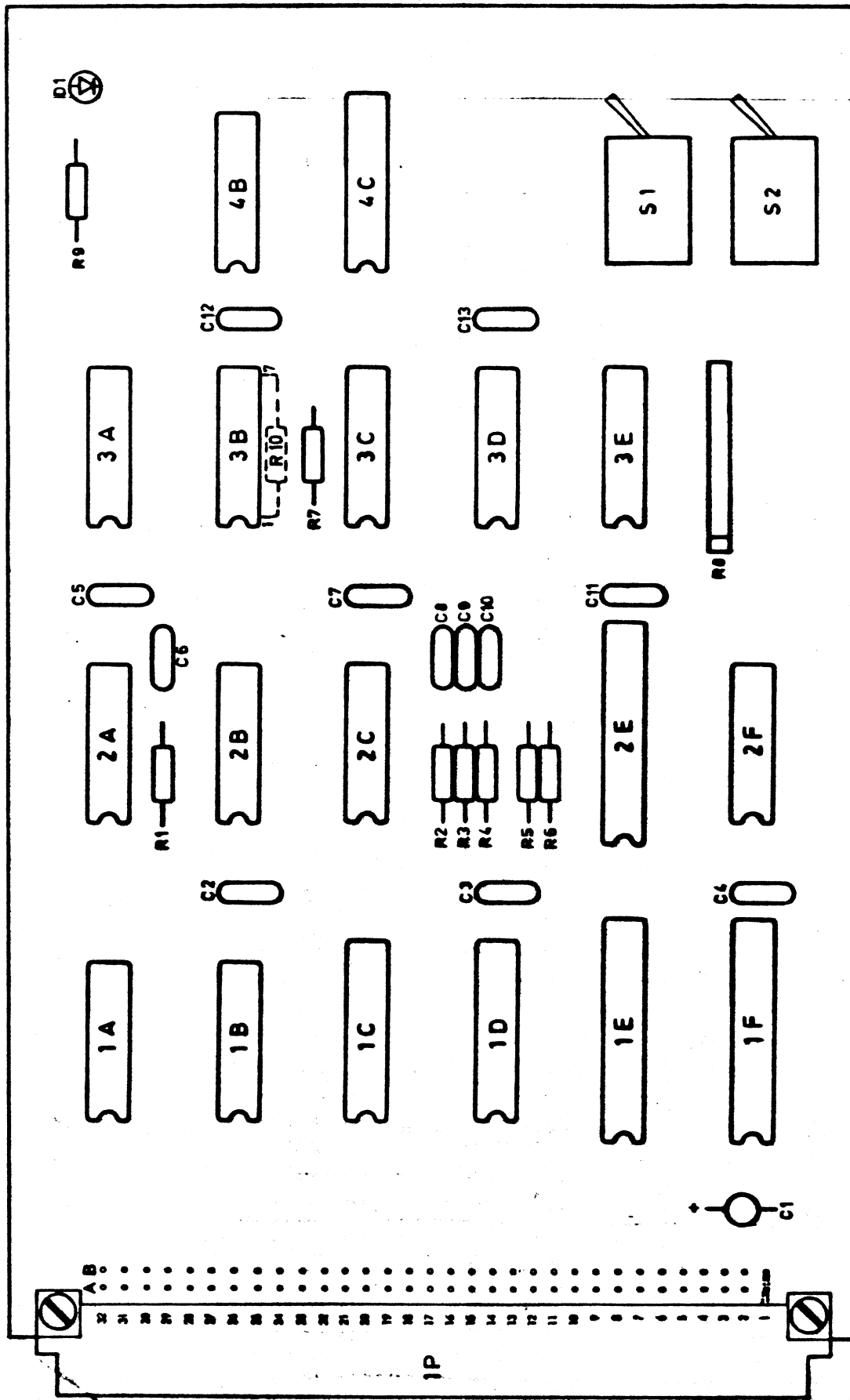
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C	A	REV	INT	HGX

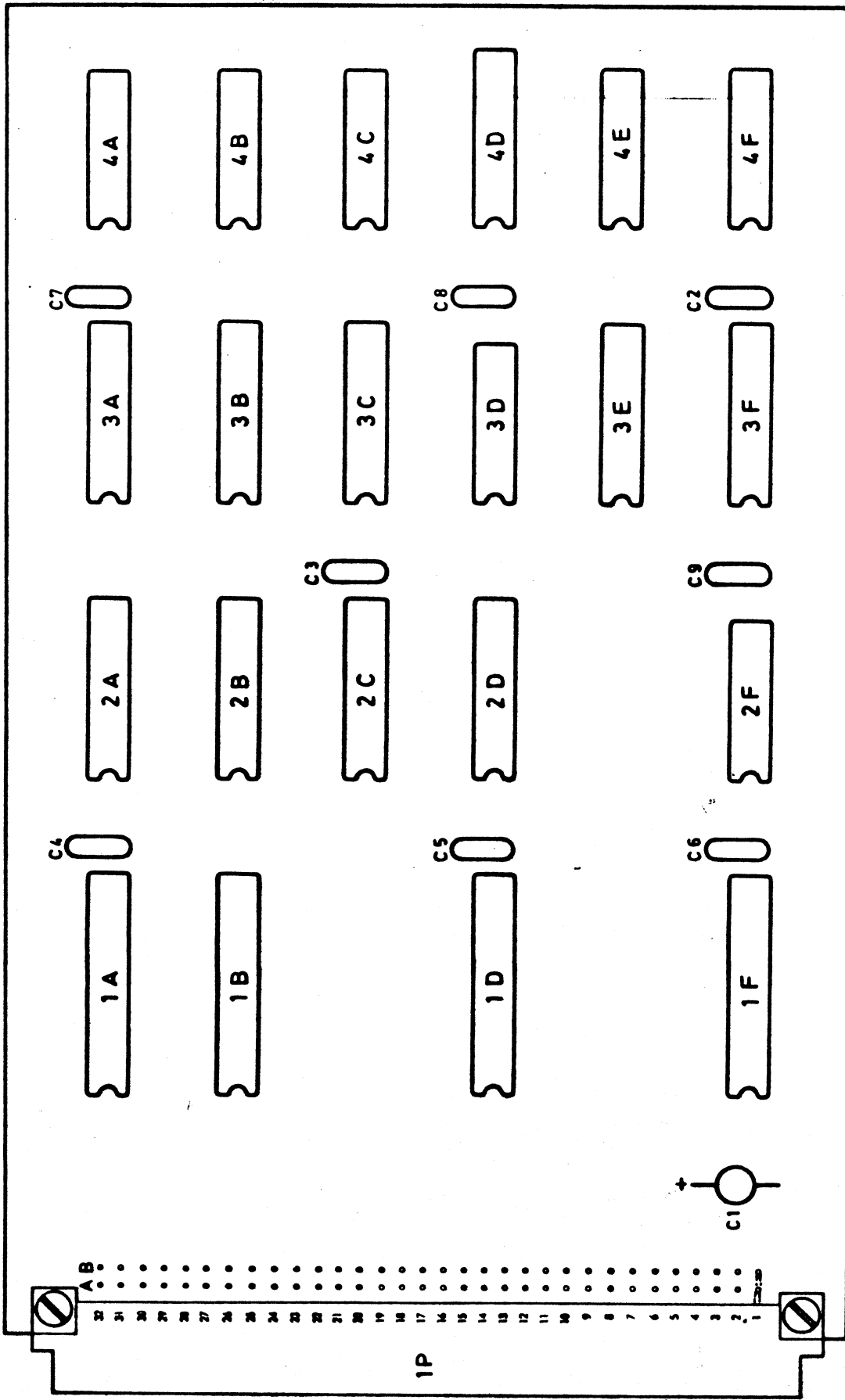
DATAINDUSTRIER AB
SWEDEN

G - 1/0

82-5035-00

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REV	INT	HOK
DATUM	NO 12 03	

DATAINDUSTRIER AB
SWEDEN

G-ADR

82-5036-00

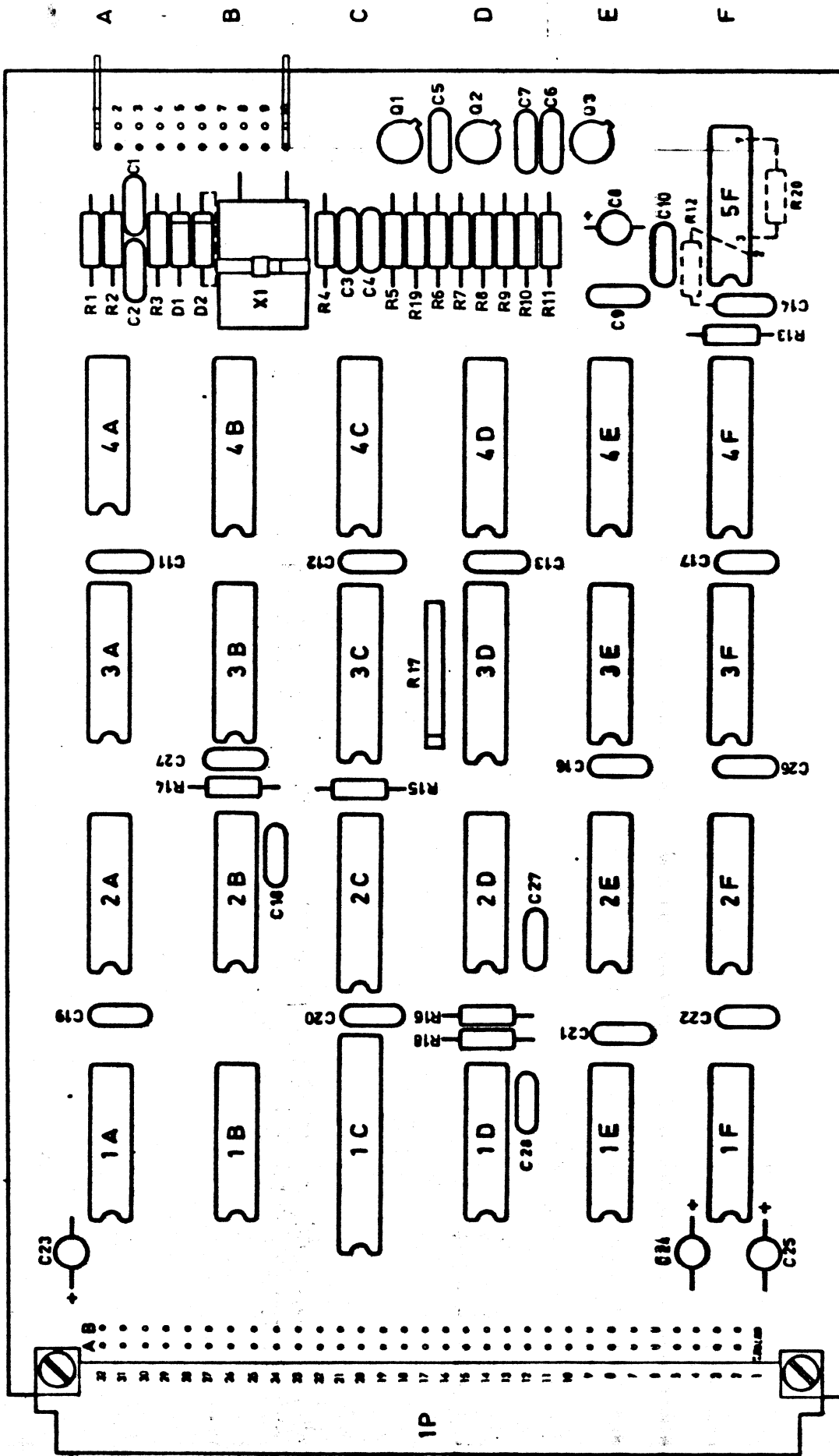
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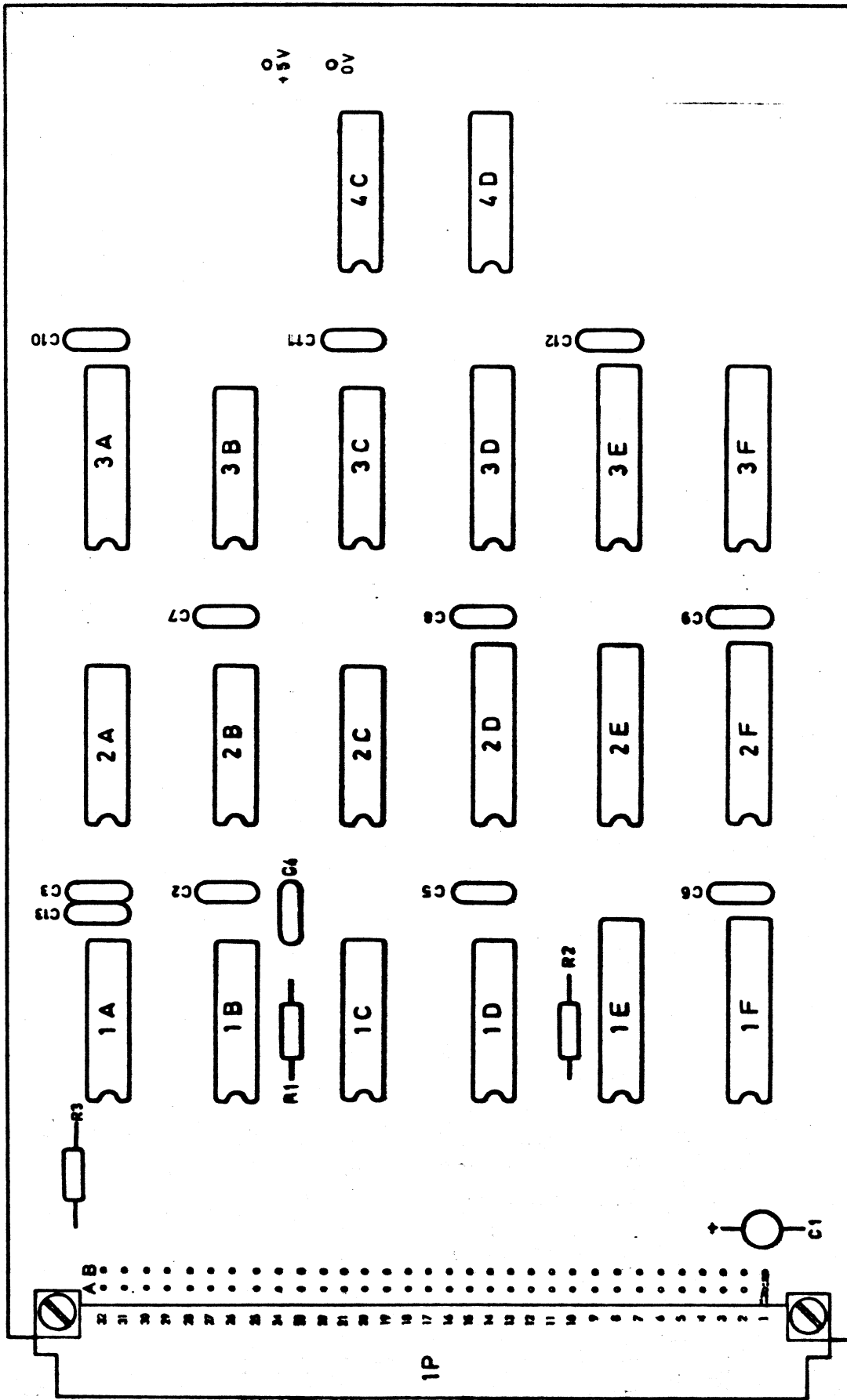
D	C	B	A	REV	INT	HGX
83.06.09	81.02.05	82.06.01	81.04.27	DATUM		80.12.08

DATAINDUSTRIER AB
SWEDEN

G - SYNC

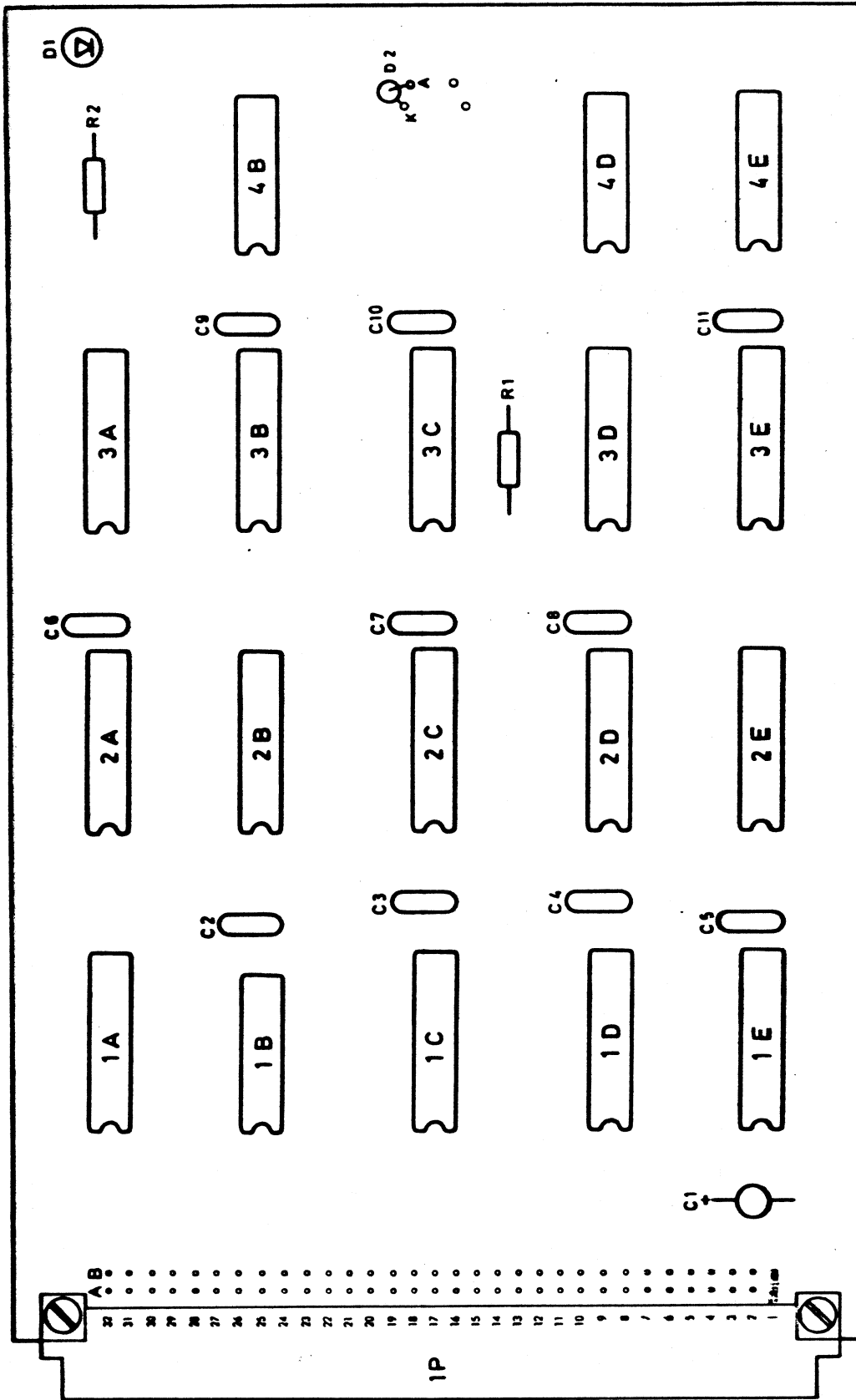
82-5037-00

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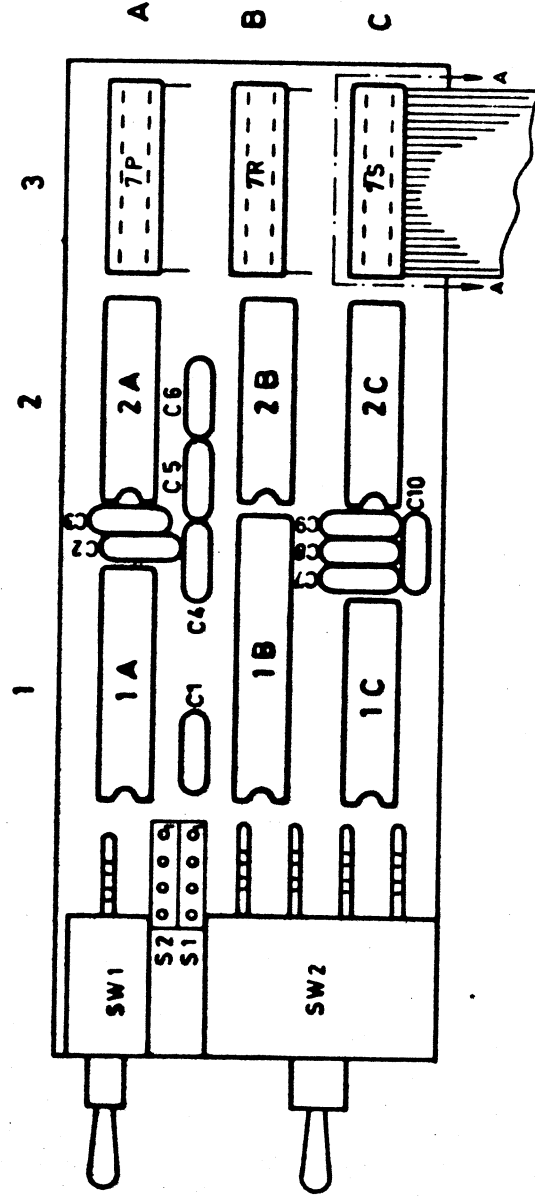
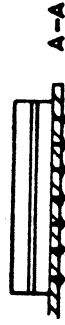
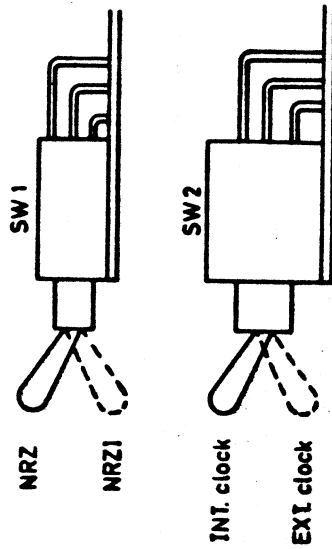


DATAINDUSTRIER AB				G-STYR		82-5038-00	
SWEDEN							
HOK		REV		DATUM		0012 05	
B		A		92 01 28		91 04 26	

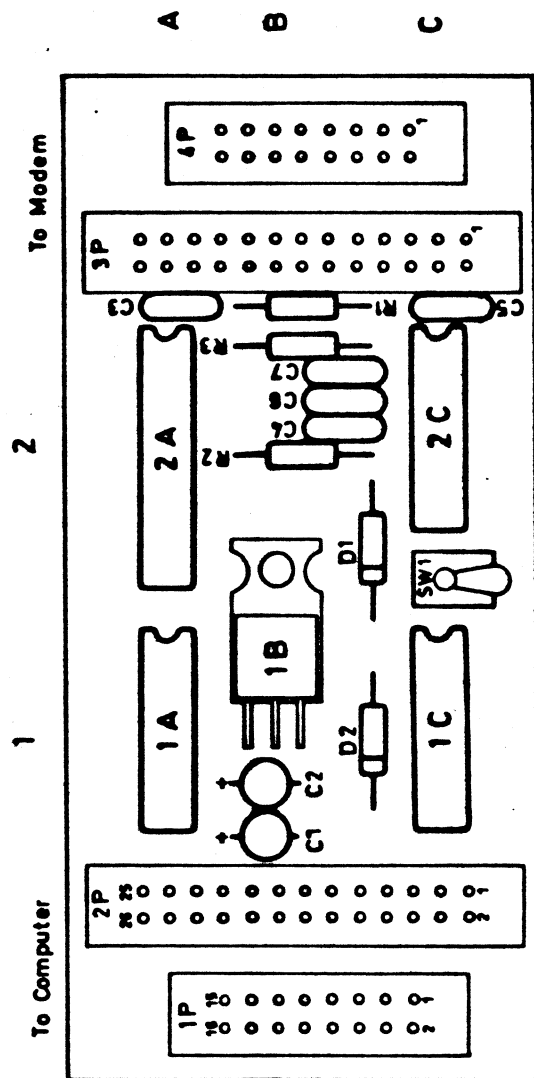
For detailed use of the
 system, see the manual.
 The system is not to be
 used for any other purpose
 than the one for which it
 was designed.



DATAINDUSTRIER AB SWEDEN				MEMORY MAPPER				82 - 5040 - 00			
REV		BIT		DATE		DATE		DATE		DATE	
01.12.28		02.12.28		01.01.09							

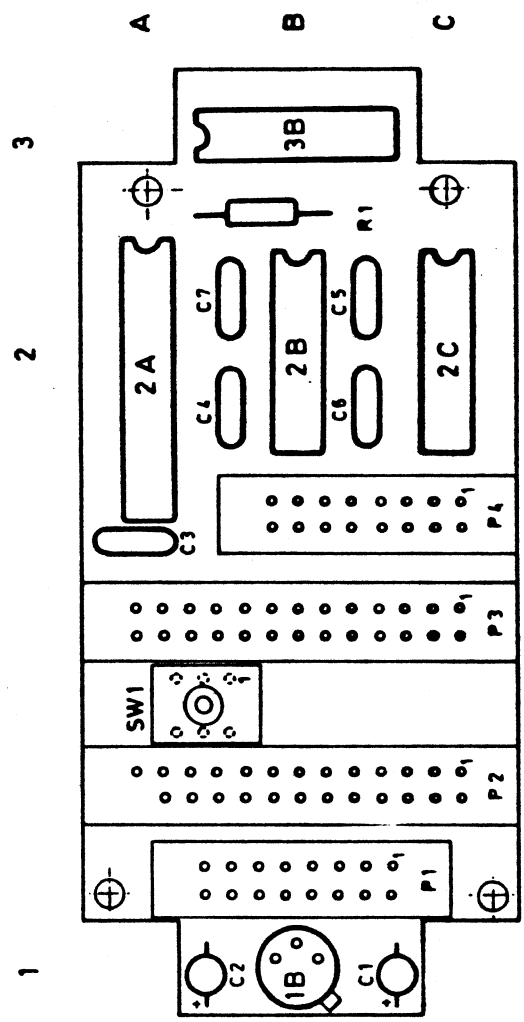


Cable 81-6189-00



1P - 4P Täckes vid våglödning / 1P - 4P protected for soldering

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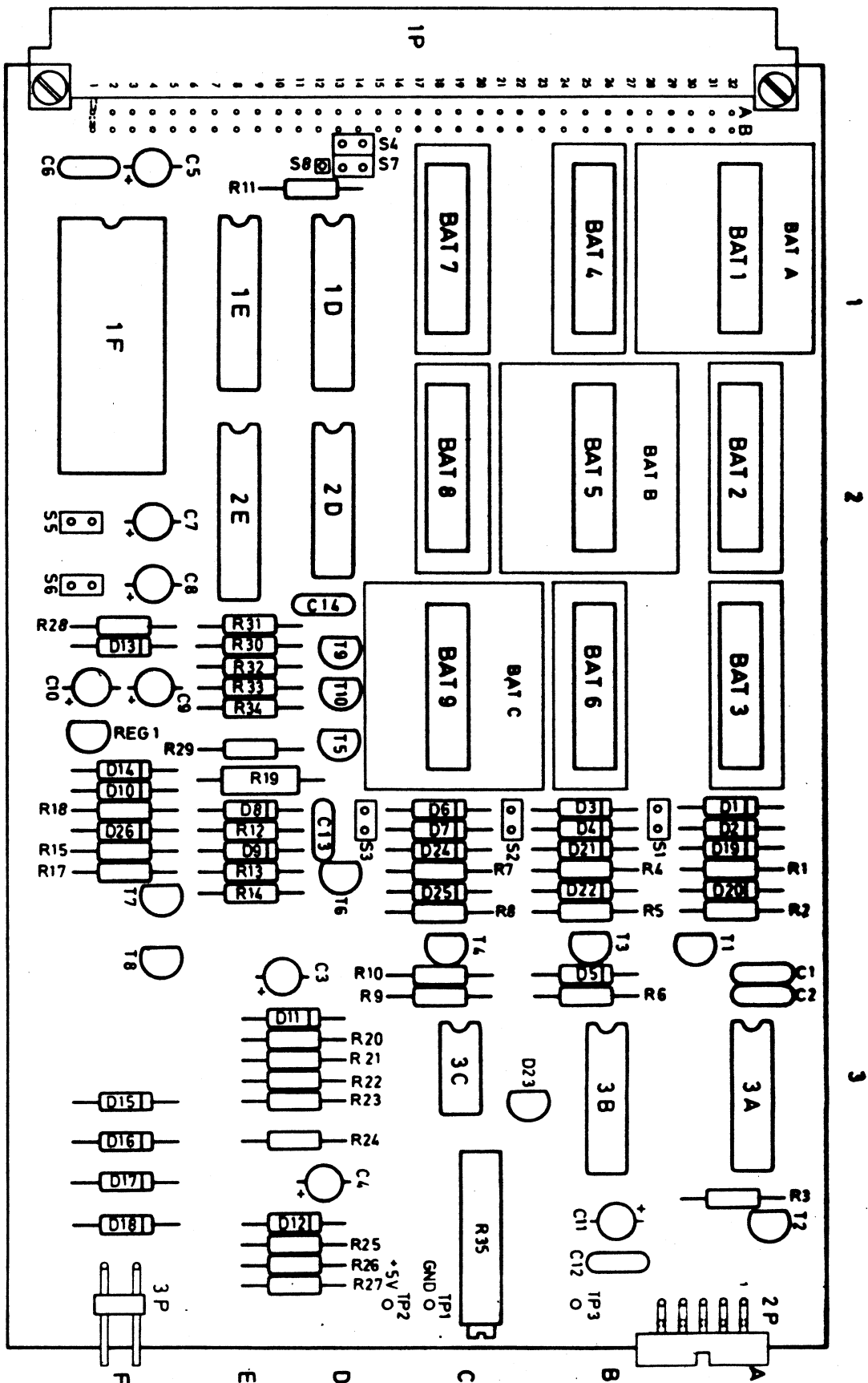




Technical drawing of a cross-section of a mechanical part. The drawing shows a central rectangular feature with a width of 12.5 and a height of 3. The part is symmetrical about a vertical centerline, indicated by a dashed line. The drawing is a black and white line drawing.

DATAINDU - ALER AB | **ELEX INTERFACE**

TELEX INTERFACE

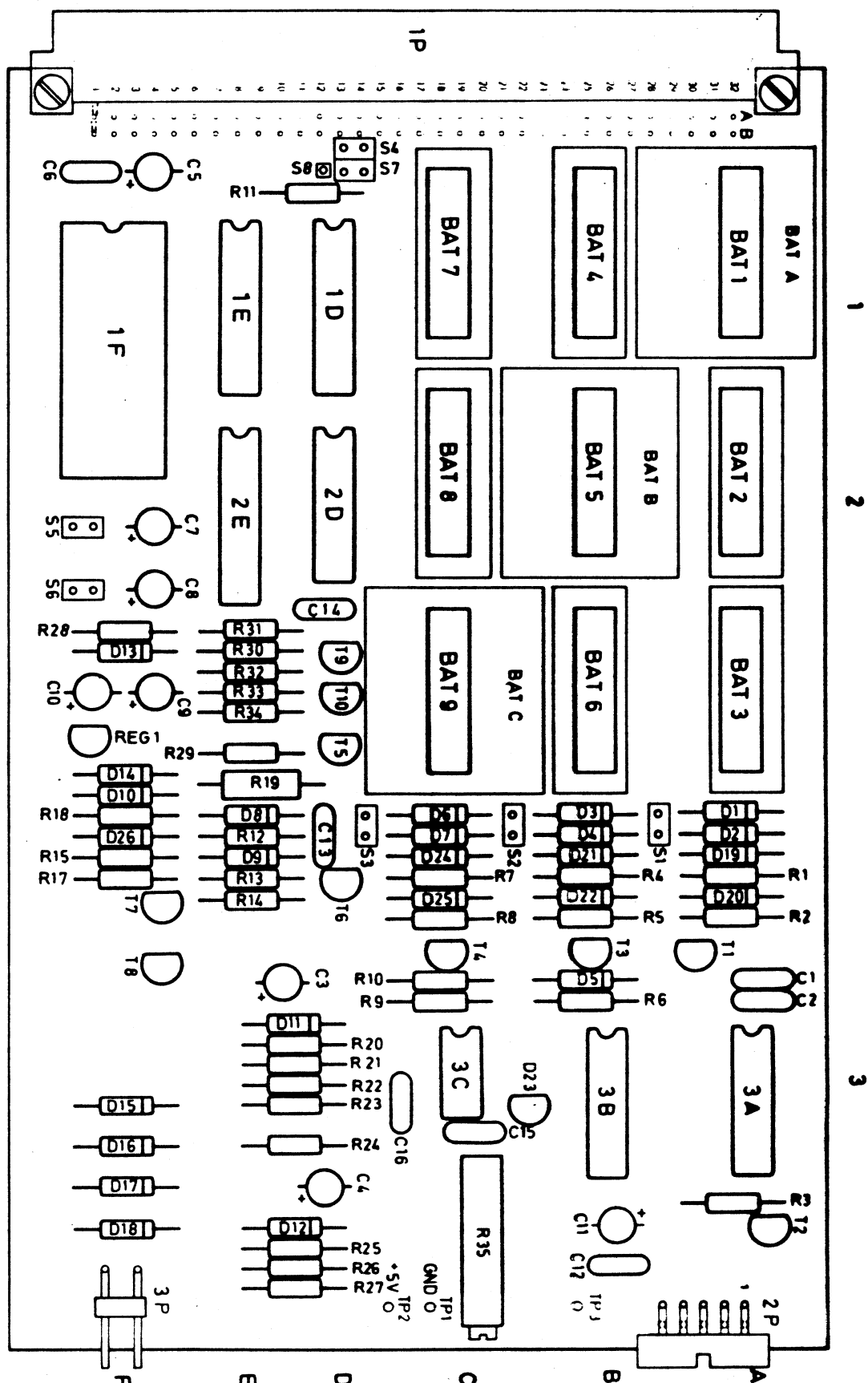


B	A	REV	DATE	CHK
8212 28	82 10 11	DATUM	82 09 01	

DATAINDUSTRIER AB
EDEN

POWER FAILURE

82-5059-10

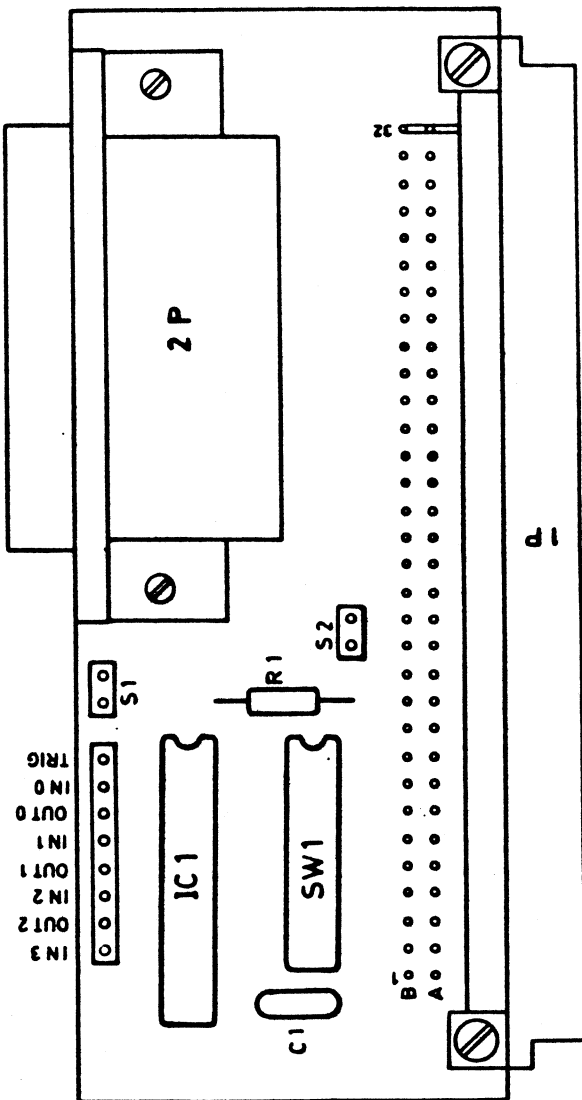


REV R11 HOK/OL
DATE 03/12/20

DATA INDUSTRIER AF
MIDEN

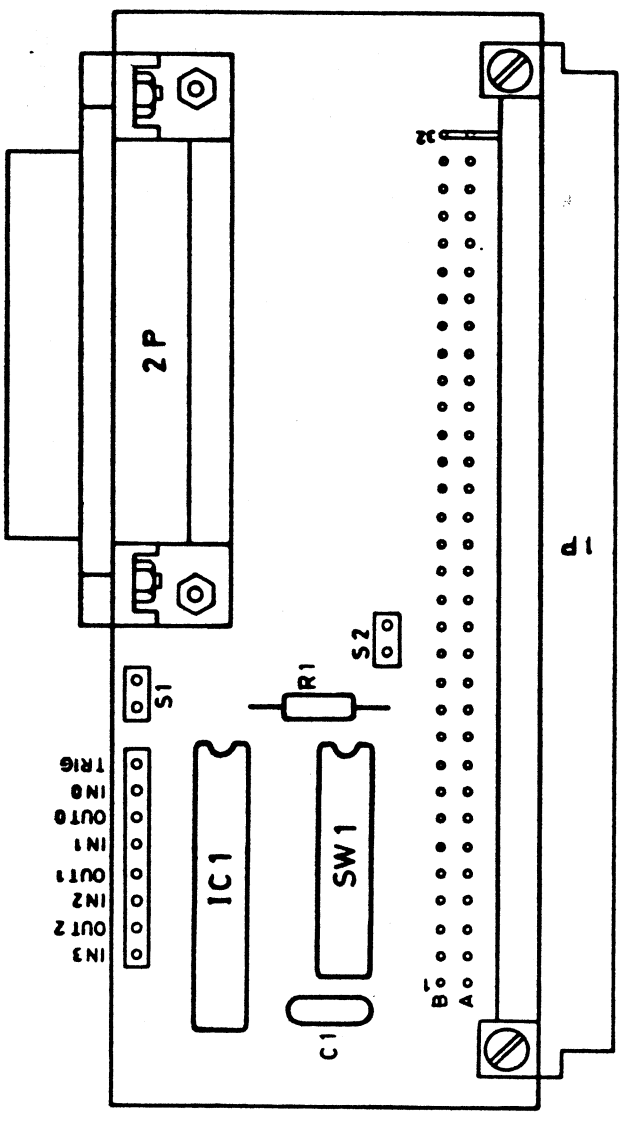
POWER FAILURE

82-5059-20

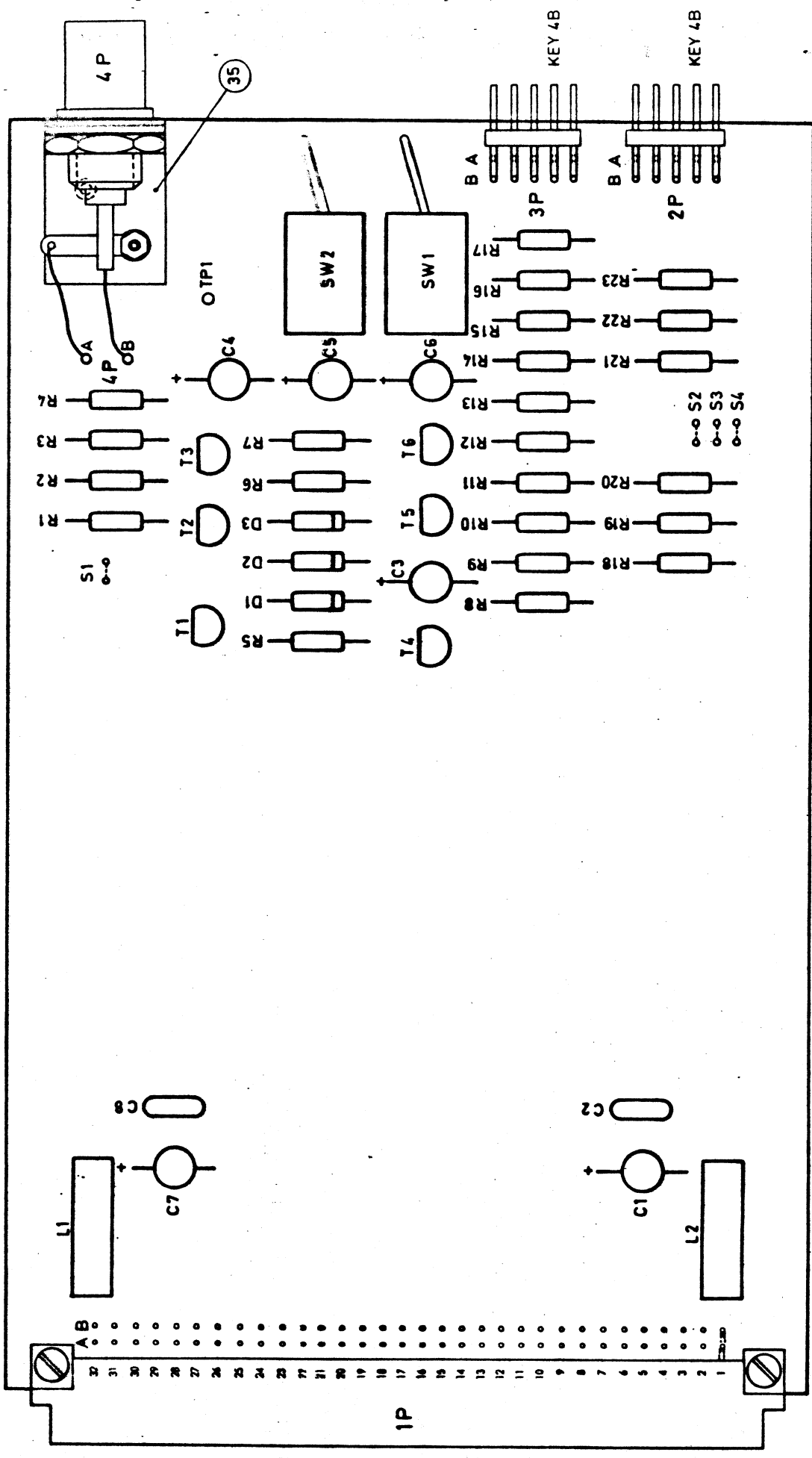


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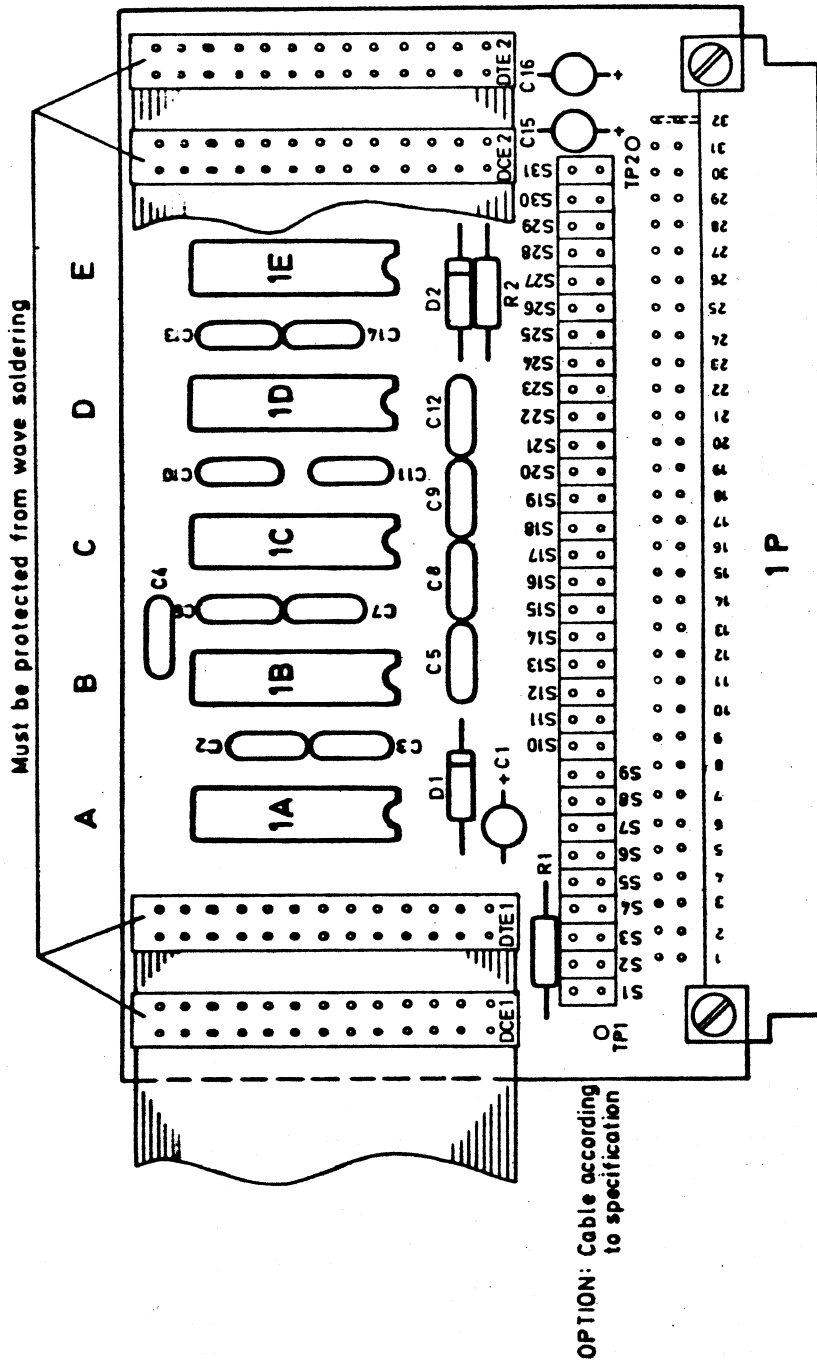
Oct 1973



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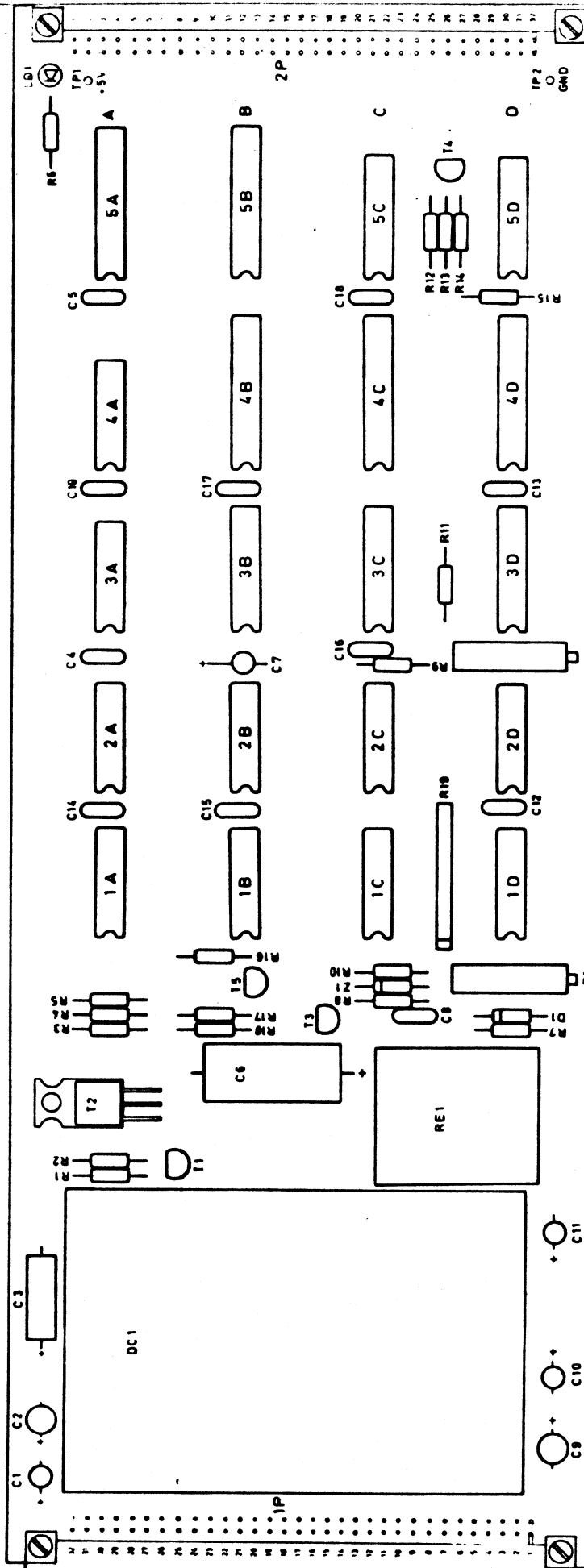


DATAINDUSTRIER AB				RGB - ENCODER		82-5067-00	
SWEDEN							
B		A		REV		HGM	
83.02.08		82.02.17		DATUM		8201.11	



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PROM PROGRAMMER

DATA INDUSTRIES AB

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REV. 01/74

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82-5071-20

Card type	Select	Baud rate	Strops																	
			S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	S13	S14	S15	S16	S17	
A117	—	1M7	O	X	X	O	X	X	X	X	X	O	C	X	O	O	C	C	O	C
1004	1M0	1N0= 0/2 1N0= OUT2	C	O	See Select	O	See Select	See Select	See Select	X	X	O	C	C	C	O	O	C	O	O
"	"	"	O	C	1M1					"	"	"	"	"	"	"	"	"	"	"
"	1M1	1N1= 0/2 1N1= OUT2	See Select	See Select	C	O	See Select	See Select	See Select	"	"	"	"	"	"	"	"	"	"	"
"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
"	1M1	1N1= 0/2 1N1= OUT2	See Select	See Select	C	O	See Select	See Select	See Select	"	"	"	"	"	"	"	"	"	"	"
"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
"	1M1	1N1= 0/2 1N1= OUT2	See Select	See Select	C	O	See Select	See Select	See Select	"	"	"	"	"	"	"	"	"	"	"
"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
"	1M1	1N1= 0/2 1N1= OUT2	See Select	See Select	C	O	See Select	See Select	See Select	"	"	"	"	"	"	"	"	"	"	"
"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
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"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
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"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
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"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
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"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
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"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
"	1M1	1N1= 0/2 1N1= OUT2	See Select	See Select	C	O	See Select	See Select	See Select	"	"	"	"	"	"	"	"	"	"	"
"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
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"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
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"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
"	1M1	1N1= 0/2 1N1= OUT2	See Select	See Select	C	O	See Select	See Select	See Select	"	"	"	"	"	"	"	"	"	"	"
"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
"	1M1	1N1= 0/2 1N1= OUT2	See Select	See Select	C	O	See Select	See Select	See Select	"	"	"	"	"	"	"	"	"	"	"
"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
"	1M1	1N1= 0/2 1N1= OUT2	See Select	See Select	C	O	See Select	See Select	See Select	"	"	"	"	"	"	"	"	"	"	"
"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
"	1M1	1N1= 0/2 1N1= OUT2	See Select	See Select	C	O	See Select	See Select	See Select	"	"	"	"	"	"	"	"	"	"	"
"	"	"	1M0		O	C				"	"	"	"	"	"	"	"	"	"	"
"	1M1	1N1= 0/2 1N1= OUT2	See Select	See Select	C	O	See Select	See Select	See Select	"	"									

Strapfields:					
LOOP TYPE	AR	AX	BR	BX	
	1	2	3	4	5
ACTIVE	C	O	C	O	C
PASSIVE	O	C	O	C	O

AR =	RCVR	CH A
AX =	XATR	CH A
BR =	RCVR	CH B
BX =	XATR	CH B

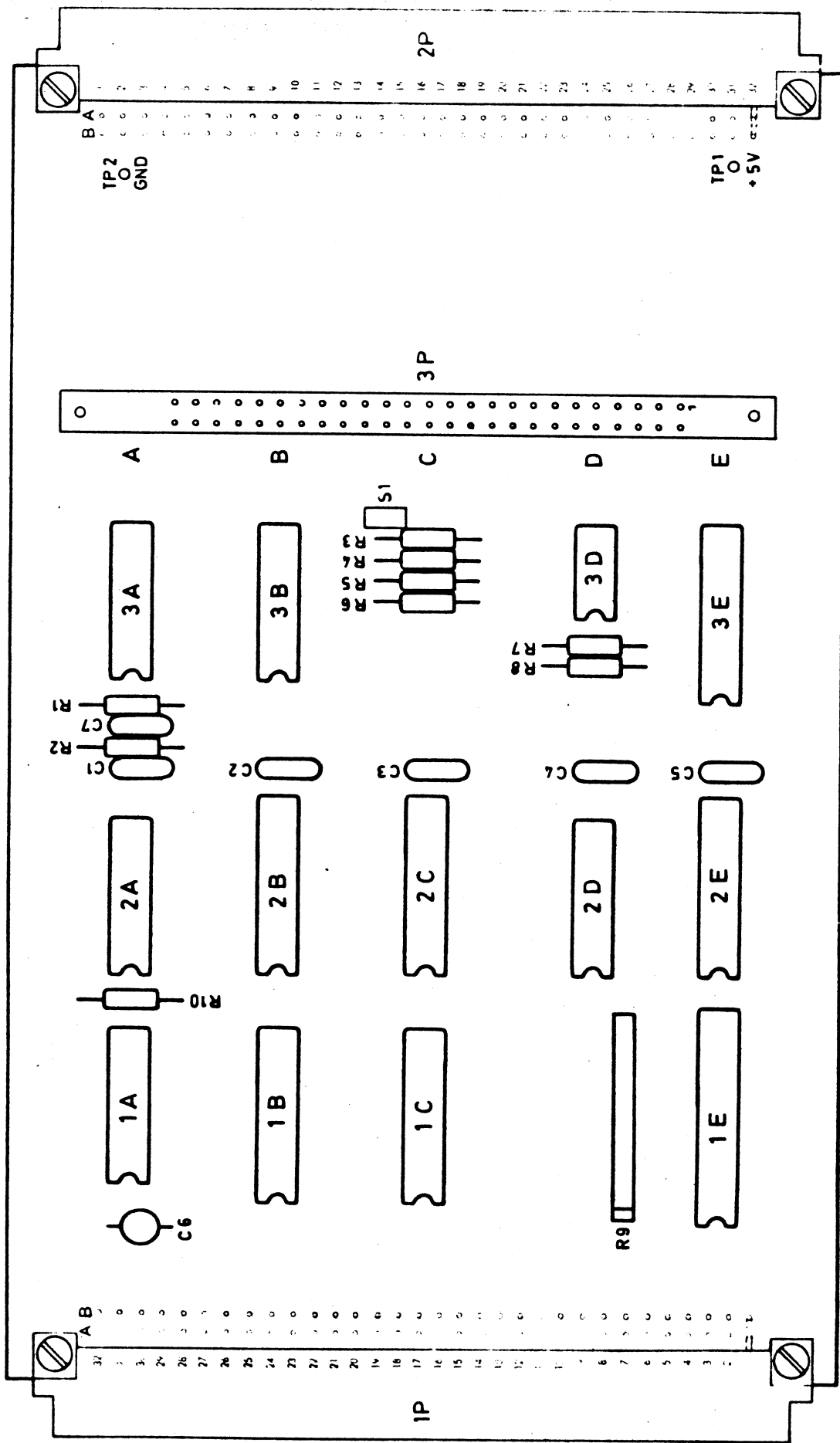
X = don't care
O = open
C = closed

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1 2)

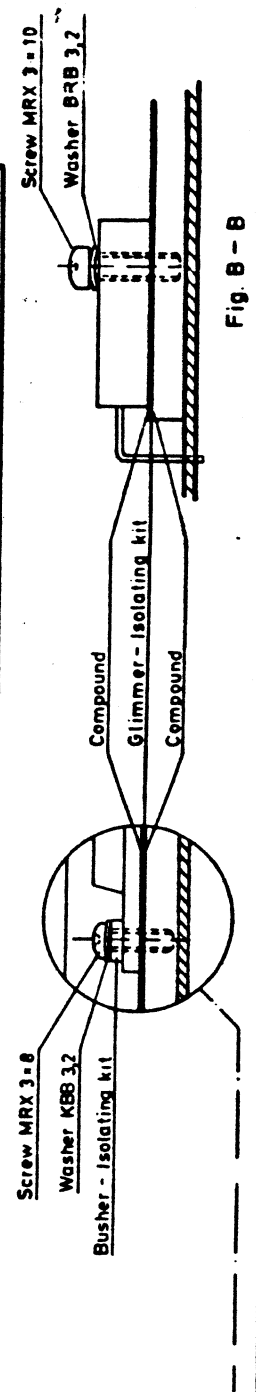
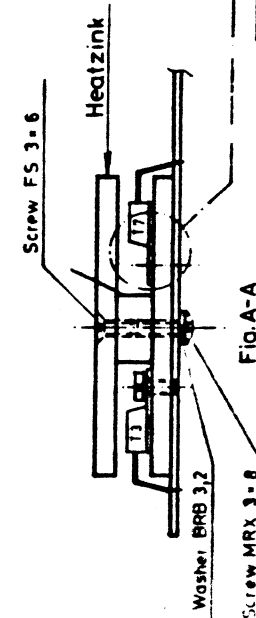
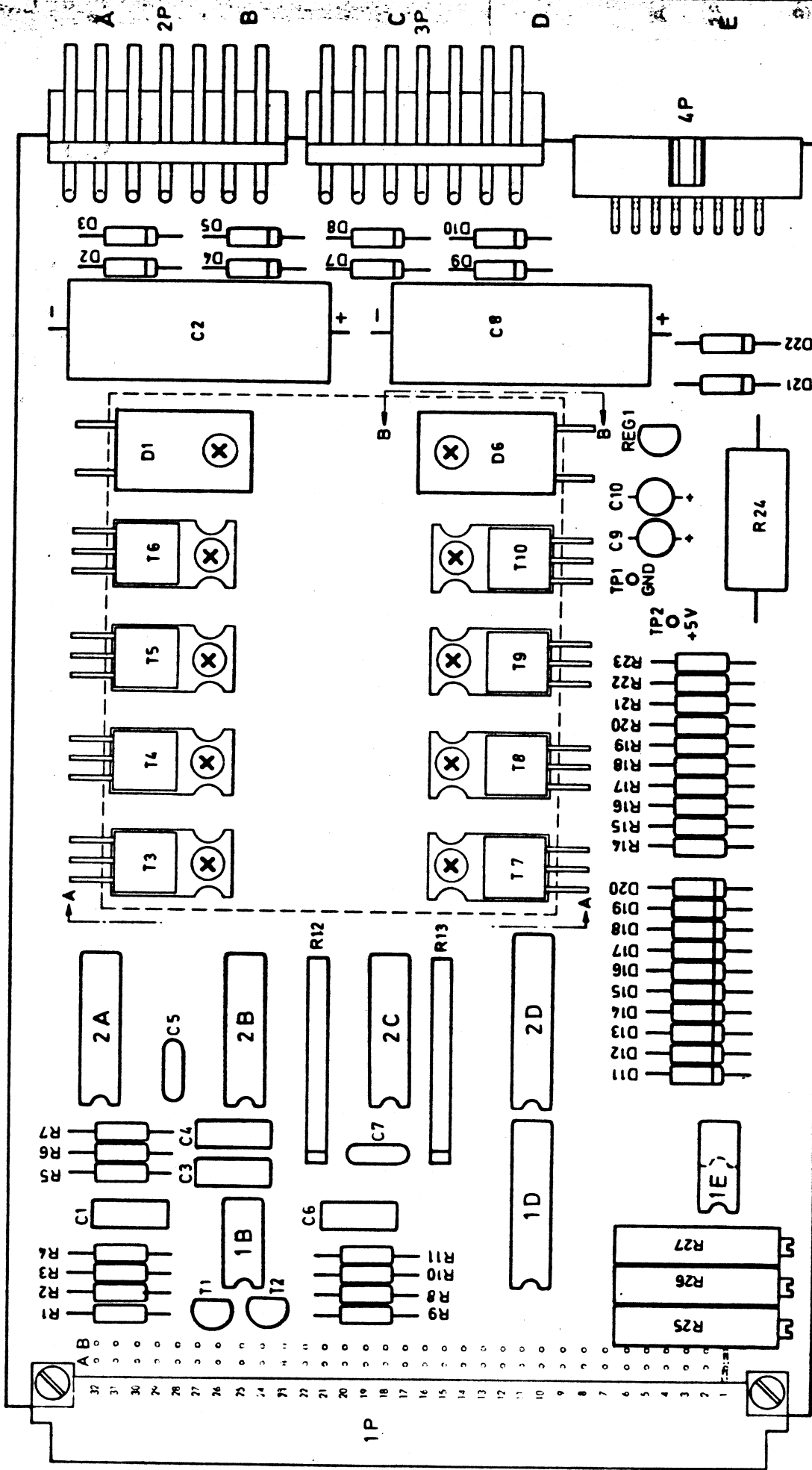


Fig. B - B

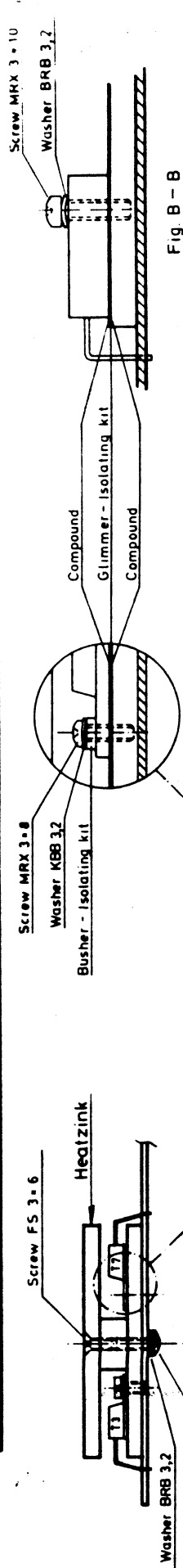
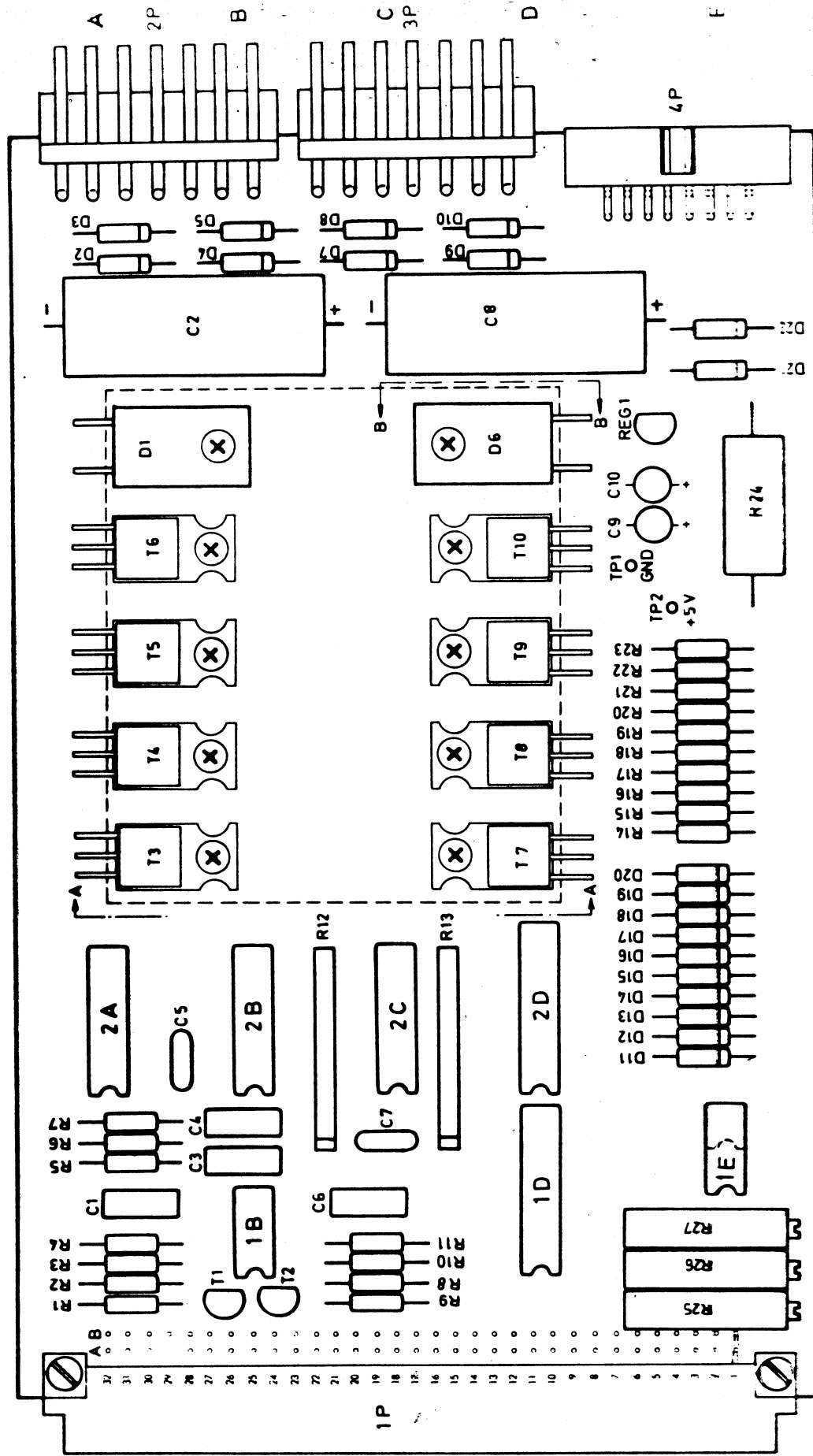
Fig. A-A

				DATAINDUSTRIER AB		STEP MOTOR POWER 1 A		82-5085-00	
				SWEDEN					
		B	A	REV	RIT	HGK			
		83.02.15	82.10.11	DATUM		82.09.06			

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1

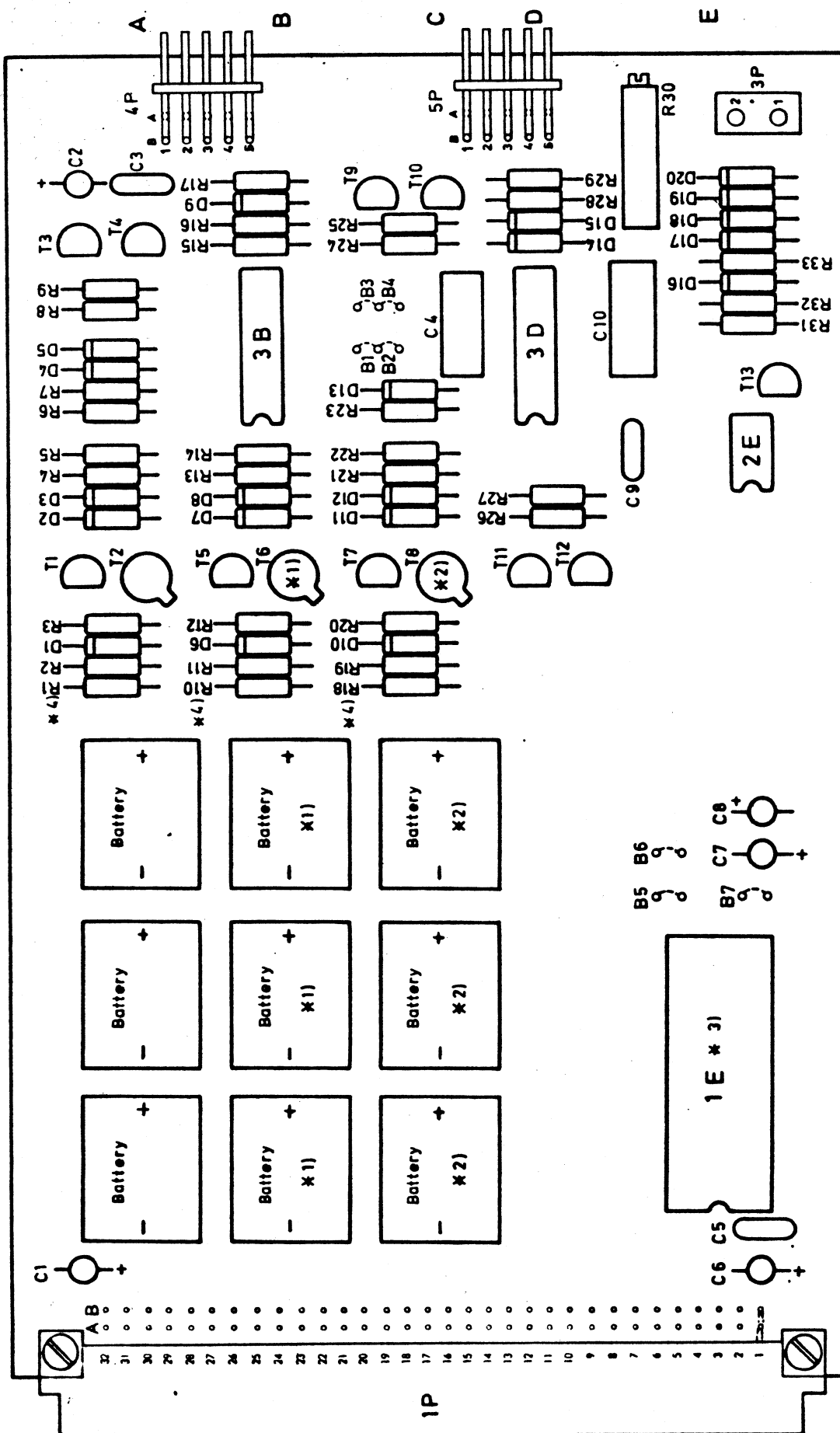
2



DATAINDUSTRIER AB				STEP POW 1A		82-5085-10	
REV	MIT	HGK	DATUM	WEDEN			
			83.11.17				

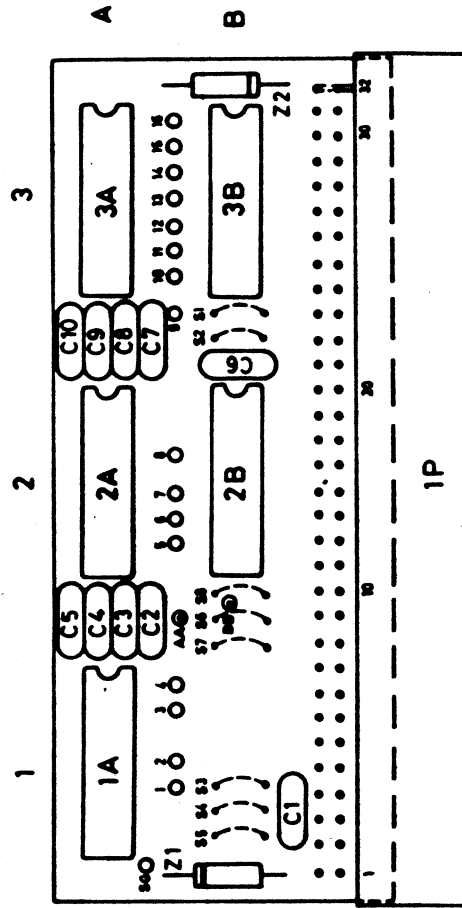
Fig. A-A

1 2 3



- * Option 1
- * Option 2
- * Option 3
- * Option 4 not mounted

DATAINDUSTRIER AB			POWER FAILURE 8 k RAM		82-5097-00	
SWEDEN						
REV		RIT	H&K			
A						
B						
811116		801025	DATUM		80.0318	

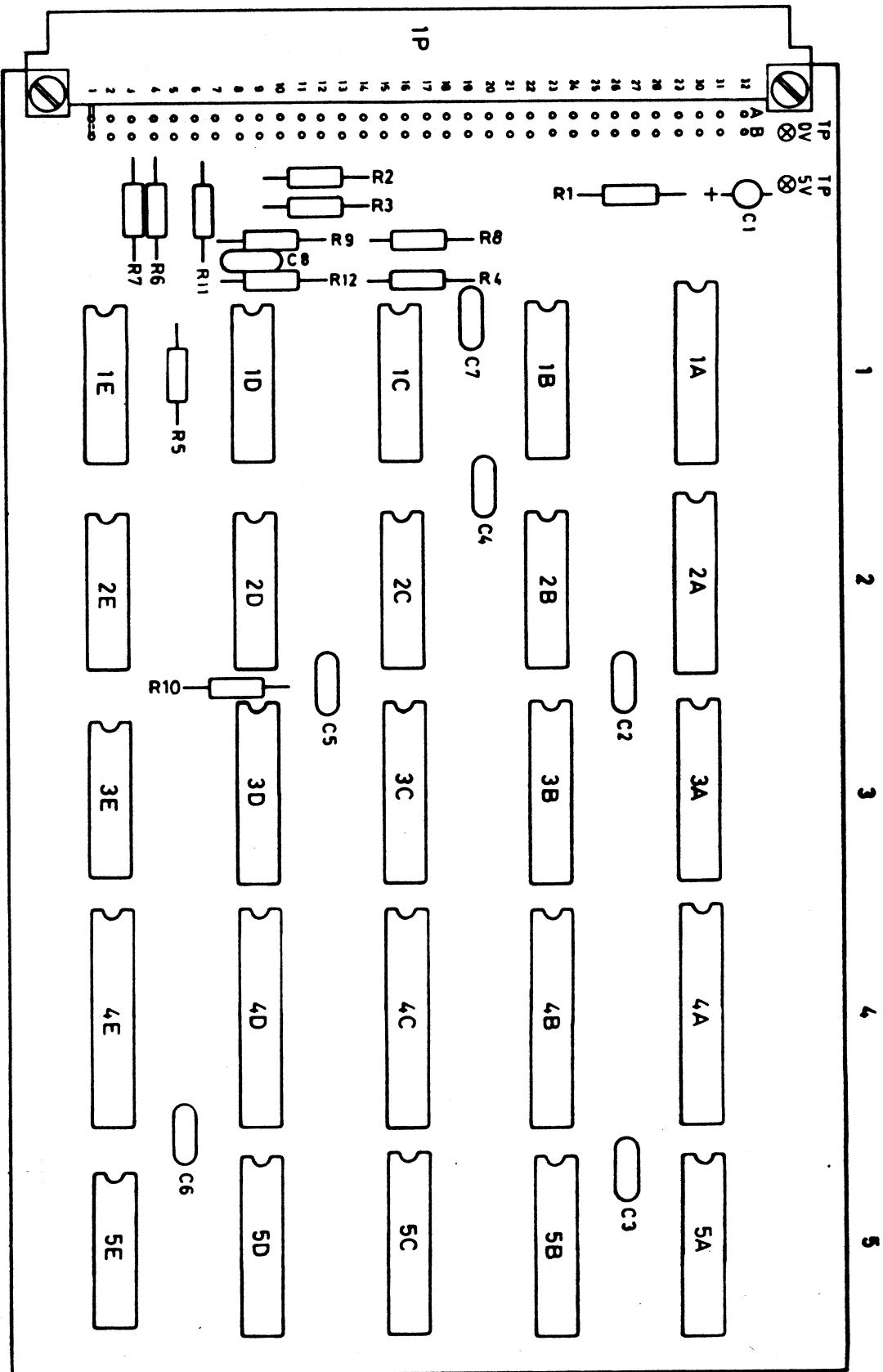


Point 1-16, S1-S8, AA, BB masked before machine soldering

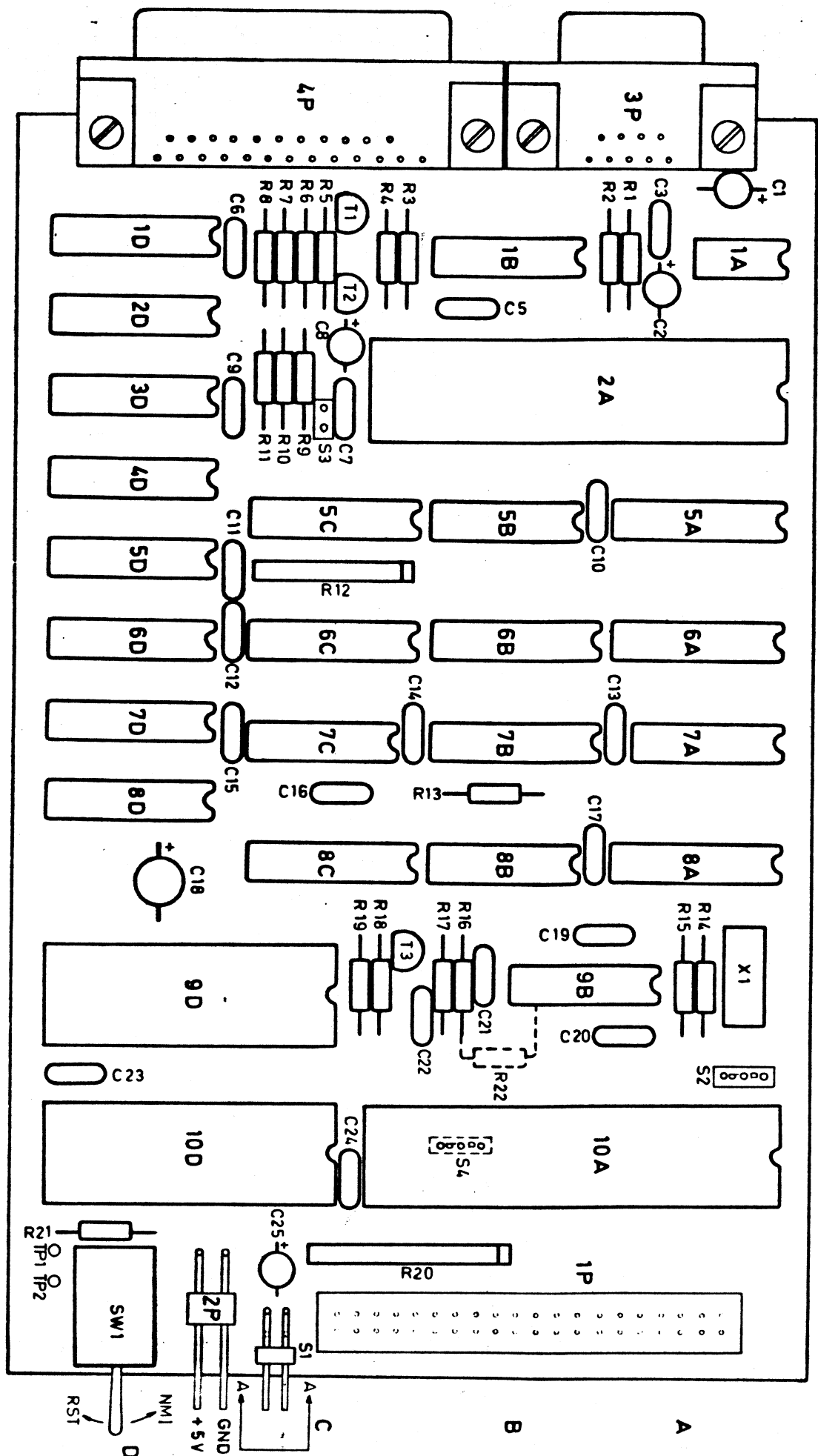


- | | |
|----|-------|
| 1 | = TD |
| 2 | RST |
| 3 | DTR |
| 4 | DCD |
| 5 | CTS |
| 6 | IN3 |
| 7 | IN2 |
| 8 | IN0 |
| 9 | RDY |
| 10 | C/TI |
| 11 | IN1 |
| 12 | SIB* |
| 13 | C/T0 |
| 14 | I/O 7 |
| 15 | " 6 |
| 16 | " 5 |
| 17 | " 4 |
| 18 | " 3 |
| 19 | " 2 |
| 20 | " 1 |
| 21 | I/O 0 |
| 22 | R1 |
| 23 | DSR |
| 24 | RD |

			A	R.F.	DATAND RIER AB SWEDEN	IO - ADAPTER	82-5099-00
		6102 19	DATE	80 01 25			



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- S1: b-external clock operation
- c-internal clock operation
- S2: a-2732 A } PROM
- b-2764 }
- S3: Watch dog enable/disable

1 2 3 4 5 6 7 8 9 10

