

M310 DELAY LINE

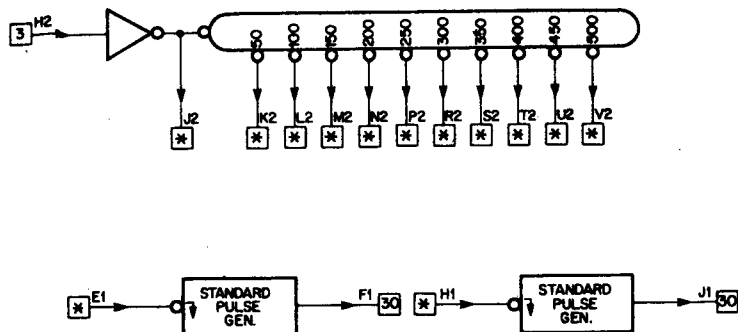
TIME
RELATED

M SERIES

Length: Standard
Height: Single
Width: Single

Price:

\$58



*-SEE TEXT

Volts +5 GND	Power mA (max.) 89	Pins A2 C2, T1
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The M310 consists of a tapped delay line with associated circuitry and two pulse amplifiers. The total delay is 500 nanoseconds with taps available at 50 nanosecond intervals.

APPLICATIONS

- Timing pulse trains
- Pulse spacing

FUNCTIONS

The time delay is increased when the amplifier is connected to the delay line taps in ascending order as follows: J2, K2, L2, M2, N2, P2, R2, S2, T2, U2, and V2. The tap J2 yields the minimum delay and the tap V2 yields the maximum delay.

The pulse amplifiers are intended to be used to standardize the outputs of the delay line. The output of the pulse amplifier is a positive pulse whose duration is typically 50 to 200 nanoseconds. These amplifiers are not intended to be driven by TTL IC logic.

M360 VARIABLE DELAY

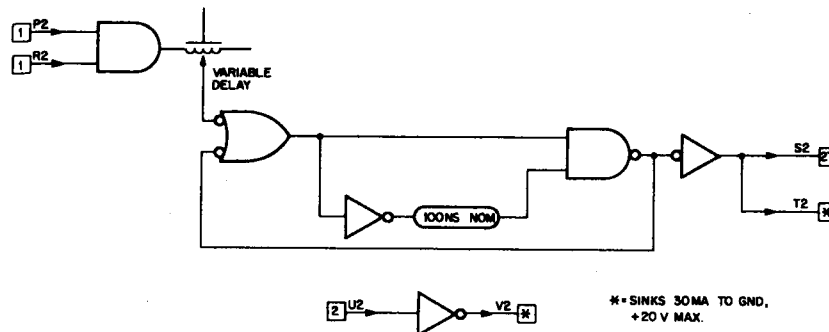
TIME
RELATED

M SERIES

Length: Standard
Height: Single
Width: Single

Price:

\$68



*-SINKS 30MA TO GND,
+20 V MAX.

Volts +5 GND	Power mA (max.) 50	Pins A2 C2, T1
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The M360 contains an adjustable delay line with a standardizing amplifier. The delay is adjustable between the limits of 50 ns to 300 ns by means of a slotted screw which is accessible from the handle end of the module.

FUNCTIONS

The output consists of a positive pulse whose width is nominally 100 nanoseconds and the leading (positive going voltage) edge of which is delayed with respect to the leading (positive going voltage) edge of the input by a length of time determined by the setting of the delay line adjustment.

Pins T and V are outputs consisting of open collector NPN transistors that can sink 30 milliamperes to ground.

Precautions: Voltage applied to pins T and V must not exceed +20 volts.

SPECIFICATIONS

The resolution of the delay adjustment is approximately one nanosecond.